Name of Faculty	Dr.B. KRISHNA CHAITANYA		
Designation	Assistant Professor		
Nature of Job/Appointment	Contract		
Date of Joining	28 - 01– 2021		
E-mail	krishnachaitanyab_eee@cbit.ac.in		
Education Qualifications	**Name of the Degree**		**Class**
Ph. D	Doctor of Philosophy		Awarded
PG	M. Tech. (Power Systems & Automation)		First Class
UG	B.TECH (EEE)		First Class with Distinction
Work Experience			
Teaching	1 Year		
Research	4 Years 6 Months		
Industry	--		
Others	--		
Area of Specialization	Power System Protection, Micro-grid,Signal processing and Pattern Recognition Application to Power System		
Professional Memberships	-		
Responsibilities held at Institution Level	-		
Responsibilities held at Department Level	-		
Research Guidance	-		
Awards Received	-		
Courses Handled at Under Graduate / Post Graduate Level.	Basic Electrical Engineering, Power Systems Operation and Control, Switchgear and Protection, Basic Electrical Engineering Lab, Electrical Machines-I Lab, Control Systems Lab		
No. of Papers Published	National Journals – 00	International Journals – 09	
	National Conference – 00	International Conference – 03	
Projects Carried out	-		
Patents	-		
Technology Transfer	-		
Invited Speaker	-		
No. of Books/ Chapter Published with details	1. Published one book chapter titled "A comprehensive review of islanding detection methods", in book titled"Uncertainties in Modern Power Systems". Publication: Academic press (Elsevier), 2020, pp. 211-256, ISBN: 978-0-12-820491-7.		
Details of Short-Term Training Programs/Faculty Development Programs/Seminars/Workshops/Other Trainings (**Attended and/or Organized**).	**WS/ Seminars/ Conferences/ STTPS/ FDPs Attended** 1. Workshop on "Research Challenges in Electrical Power and Energy", during 10-12May, 2021, Organised by the Department of EEE, Sri VenkateswaraEngineering College,Tirupati. 2. Short term training program on "Recent Trends in Power Electronics, Power System and Renewable Energy System", during 17-21 February, 2020, Organised by the Department of EE, National Institute of Technology Raipur. 3. Workshop on "Artificial Intelligence", during 22-26 September, 2019, Organised by AICTE Training And Learning (ATAL) Academy conducted at NIT Raipur. 4. Global Initiative of Academic Networks (GIAN) course on "Recent trends in Protection of Microgrids with high DER penetration: Issues, Challenges and Mitigation" during 12-16 February, 2018, Organised at Delhi Technological University, Delhi. 5. Training course on "Hands-On Protection Relay School" during 5-10 February, 2018, Organised by the Power systems division, Central power research institute (CPRI) Bangalore. 6. Workshop on "Power Quality challenges with Renewable Energy integration in Smart Grid" during 23-24 February, 2017, Organised at CPRI Bangalore.		

Details of Journal Publications/ Conferences (**National and International**)

International Journals from the year 2017

1. B.K. Chaitanya, AnamikaYadav, Mohammad Pazoki, "Reliable Islanding Detection Scheme for Distributed Generation based on Pattern-Recognition", *IEEE Transactions on Industrial Informatics*. doi: 10.1109/TII.2020.3029675
2. B.K. Chaitanya, AnamikaYadav, and Mohammad Pazoki, "An Advanced Signal Decomposition Technique for Islanding Detection in DG System", *IEEE Systems Journal*. doi: 10.1109/JSYST.2020.3017157.
3. B.K. Chaitanya, AnamikaYadav, and Mohammad Pazoki, "An Intelligent Detection of High-Impedance Faults for Distribution Lines Integrated With Distributed Generators," *IEEE Systems Journal*, vol. 14, no. 1, pp. 870-879, March 2020.
4. B.K. Chaitanya, AnamikaYadav, "An Intelligent Faulty Line Identification Scheme for Micro-grids", *Iranian Journal of Science and Technology, Transactions of Electrical Engineering* (Springer), Volume 44, Pages 537-549, 2020.
5. B.K. Chaitanya, AnamikaYadav, and Mohammad Pazoki, "Wide area monitoring and protection of microgrid with DGs using modular artificial neural networks", *Neural Computing & Applications* (Springer) 32, 2125–2139 (2020).
6. B.K. Chaitanya, AnamikaYadav, and Mohammad Pazoki, "An improved differential protection scheme for micro-grid using time-frequency transform", *Electrical Power & Energy Systems* (Elsevier), Volume 111, Pages 132-143, ISSN 0142-0615, 2019.
7. B.K. Chaitanya, and AnamikaYadav, "Decision tree aided travelling wave based fault section identification and location scheme for multi-terminal transmission lines", *Measurement* (Elsevier), Volume 135, Pages 312-322, ISSN 0263-2241, 2019.
8. B.K. Chaitanya, and AnamikaYadav, "An intelligent fault detection and classification scheme for distribution lines integrated with distributed generators", *Computers & Electrical Engineering* (Elsevier), Volume 69, Pages 28-40, ISSN 0045-7906, 2018.
9. B. K. Chaitanya, A.K. Soni and AnamikaYadav, "Communication assisted fuzzy based adaptive protective relaying scheme for micro-grid", *Journal of Power of Technologies* 2018, 98(1), pp. 57-69.

International /National Conferences from the year 2017

1. T.P. Bhargav, B. K. Chaitanya and A.Yadav, "Bus-Bar fault detection and classification using Fast S-transform and Artificial Neural Networks", 1st International Conference on Power Electronics and Energy (ICPEE), KIIT University, India, 2021, pp. 1-6.
2. B. K. Chaitanya, AnamikaYadav, and Mohammad Pazoki, "High Impedance Fault Detection Scheme for Active Distribution Network Using Empirical Wavelet Transform and Support Vector Machine", Published in IPAPS-2021, Shiraz University, Iran.
3. B. K. Chaitanya and A.Yadav, "Hilbert–huang transform based islanding detection scheme for Distributed generation", PIICON, NIT Kurukshetra, India, 2018, pp. 1-5.