


Name of Faculty	B. Poonguzharselvi	
Designation	Assistant Professor	
Nature of Job/Appointment	Regular	
Date of Joining	24 - 06 - 2019	
E-mail	poonguzharselvi_cse@cbit.ac.in	
Education Qualifications	Name of the Degree	Class
Ph. D	--	--
PG	M.E. (CSE)	Distinction
UG	B. E. (CSE)	Distinction
Work Experience		
Teaching	08 Years	
Research	--	
Industry	--	
Others	--	
Area of Specialization	Data Mining, Data Analytics	
Professional Memberships	Member, ICSES Number: 8715	
Responsibilities held at Institution Level	<ol style="list-style-type: none"> 1. Member, Sports and Games Committee Shruthi - 2021. 2. Member, Literary Arrangements Committee Shruthi-2020. 3. Member, Literary Arrangements Committee Shruthi-2019. 	
Responsibilities held at Department Level	<ol style="list-style-type: none"> 1. Member, UG-NBA (Criteria– IV) 2020. 2. Member, PG-NBA (Criteria – II) 2019. 3. Faculty Coordinator, Data Addicts in Head Start-2K19. 4. Attendance In-charge for C1-IV SEMESTER-2019. 5. Staff Coordinator, Treasure Hunt in Head Start-2K18. 6. Staff Coordinator, Treasure Hunt in Head Start-2K17. 	
Research Guidance	--	
Awards Received	<ol style="list-style-type: none"> 1. Qualified in UGC- NET Dec-2018 2. Secured 35th UNIVERSITY RANK in Anna University, Tamilnadu. 3. Received BEST STUDENT IN ACADEMIC PERFORMANCE award from Kongu Engineering College, Tamilnadu. 	
Courses Handled at Under Graduate / Post Graduate Level.	Distributed Computing, Professional Ethics and Human Values, Programming and Problem Solving, Object Oriented Programming, Mobile Application Development, Software Engineering, Bridge Course, Data Structures	
No. of Papers Published	National Journals – Nil International Journals – 11	
	National Conference – Nil International Conference – 02	
Projects Carried out	--	
Patents	--	
Technology Transfer	--	
Invited Speaker	--	
No. of Books/Chapter Published with details	<ol style="list-style-type: none"> 1. S.Karunakaran and B.Poonguzharselvi (2021), "Exploration of Power Delay Product on Feedback Based DET Flip Flop Utilizing Dual Sleep and Dual Slack Approach", Research Trends in Applied Research, Published By: Weser Books, ISBN: 978-3-96492-236-6, vol. 7, pp. 101-111. 2. S.Karunakaran and B.Poonguzharselvi (2021), "Analysis of Low Power VLSI Design of Static Recovery Full Adder Cells", Research Trends in Applied Research, Published By: Weser Books, ISBN: 978-3-96492-218-2, Vol. 8, pp. 17-26. 	

Details of Short-Term Training Programs/Faculty Development Programs/Seminars/Workshops/Other Trainings (**Attended and/or Organized**).

1. Attended an One week Faculty Development Program on "NBA Accreditation Process & SAR File Preparation", under AICTE Margadarshan Scheme conducted by Dept. of Information Technology, Anurag University during 08-11-2021 to 12-11-2021.
2. Participated in AICTE Sponsored one week online Short-Term Training Program on "Practical Exposure on Machine Learning and Data Science" organized by Department of ECE, GPREC(A), Kurnool during 13-09-2021 to 18-09-2021.
3. Attended the One Week Short Term Training Program (AICTE Sponsored) on "Data Science and Big Data Analytics", conducted by Indo Global College of Engineering, Abhipur, New Chandigarh during 30-08-2021 to 04-09-2021.
4. Completed One Week STTP on "Emerging Trends In VLSI Technology" organized by Malla Reddy Engineering College for Women during 14-07-2021 to 19-07-2021.
5. Participated in one week Online STTP on AI-MIMO, Organized by Department of Electronics & Communication Engineering, Gokaraju Rangaraju Institute of Engineering and Technology during 26-04-2021 to 01-05-2021.
6. Participated in the AICTE Sponsored One Week Online Short Term Training Programme (STTP) On "SciLab: An Effective Open Source Tool for Computer Vision and Machine Learning" conducted by the Department of ECE, Vidya Jyothi Institute of Technology (Autonomous) in association with AICTE during 14-12-2020 to 19-12-2020.
7. Participated in online AICTE Sponsored STTP on "Intelligent Edge Computing Techniques for Diverse Applications", organized by M. S. Ramaiah Institute of Technology during 14-12-2020 to 19-12-2020.
8. Completed AICTE Training And Learning (ATAL) Academy Online one week FDP on "Data Sciences" at Institute of Road and Transport Technology during 02-11-2020 to 06-11-2020.
9. Completed AICTE Training And Learning (ATAL) Academy Online one week FDP on "Augmented Reality (AR)/ Virtual Reality (VR)" at Chaitanya Bharathi Institute of Technology during 26-10-2020 to 30-10-2020.
10. Participated in AICTE Sponsored 6-Days Online Short-Term Training Program on "Business Analytics", Organized by MCKV Institute of Engineering, Howrah during 12-10-2020 to 17-10-2020.
11. Participated in the six days Short Term Training Programme on "High Performance Intelligent Real Time Embedded System Design and Internet of Things to accelerate automation system into autonomous system" Organized by Department of ECE, R.M.K. Engineering College, Tamilnadu during 01-09-2020 to 07-09-2020.
12. Participated in one week National Level Online FDP on "MATLAB and its applications in Engineering using Machine Learning & Deep Learning" organized by G Pulla Reddy Engineering College, kurnool in association with MathWorks & Capricot Technologies Pvt. Ltd. during 13-07-2020 to 17-07-2020.
13. Participated in an online Two Weeks FDP on NAAC ASSESSMENT & ACCREDITATION process Organised by IQAC, Shadan College of Engineering and Technology, Hyderabad, Telangana during 06-07-2020 to 17-07-2020.
14. Participated in Two Day online workshop on "DevOps" organized by Kakatiya Institute of Technology & Science, Warangal during 12-07-2020 to 13-07-2020.
15. Completed "Introduction to Big Data" an online non-credit course authorized by University of California San Diego and offered through Coursera on 02-07-2020.
16. Completed "Python Data Structures" an online non-

- credit course authorized by University of Michigan and offered through Coursera on 01-07-2020.
17. Attended a Leadership Talk with Shri Dipendra Manocha, (Motivational Speaker) by MHRD's Innovation Cell on 27-06-2020.
 18. Attended 6-Day Faculty Development Program on "Disruptive Technologies in Electronics & Communications" organized by Vidya Jyothi Institute of Technology (Autonomous) in collaboration with IETE Student Chapter during 22-06-2020 to 27-06-2020.
 19. Attended One Week International Level Faculty Development Programme on "Advanced Data Science and its Applications" organized by BVRIT HYDERABAD College of Engineering for Women during 22-06-2020 to 26-06-2020.
 20. Attended 3-Day Faculty Development Programme on "Information Security & Cryptography" organized by Lendi Institute of Engineering & Technology, Vizianagaram during 20-06-2020 to 22-06-2020.
 21. Attended a Leadership Talk with Mr. Shridhar Venkat, CEO (Akshay Patra Foundation) by MHRD's Innovation Cell on 19-06-2020.
 22. Completed "Introduction to Data Science in Python" an online non-credit course authorized by University of Michigan and offered through Coursera on 18-06-2020.
 23. Participated in 2-Day Online Web series on "Biomedical Imaging" organized by KMIT, Hyderabad 16-06-2020 to 17-06-2020.
 24. Participated in a Webinar on "CO-PO Attainment Computation and Outcomes Analysis" organized by InPods ED-tech on 13-6-2020.
 25. Attended a Leadership Talk with Shri R Subrahmanyam, IAS (Secretary Ministry of Social Justice & Empowerment) by MHRD's Innovation Cell on 13-06-2020.
 26. Attended One Week Faculty Development Programme on "PHP & MySQL" organized by Sridevi Women's Engineering College, Hyderabad in association with Spoken Tutorial IIT-Bombay during 08-06-2020 to 12-06-2020.
 27. Attended 2-Day Online Faculty Development Program on "Effective Technical Report Writing Using LaTeX" organized by MGIT, Hyderabad during 08-06-2020 to 09-06-2020.
 28. Completed an Awareness Program on "Coronavirus-(COVID-19)" at JNTUH College of Engineering Jagtial on 31-05-2020.
 29. Attended a Leadership Talk With Dr. Nilesh N Oak, Expert (Indian Civilization & History) by MHRD's Innovation Cell on 30-05-2020.
 30. Attended One Week Faculty Development Program on "Outcome Based Education and NBA Accreditation Process-(UG)" organized by Chaitanya Bharathi Institute of Technology, Hyderabad during 28-05-2020 to 01-06-2020.
 31. Attended a Leadership Talk on India's First Leadership Talk Series Session with Mr Mahesh Babu CEO Mahindra Electric Mobility Ltd. by MHRD's Innovation Cell on 23-05-2020.
 32. Attended 5-Day National Level Online Faculty Development Programme on "ARTIFICIAL INTELLIGENCE" organized by Chaitanya Bharathi Institute of Technology in collaboration with Brain-O-Vision India Pvt. Ltd. during 22-05-2020 to 26-05-2020.
 33. Attended 5-day Online Short Term Training Program on "MATLAB based Teaching-Learning in Mathematics, Science and Engineering" organized by Ramrao Adik Institute of Technology, Nerul, Navi Mumbai in collaboration with DesignTech Systems Pvt. Ltd., Mumbai during 18th to 22nd May 2020.
 34. Attended Leadership Talk With Dr. Pramod Chaudhari, Founder, Chairman, Praj Industries Limited and Dr. Abhay Jere, Chief Innovation Officer

- MHRD Innovation Cell by MHRD's Innovation Cell on 16-05-2020.
35. Attended workshop on "Artificial Intelligence/Machine Learning and Deep Learning" organized by DigitalTrack Solutions Pvt Ltd. on 15-05-2020.
 36. Completed "Programming for Everybody (Getting Started with Python)" an online non-credit course authorized by University of Michigan and offered through Coursera on 14-05-2020.
 37. Participated in the webinar on "INDIAN PATENT DRAFTING AND APPLYING" organized by PANIMALAR INSTITUTE OF TECHNOLOGY, Chennai on 13-05-2020.
 38. Participated in the webinar on "IMAGE PROCESSING USING PYTHON" hosted by Mount Zion College of Engineering and Technology Pudukkottai in association with PANTECH on 12-05-2020.
 39. Participated One Hour Live Webinar on "DIGITAL TRANSFORMATION" organized by Storm Overseas on 12-05-2020.
 40. Attended 5-Days online Faculty Development Program on "Cryptography and Network Security" organized by Panimalar Institute of Technology, Tamilnadu during 11-05-2020 to 15-05-2020.
 41. Attended the India First Leadership Talk webinar with Prof D. P. Singh, Chairman UGC Broadcasted by MHRD's Innovation Cell on 09-05-2020.
 42. Completed "Machine Learning With Big Data" an online non-credit course authorized by University of California San Diego and offered through Coursera on 08-05-2020
 43. Participated in the IIC Online Sessions conducted by Institution's Innovation Council (IIC) of MHRD's Innovation Cell, New Delhi to promote Innovation, IPR, Entrepreneurship, and Start-ups among HEIs from 28th April to 22nd May 2020 during COVID-19 nationwide lockdown.
 44. Completed 12 weeks NPTEL-AICTE Faculty development programme on "Internet of Things" organized by IIT Kharagpur and Sponsored by Ministry of HRD, Govt. of India during July to October 2019.
 45. Completed 12 weeks NPTEL-AICTE Faculty development programme on "Joy of Computing using Python" organized by IIT Madras and Sponsored by Ministry of HRD, Govt. of India during January to April 2019.
 46. Completed 12 weeks NPTEL-AICTE Faculty development programme on "Software Engineering" organized by IIT Madras and Sponsored by Ministry of HRD, Govt. of India during July to October 2018.
 47. A 2-day National Workshop on "Interface of Statistical Analysis Using R" by CBIT, Hyderabad during 09-02-2018 to 10-02-2018.
 48. A One Week Faculty Development Programme on "Transforming into Better Performers" organized by In-house Training Centre (ITC), CBIT Hyderabad during 27-11-2017 to 02-12-2017.
 49. A One Week National Level Faculty Development Programme on "Meeting the Challenges of Teachers in Modern Engineering Education" organized by In-house Training Centre(ITC), CBIT Hyderabad during 12-06-2017 to 17-06-2017.
 50. A 2-day International Workshop on "Big Data and Machine Learning" by CBIT, Hyderabad during 27-03-2017 to 28-03-2017.
 51. A 2-day National Workshop on "Privacy Preserving & ICT: Research Ideas" by CBIT, Hyderabad during 02-03-2017 to 03-03-2017.
 52. A One Week National Level Faculty Development Programme on "Emerging Trends, Issues and Challenges in Big Data & Cloud computing" by CBIT, Hyderabad during 20-02-2017 to 25-02-2017.

International Journal

1. B. Poonguzharselvi (2021), "Review on Placement Prediction Models", Journal of Interdisciplinary Cycle Research, ISSN: 0022-1945, Vol. XIII, Issue V, May 2021, Pages. 2259-2264.
2. B. Poonguzharselvi, Mohammad Mahaboob Ali Ashraf, Vadlamani V S S Subhash, S.Karunakaran (2021), "Prediction of Liver Disease Using Machine Learning Algorithm and Genetic Algorithm", Annals of R.S.C.B., ISSN:1583-6258, Vol. 25, Issue 4, April 2021, Pages. 2347 – 2357, *Scopus*.
3. S. Karunakaran, B. Poonguzharselvi, T. Logeswaran, S. Senthil kumar, S.SathishKumar(2021), "Power Efficient VLSI Architecture of 4X4 Modified Column Bypassing Multiplier", Annals of R.S.C.B., ISSN:1583-6258, Vol. 25, Issue 4, April 2021, Pages. 2116 – 2122, *Scopus*.
4. S Karunakaran, B Poonguzharselvi (2019), "Investigations on power dissipation of low power VLSI architectures for Voltage level shifters" , Journal of Advanced Research in Dynamical and Control Systems, Volume-11, Issue 05, pp. 2229-2234, *Scopus*.
5. S Karunakaran, B Poonguzharselvi (2019), "High Performance VLSI Architecture for Braun Multiplier", International Journal of Innovative Technology and Exploring Engineering (IJITEE), Volume 8, Issue 10, pp.362-365, *Scopus*.
6. S Karunakaran, Boddupally Harshitha, B Poonguzharselvi, K Jai Ganesh (2018), "Exploration of power delay product [PDP] on feedback based dual edge triggered flip flop utilizing dual sleep and dual slack approach", International Journal of Engineering & Technology, Vol. 7, Issue 4, pp. 3388-3391, *Scopus*.
7. Karunakaran S, Pandurangaiah Y, Joseph Antony Prathap, Poonguzharselvi B (2018), "Exploration of Power Delay Product of Various VLSI Multiplier Architectures", International Journal of Mechanical Engineering and Technology (IJMET), Vol. 9, Issue 1, pp. 53-59, *Scopus*.
8. S Karunakaran, B Poonguzharselvi and M Narayana (2018), "Analysis of Low Power VLSI Design of Adder Cells", Journal of Advanced Research in Dynamical and Control Systems, pp. 883-887, *Scopus*.
9. S Karunakaran, B Poonguzharselvi (2017), "Exploration on Power Delay Product of Basic Logic Gates for Various CMOS Logic Styles", International Journal of Engineering Studies, Volume 9, Issue 2, pp.111-120.
10. S Karunakaran, B Poonguzharselvi (2017), "VLSI Architecture of An 8-Bit Multiplier Using Vedic Mathematics in 180nm Technology", International Journal of Advances in Engineering & Technology, Volume 10, Issue 3, pp. 401-410.
11. B Poonguzharselvi, V Vetrivelvi (2012), "Trust Framework for Data Forwarding in Opportunistic Networks Using Mobile Traces", International Journal of Wireless and Mobile Networks (IJWMN), Volume 4, Issue 6.

International /National Conferences

1. B Poonguzharselvi, V Vetrivelvi (2013), "Survey on Routing Algorithms in Opportunistic Networks", IEEE International Conference on Computer Communication and Informatics (ICCCI -2013), *Scopus*.
2. B Poonguzharselvi, V Vetrivelvi(2012), "Data Forwarding in Opportunistic Network Using Mobile Traces", International Conference on Computer Science & Information Technology (CS & IT), pp. 425–430.