

Name of Faculty Dr. M.L.N.ACHARYULU

Designation Associate Professor

Nature of Job/Appointment Contact

Date of Joining 30-01- 2021

E-mail mlncharyulu\_ece@cbit.ac.in



Education Qualifications	Name of the Degree	Class
Ph. D	Doctor of Philosophy (ECE) JNTU,Hyderabad	Awarded
PG	M.E(Microwave &RADAR) OSMANIA UNIVERSITY	First Class
UG	AMIETE (ECE) IETE,NEW DELHI	Second Class

Work Experience	
Teaching	23 Years(including research)
Research	05 Years
Industry	--
Others	--

Area of Specialization Microwave&Radar, VLSI

Professional Memberships LMIETE, LMISTE,LMIAENG

Responsibilities held at Institution Level	NIL
Responsibilities held at Department Level	1. Co-ordinator for Department Antirragging Committe 2. Member, Course Expert Group. 3. Member,NBA Criterions (6&10) for department 4. Member, CIE

Research Guidance -----  
Awards Received -----

Courses Handled at Under Graduate / Post Graduate Level. EMTL, AWP, MWE, Radar, EDC,S&S,VLSI,SC,NATL,RFIC,GPS, CMC

No. of Papers Published National Journals – -- International Journals – 20  
National Conference – 4 International Conference –3

Projects Carried out -----  
Patents -----

Technology Transfer

Invited Speaker 1.Lectures (5) on “Antenna &Wave propagation on 2007 August organized by MLRIT, Dept of ECE  
2. A Lecture on “Evolution of technology in MicroElectronics” .on 19-10-2012 organized by BVCE,ODALAREVU, Dept. of ECE.  
3. A Lecture on “Microwave antennas “ on 28-03-2013 organized by BVCE,ODALAREVU, Dept. of ECE  
4. A Lecture on “Impulse radiating antenna design and applications “on 18-12-2-18 in faculty development program organized by SNIST, Hyderabad Dept of ECE

1. Design of conical TEM feed for an Impulse radiating antenna , Lambert Academic Publication, june /2012, m.l.n.acharyulu,dr.n.s.murthi sarma,Prasanna Rajan ISBN :978-3-8484-1094-1

No. of Books/Chapter Published with details 2.Microstrip L Band Filter For Clock Source At Submeter Wavelength, Lambert Academic Publication, Aug/2012, Mohan,M.L.N.Acharyulu,Dr.N.S.M.Sarma ISBN:978-3-8484-1308-9

**FDPs Attended:**  
1.Attended webinar on “Antenna protocols” organized by department of electronics and communication engineering ,RVS College of engineering & Technology , Coimbatore on 1.6.2020  
2.Participated webinar on “Next generation AI Technologies for Cyber Physical Systems “organized by St.Peters Engineering college ,Hyderabad on 13.05.2020

Details of Short-Term Training Programs/Faculty Development Programs/Seminars/Workshops.Other Trainings (**Attended and/or Organized**).

3. Attended workshop on "VLSI CIRCUITS ANALOG AND DIGITAL DESIGN PERSPECTIVES" Organized by department of ECE, JNTUA College of engineering associated with ASM Semiconductors and HCL Technologies 16.5.2020 to 17.05.2020.
4. actively participated in the Two-day workshop on "Wired and Wireless Network Simulation using Network Simulator (NS-2)" organized by dept of ECE, St. Peters Engineering college, from 19.05.2020 to 20.05.2020.
5. actively participated in "**one week National Level Faculty Development program on Innovation to Academicians**" organized by National Science And Technology Entrepreneur Ship Development Board, DEPARTMENT OF SCIENCE AND TECHNOLOGY, GOVT. OF INDIA and Ramachandra College of Engineering, ELURU, AP(st) From 11.5.2020 to 16.5.2020
6. Attended webinar on "CHALLENGES IN CYBER SECURITY" organized by department of electronics and communication engineering, saveetha engineering college, Chennai on 14.5.2020
7. Attended 3day webinar Faculty Development Program on "**IOT with Artificial Intellegent in collaberation with 360Dogi TMG**" organized by department of electronics and communication engineering, sir CR Reddy College of engineering, Eluru from 27.5.2020 to 29.05.2020.
8. Participated in the faculty development program on "**Research Paper & Project Proposal Writing**" under IEEE Student Branch held on 15-May to 16-May, 2020 organized by the Department of Electrical and Electronics Engineering, SREE VIDYANIKETHAN ENGINEERING COLLEGE(A), Sree Sainath Nagar, A. Rangampet, Tirupati
9. Participated in online Faculty development program on Renewable Energy sources :A Way Ahead organized by mechanical dept in association with ASM international Pune Chapter and ISARAE Nagpur chapter " Organized by CUMMINS COLLEGE OF ENGINEERING FOR WOMENS, NAGPUR MH(st)15 th may to 21 may 2020.
10. participated 2-week AICTE Funded Faculty Development Programme on Pedagogical Training on Outcome Based Education slot 1 conducted in the online mode from 2<sup>nd</sup> Nov to 16<sup>th</sup> Nov 2020 organized and Hosted by St. Peters engineering College(A), Hyderabad

#### Details of Journal Publications/ Conferences

1. Acharyulu, MLN.; Murthysarma, K. Lalkishore ;, Novelty of Multi Route Coding for Reduction of IR Drop " International Journal of Grid and Distributed Computing Vol. 13, No. 2, (2020), pp. 785 – 791 (SCI JOURNAL)
2. Acharyulu, MLN.; Murthysarma, K. Lalkishore, "Multi route coding for reduction of IR drop losses and length" international journal of research and development, volume 4, Issue 1, January 2019.
3. Acharyulu, MLN.; N.S. Murthy sarma, K. Lal Kishore, "Novelty of diagonal power routing in design of op-amp at nanometer scale" international Journal of Communication Engineering and Its Innovations Volume 4 Issue 1, Page 1-14 MAT journals 2018
4. Acharyulu, MLN.; N.S. Murthy sarma, K. Lal Kishore, "validation of diagonal power routing approach in very large scale integrated circuit design for better prospects over orthogonal routing at nano metre era" CVR Journal of Science & Technology, Volume 12, June 2017
5. Acharyulu, MLN.; N.S. Murthy sarma, K. Lal Kishore, "Adressing of Electromagnetic Interference Aspects in VLSI design", International Journal of Advanced Research in Computer and Communication Engineering Copyright to IJARCCCE DOI 10.17148/IJARCCCE.2016.5652 242 Vol.No. 5, Issue No. 6, June 2016.
6. Acharyulu, MLN.; N.S. Murthy sarma, K. Lal Kishore, "Literature Review on High Frequency Affects and Methodologies in Reduction of EM Interference and IR Drop in VLSI Circuits," IJIREICE Vol.No. 4, Issue No. 5, May 2016
7. Acharyulu, MLN.; N.S. Murthysarma, K. Lal Kishore "Minimization of IR Drop Using Diagonal Power Routing Technique in Nanometer Era in VLSI", International Journal of Scientific Engineering and Technology Volume No. 4 Issue No. 4, pp: 247-251, April. 2015
8. Acharyulu, MLN.; N.S. Murthysarma, K. Lalkishore, "Electromagnetic Interference Reduction Technique For Dynamic IR drop Reduction", International Research Journal Of Sustainable Science & Engineering .Volume No: 2, Issue: No: 2, 2014.
9. Acharyulu, MLN.; N.S. Murthysarma, K. Lalkishore, "Investigation of Inductance effects reduction in IR drop analysis using diagonal power routing in Power grid circuits in VLSI," International Journal of Scientific & Engineering Research, Volume 4, Issue 11, November-2013
10. Acharyulu, MLN.; Murthysarma, K. Lalkishore "A Novel Approach of Inductive Effects and Reduce IR Drop in High Speed Vlsi," International Journal of Engineering Research & Technology (IJERT) Vol. 2 Issue 10, October – 2013
11. Acharyulu, MLN.; Murthysarma, K. Lalkishore "Investigation of Electromagnetic Interference in CMOS Power

- Distribution Networks”,International Journal of Engineering Research Volume No.2, Issue No. 7, pp : 424-431 , Oct. 2013
- 12 Acharyulu MLN; Murthi sarma NS; Madanmohan MS,“Adaptive array algorithm for electromagnetic modelling”, International journal of wireless communication andTechnologies;Dec ’12 –Jan ’13.
- 13 MurthySharma,N.S.; M.L.N.Acharyulu, B.rajayalakshmi and K.Lalkishore,“Investigation Of EMI/EMC inspects in Design Of a S band Band Pass Filter Using VLSI,” (IJ-ETA-ETS), Vol. 4 No.1, P.37-42, January, 2011
- 14 Acharyulu,MLN.; Murthysarma “Lens shaping for impulse antenna used formicrowave applications”,International journal of computing andbusiness systemsVolume No.4, Issue No. 7, July,2011
- 15 Acharyulu MLN; murthy sarma NS; Naga Raju A; “Electromagnetic simulation and design of a conical feed of an impulse radiating antenna formicrowave applications”.International journal of electrical, electronics and computing technology, Sept –December 2011
- 16 G.Aruna Kumari; Acharyulu MLN; “Brain tumor detection and classification using active contour and SVM” International journal of Management, technology andEngineering,october 2019.
- 17 Acharyulu MLN, Balraju,Bheem sing,Manvitha;,”Implementation Of Spi Protocol In Fpga And Validation With Various Inputs” ,IJCRT\_195387,june 2020
- 18 Acharyulu MLN,Suhitha reddy: “A Novel Approach of ONOFIC Method to the Minimization of Leakage Power in CMOS Circuits”, International Journal of Electronic Design Engineering, Volume-7, Issue-1 (January-April, 2021)

#### PUBLICATIONS IN INTERNATIONAL and NATIONAL CONFERENCES

- 1 Acharyulu,MLN.;Arun kumar sarma;,”Wireless power generation using solar power satellites and Rectenna”,proceeding of 1st National conference, BMCT Indore,27th&28th April 2012
- 2 Acharyulu,MLN.; Murthysarma, K.Lalkishore ‘Investigation of Inductive effects and IR drop Analysis in high speed VLSI circuits” proceedings of 59th IRF international conference pune ,India. pp.68-75 03rdJuly 2016
- 3 Acharyulu,MLN.; Murthysarma, K.Lalkishore “Evaluation and analysis of the dynamic voltage IR drop using diagonal power routing method” Proceedings of National conference on modern electronics & High speed communications, SNIST,Hyderabad pp.2-5, 29-30 ,July 2016.
- 4 Acharyulu, MLN.; Murthysarma, K.Lalkishore,; “Validation of Diagonal Power Routing Approach in Very Large Scale Integration Design for Better Prospects over Orthogonal Routing atNanometre Era” International Conference on Energy, Communication, Data Analytics and Soft Computing (ICECDS 2017)”IEEE conference Chennai section SKR Egeineering college Poonamalle, Tamil Nadu 1st&2nd August of 2017
- 5 Acharyulu,MLN.; Murthysarma, K.Lalkishore , “Novelty of diagonal power routing in design of op-amp at nanometre scale”Conference Proceeding of National Conference on Signal Processing & Communication,6&7 April 2018.
- 6 Acharyulu, MLN.; Murthysarma, K.Lalkishore,;Lens antennas for microwave applications .Proceedings of international conference on electrical ,Electronics and Communication Engineering (ICEECE 2018)SPEC,Hyderabad ,27th&28th of APRIL.2018.
- 7 Acharyulu,MLN.; Murthysarma, K.Lalkishore,;multi route coding for reduction of power dissipation and length for nano meter technology”.international conference on computer science,Electrical ,Elctronics and communication Engineering ,in SPEC,Hyderabad ,2020 October