Name of Faculty	Dr. N.Chintaiah		
Designation	Assistant Professor		
Nature of Job/Appointment	Contractual		
Date of Joining	28-01-2021		
E-mail	chintaiah_ece@cbit.ac.in		

Education Qualifications	Name of the Degree	Class
Ph. D	Doctor of Philosophy (ECE) Sri Venkateswara University, Tirupati	Awarded
PG	M. Tech.(Embedded systems) JNTUK	Distinction
UG	B. Tech. (ECE) JNTUH	1st class

Work Experience		
Teaching	8 Years
Research	4 Years
Industry	--
Others	--

Area of Specialization	VLSI
Professional Memberships	NIL
Responsibilities held at Institution Level	NIL
Responsibilities held at Department Level	NIL
Research Guidance	NIL
Awards Received	NIL
Courses Handled at Under Graduate / Post Graduate Level.	EDC,ECA,MPMC,PTSP,VLS IDESIGN,DASP

No. of Papers Published	National Journals – --0	International Journals – 6
	National Conference –1	International Conference –2

Projects Carried out	NIL
Patents	NIL
Technology Transfer	NIL
Invited Speaker	**NIL**
No. of Books/Chapter Published with details	NIL
Details of Short-Term Training Programs/Faculty Development Programs/Seminars/Workshops.Other Trainings (**Attended and/or Organized**).	**NIL**
Other information	UGC-NET-JRF QUALIFIED

Details of Journal Publications/ Conferences

1. N.Chintaiah, and G. Umamaheswara Reddy "Optimized Power Control using Adaptive Bus Encoding Technique in Data Transmission", Computers and Electrical Engineering Vol .78, August 2019, pp. 463-471. (Elsevier -UGC-CARE Listed, S.No:7711, Scopes id:18159) IF: 2.16.

2. N.Chintaiah, and G. Umamaheswara Reddy "Low power sector based transition reduction bus encoding in SoC Interconnects", International Journal of Computer Aided Engineering and Technology. (Accepted in 18th December 2018, UGC-CARE Listed, S.No:37504, Scopes id: 21100220382) IF:0.43.

3. N.Chintaiah, and G. Umamaheswara Reddy "Low power bus encoding schemes to minimization of Cross-talk in VLSI Interconnects", International Journal of Engineering and Technology, Vol .7, November 2018, pp.644-647. (UGC-CARE Listed, S.No: 45632, Scopes id: 21100805731) IF:0.08.

4. N.Chintaiah, and G. Umamaheswara Reddy "Classification and evaluation of transition reduction data encoding techniques for low power SoC/NoC Interconnects", I-manager's journal of Embedded Systems, Vol.6, No.1, December 2017, pp. 35-46. (UGC Listed, S.No: 6356)

5. N.Chintaiah "Automated Advanced Industrial and Home Security Using GSM and FPGA",

International journal of Computer Science and Information Technology, Vol .2, No.4, September 2011, pp.1598-1602.

6. N.Chintaiah "Time scale Modification of of Speech Signals using Wavelet packet Transform", International Journal of Science and Research,Vol.4, No.12, pp-1174-1179 December 2015.

International Conference Publications

1. N.Chintaiah, and G. Umamaheswara Reddy "Fibonacci series based Transition reduction Dynamic Sector CODEC Design", 2nd IEEE International Conference on Computing Methodologies and Communication (ICCMC 2018), 15th and 16th Feb-2018, pp 603-607.

2. N.Chintaiah, and G. Umamaheswara Reddy "FPGA Implementation of Coding Minimizing Delay and Power In SOC Interconnects", 1st Springer International Conference on Soft Computing and Signal Processing (ICSCSP–2018), 22nd and 23nd June-2018, pp 427-435.[Springer journal "Advances in Intelligent Systems and Computing"]

National Conference Publications

1. N.Chintaiah, and J. Sneha latha "Advanced industrial and home security using GSM and FPGA", National Conference on Emerging Trends in Electronics and Communication (NCETEC-2K11) 3rd and 4th December-2011. pp 45-50.

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