

Name of Faculty Mrs. GHATA CHAUHAN
 Designation Assistant Professor
 Nature of Job/Appointment Contract (till 31/May/2021)
 Date of Joining 24 - 06 - 2019
 E-mail ghatachauhan_ece@cbit.ac.in



Education Qualifications	Name of the Degree	Class
PG	M. Tech. (VLSI Design)	Distinction
UG	B. E. (ECE)	Distinction

Work Experience

Teaching	3 Years
Research	--
Industry	--
Others	--

Area of Specialization VLSI

Professional Memberships	--
Responsibilities held at Institution Level	--
Responsibilities held at Department Level	--
Research Guidance	--
Awards Received	--

Courses Handled at Under Graduate / Post Graduate Level. Applications of IoT, VLSI Technology,

No. of Papers Published	National Journals – Nil	International Journals – 01
	National Conference – Nil	International Conference – Nil

Projects Carried out	--
Patents	--
Technology Transfer	--
Invited Speaker	--
No. of Books/Chapter Published with details	--

Details of Short-Term Training Programs/Faculty Development Programs/Seminars/Workshops/Other Trainings (Attended and/or Organized).	<ol style="list-style-type: none"> 1. Participated in One Week Online Faculty Development Programme on "Future Nano Electronic Devices & Circuits" Organized by Department of ECE, Mahatma Gandhi Institute of Technology, Hyderabad, Telangana from 6th to 10th July, 2020. 2. Successfully completed One Week Online Faculty Development Program on "Research Opportunities in Image Processing and Machine Learning", conducted by the Department of Electronics and Communication Engineering during 29th June 2020 to 03rd July 2020. 3. Successfully completed Hands-on Workshop on "BodhiTree/SAFE workshop" organized by IIT Bombay Mneic conducted on 20-06-2020 4. Attended India's First Leadership Talk by "Mr. Shridhar Venkat, (CEO) Akashaya Patra Foundation" organized by MHRD's Innovation Cell on 20th June, 2020. 5. Successfully completed 5 day Faculty Development Programme conducted by ATAL Training Programme, NITTTR Chandigarh on "Quantum Computing" during 15th to 19th June 2020. 6. Attended Webinar on "Trends and Technologies in VLSI" conducted by Pantech E-learning on 19th May 2020. 7. Participated in the IIC Online Sessions conducted by Institution's Innovation Council (IIC) of MHRD's Innovation Cell, New Delhi to promote Innovation, IPR, Entrepreneurship, and Start-ups among HEIs from 28th April to 22nd May 2020 during COVID-19 nationwide lockdown.
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8. Successfully completed one week Faculty Development Programme on "OUTCOME BASED EDUCATION AND NBA ACCREDITATION PROCESS" organized by CBIT, Hyderabad during 28-05-2020 – 01-06-2020.
9. Completed Two day FDP on "Effective Technical Report Writing Using LaTeX" organized by Dept. of ECE, MGIT, Hyderabad during 08-06-2020 and 09-06-2020.
10. Successfully completed 4 day Hands-on Workshop on "Effective and Efficient Online Teaching in the Age of Corona" organized by IIT Bombay Mneict conducted during 21-05-2020 to 24-05-2020.
11. Completed five Days Online Faculty Development Programme on "RECENT COMMUNICATION TOOLS AND ITS APPLICATION" Organized by Department of Electronics and Communication Engineering, St. Martin's Engineering College during 19-05-2020 to 23-05-2020.
12. Completed online certification skill development program on "Design, Implementation and Verification in VLSI" organized by Sandeepani School of embedded system design during 27-04-2020 to 01-05-2020.
13. Attended National Seminar on "IoT for Real World" organized by department of CSE, IT and MCA of CBIT and MGIT, Hyderabad held on 28-10-2019.

Details of Journal Publications/
Conferences (National and
International)

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