

Name of Faculty MOUNIKA JAMMULA
 Designation Assistant Professor
 Nature of Job/Appointment 24– 06 - 2019
 Date of Joining Regular
 E-mail jmounika_ece@cbit.ac.in



Education Qualifications	Name of the Degree	Class
Ph. D		pursuing
PG	M.E.	First
UG	B. Tech (ECE)	First

Work Experience

Teaching	8 Years
Research	--
Industry	--
Others	--

Area of Specialization Systems and Signal Processing

Professional Memberships --

Responsibilities held at Institution Level --

Responsibilities held at Department Level --

Research Guidance --

Awards Received

Courses Handled at Under Graduate / Post Graduate Level. Analog Communication, Electronic Instrumentation, Elements of ECE, Digital Image Processing, Internet of Things, Micro Controllers and Programmable DSPs

No. of Papers Published	National Journals – Nil	International Journals – 03
	National Conference – Nil	International Conference – Nil

Projects Carried out --

Patents --

Technology Transfer --

Invited Speaker --

No. of Books/Chapter Published with details --

- Details of Short-Term Training Programs/Faculty Development Programs/Seminars/Workshops. Other Trainings (Attended and/or Organized).
1. Participated one week technical training program on "Android Application Development(code E)" conducted by IETE Mumbai in association with Pantech E learning from 6th July to 10th July.
 2. Participated in one FDP on "Future Nano Electronic Devices and Circuits" organized by Department of ECE, MGIT, Hyderabad from 6th July to 10th July.
 3. Attended one week FDP on "Research Opportunities in Image Processing and Machine Learning", conducted by the Department of Electronics and Communication Engineering during 29th June to 03rd July 2020 at CBIT Hyderabad.
 4. Participated in the webinar on "Present Trends and Research scopes in 5G Wireless Communications" conducted by the Department of Electronics and Communication Engineering, GPREC, Kurnool on 27th and 28th June, 2020.

International/National Journals from the Year 2017

1. Mohd Ziauddin Jahangir, J. Mounika, "Design and simulation of an innovative CMOS ternary 3 to 1 multiplexer and the design of ternary half adder using ternary 3 to 1 multiplexer", Microelectronics Journal 90 (2019) 82–87.
2. MounikaJammula, "Analysis of Modified Reed Solomon Error Correcting Codes", International Journal of Recent Scientific Research, IJRSR, Vol. 9, Issue, 6(A), pp. 27225-27228, June, 2018.
3. MounikaJammula, 2017 " Automatic Speech Recognition system for class room database management in Fixed – C Language ", e-ISSN: 2278-2834, p-ISSN: 2278-8735. Volume 12, Issue 4, Ver. III (Jul.-Aug.

