

Name of Faculty N. JAGAN MOHAN REDDY
 Designation Assistant Professor
 Nature of Job/Appointment Regular
 Date of Joining 09 - 08- 2006
 E-mail jaganmohanreddy_ece@cbit.ac.in



Education Qualifications	Name of the Degree	Class
Ph. D	--	--
PG	ME (Communication Engineering)	First with Distinction
UG	B.Tech (ECE)	First with Distinction

Work Experience

Teaching	14 Years
Research	--
Industry	--
Others	--

Area of Specialization Communication Engineering, Digital VLSI design

Professional Memberships --

Responsibilities held at Institution Level

1. Member, Course Expert Group
2. Member, Syllabus Review Group
3. Lab In-charge for IC lab
4. Worked as Alumni Coordinator in CBIT from 01.01.2016 to 10.10.2018

Responsibilities held at Department Level

Research Guidance --

Awards Received

Courses Handled at Under Graduate / Post Graduate Level. VLSI Design, VLSI Technology, Low Power VLSI, CPLD and FPGA Architectures, Digital IC Design, Linear IC Design, Integrated Circuits and Applications, Network and Transmission lines, Network Analysis.

No. of Papers Published National Journals – -Nil International Journals – 04

National Conference – -Nil International Conference –01

Projects Carried out --

Patents --

Technology Transfer --

Invited Speaker --

No. of Books/Chapter Published with details --

- Details of Short-Term Training Programs/Faculty Development Programs/Seminars/Workshops. Other Trainings (Attended and/or Organized).
1. Successfully Completed “Interfacing with the Arduino” an online non-credit course authorized by University of California, Irvine and offered through Coursera on 09-07-2020
 2. Successfully Completed “SQL for Data Science” an online non-credit course authorized by University of California, Davis and offered through Coursera on 23-06-2020
 3. Successfully Completed “The Arduino Platform and C Programming” an online non-credit course authorized by University of California, Irvine and offered through Coursera on 03-06-2020
 4. Successfully completed the “Outcome Based Education and NBA Accreditation Process-(UG)” an online one week Faculty Development Program during 28-05-2020 to 01-06-2020 organized by Chaitanya Bharathi Institute of Technology, Hyderabad.
 5. Successfully Completed “Python Data Structures” an online non-credit course authorized by University of Michigan and offered through Coursera on 31-05-2020
 6. Participated in the IIC Online Sessions conducted by Institution’s Innovation Council (IIC) of MHRD’s Innovation Cell, New Delhi to promote Innovation, IPR, Entrepreneurship, and Start-ups among HEIs from 28th April to 22nd May 2020 during

- COVID-19 nationwide lockdown.
7. Successfully Completed "Introduction to the Internet of Things and Embedded Systems" an online non-credit course authorized by University of California, Irvine and offered through Coursera on 19-05-2020
 8. Successfully Completed "Effective and Efficient online Teaching in the Age of Corona, A Hands On Workshop" on 17-05-2020 by This training was organized by the Teaching Learning Centre (ICT) at IIT Bombay, funded by the Pandit Madan Mohan Malaviya National Mission on Teachers and Teaching (PMMMNTT), MHRD, Govt. of India
 9. Successfully Completed "AI For Everyone" an online non-credit course authorized by deeplearning.ai and offered through Coursera on 07-05-2020
 10. Successfully completed the "Design, Implementation and verification in VLSI" an online certification skill development Program from 27-04-2020 to 01-05-2020 by Sandeepani school of Embedded Systems Design, Bangalore.
 11. A One Week Faculty Development Program on "Accreditation and Outcome Based Learning", during August -October 2019, by NPTEL-AICTE.
 12. A One Week Faculty Development Program on "Hardware Modeling Using Verilog", during August -October 2019, by NPTEL-AICTE.
 13. A One and half Week Faculty Development Program on "Switching Circuits and Logic Design", during July-October 2019, by NPTEL-AICTE.
 14. A Half Week Faculty Development Program on "Global Navigation Satellite Systems and Applications", during July-August 2019, by NPTEL-AICTE.
 15. A One Week Faculty Development Program on "CMOS Digital VLSI Design", during February -April 2019, by NPTEL-AICTE.
 16. A One Week Faculty Development Program on "REAL TIME EMBEDDED SYSTEMS AND IOT, IT'S APPLICATIONS", from 09.04.2018 – 14.04.2018 by the E&ICT Academy, National Institute of Technology Warangal at Joginpally B R Engineering College, Hyderabad.
 17. A Two Week Faculty Development Program on "Digital VLSI Circuit Design,", from 03.06.2017 - 12.06 2017, Jointly organized through NKN by Electronics and ICT Academies of IIT Roorkee, at National Institute of Technology Warangal.

International/ National Journals from the Year 2017

1. N. Jagan Mohan Reddy (2019) "Comparison Study of Hamming and Kaiser Window over Band Pass Filter Banks in Cochlear Implant System" SSRG International Journal of Electronics and Communication Engineering (SSRG-IJECE) – Volume 6 Issue 5 – May 2019 pp: 35-44
2. N. Jagan Mohan Reddy (2018) "Radio Frequency Identification and Detection (RFID) based e-passport verification scheme using Contactless IC Technology" IJRECE VOL. 6 ISSUE 2 APR.-JUNE 2018, pp:1951-1956.