

Name of Faculty Sri. P. CHANDRA SEK HAR
 Designation Assistant Professor
 Nature of Job/Appointment Regular
 Date of Joining 07 – 03 - 2014
 E-mail pchandrsekhar_ece@cbit.ac.in



Education Qualifications	Name of the Degree	Class
Ph. D	Doctor of Philosophy	Pursing
PG	M. Tech. (Electronics)	First
UG	B. E. (ECE)	First

Work Experience

Teaching	15 Years
Research	02 years
Industry	---
Others	---

Area of Specialization Communication and Signal Processing, VLSI Design

Professional Memberships IEEE Member, Number: 94093248

Responsibilities held at Institution Level --

- Responsibilities held at Department Level
1. Coordinator for 'Teaching Learning Process-CCC-MCC Committee'.
 2. Member of Course Export Group
 3. In-charge for Electronics Circuits –II Lab.
 4. Member of Technical Committee for Synapse.

Research Guidance ---

Awards Received ---

Courses Handled at Under Graduate / Post Graduate Level. Electronic Devices, VLSI Design, Analog Circuits, Signal & Systems, Linear Integrated Circuits, Probability Theory and Stochastic Processing, Analog and Mixed Signal IC Design, RFIC Deign, Analog & Digital VLSI Design, Principles of CMOS VLSI Design.

No. of Papers Published National Journals – Nil International Journals – 3
 National Conference – 01 International Conference – 02

Projects Carried out --

Patents --

Technology Transfer --

Invited Speaker --

No. of Books/Chapter Published with details --

- Details of Short-Term Training Programs/Faculty Development Programs/Seminars/Workshop/ Other Trainings (Attended and/or Organized).
1. Successfully Organized a One Week Faculty Development Programme on "IoT for Emerging Applications" during 04th – 08th February, 2020 in association with Electronics & ICT Academy IIT Guwahati at CBIT.
 2. Successfully Organized a Two-Days Workshop "Analog VLSI Design – Bridging gap between classroom and industry" in association with 'Joint chapter of CAS /EDS IEEE Hyderabad section on 17th and 18th of March, 2017.
 3. Participated in the IIC Online Sessions conducted by Institution's Innovation Council (IIC) of MHRD's Innovation Cell, New Delhi to promote Innovation, IPR, Entrepreneurship, and Start-ups among HEIs from 28th April to 22nd May 2020 during COVID-19 Nationwide Lockdown.
 4. Participated in one week online Refresher Course on "Bharateeya Chaitanyam", organized by Geervana Bharathi of Chaitanya Samskruthi from 29th June to 05th July 2020.
 5. Participated in the webinar on "Present Trends and Research scopes in 5G Wireless Communications" conducted by the Department of Electronics and Communication Engineering, GPREC, on 27th and 28th June, 2020.
 6. Participated in webinar on "Bock Level Deign using IP Integrator in

- Xilinx Vivado” held on 26th June 2020 in association with Core EL Technologies and Xilinx
7. Participated in the webinar on “Publishing Research Articles in Scopus Indexed Journals” conducted by CBIT School of Management Studies on 24th June, 2020.
 8. Participated in a webinar on “Research challenges and Career Opportunities in VLSI” held on 6th June 2020 organized by department of ECEC, NBKR Institute of Science and technology-AP
 9. Successfully Completed a one week FDP on “Outcome Based Education and NBA Accreditation Process” at CBIT during 28-05-2020 to 01-06-2020.
 10. Participated in webinar organized by Internal Quality Assurance Cell (IQAC), Rai University on “How to publish research Articles in CI Journals using Info-Graphic Tools” on 4th June 2020.
 11. Successfully Completed “TCP/IP and Advanced Topics” a 5-Week online non-credit course authorized by University of Colorado and offered through Coursera on 13-05-2020.
 12. Successfully Completed “Peer-to-Peer Protocols and Local Area Networks” a 5-Week online non-credit course authorized by University of Colorado and offered through Coursera on 13-05-2020.
 13. Successfully Completed “Packet Switching Networks and Algorithms” a 5-Week online non-credit course authorized by University of Colorado and offered through Coursera on 13-05-2020.
 14. Successfully Completed “Fundamentals of Network Communications” a 5-Week online non-credit course authorized by University of Colorado and offered through Coursera on 13-05-2020.
 15. Successfully Completed “Introduction to Electronics” a 7-Week online non-credit course authorized by Georgia Institute of Technology and offered through Coursera on 17-05-2020.
 16. Successfully Completed “AI for Everyone” a 4-Week online non-credit course authorized by Deeplearning.ai and offered through Coursera on 11-05-2020.
 17. Successfully Completed “Wireless Communications for Everybody”, a 6-Week online non-credit course authorized by Yonsei University and offered through Coursera on 11-05-2020.
 18. Successfully completed the 'Effective and Efficient Online Teaching in the Age of Corona, A Hands On Workshop' on 17th May 2020 organized by the Teaching Learning Centre (ICT) at IIT Bombay, under by the Pandit Madan Mohan Malaviya National Mission on Teachers and Teaching (PMMMNMTT), MHRD, Govt. of India.
 19. Participated in a Five Days Online Faculty Development Programme on “Recent Communication Tools and its Application” Organized by Department of Electronics and Communication Engineering, St. Martin’s Engineering College From 19th to 23rd May 2020
 20. Participated in One week programme "NAAC Awareness Programme for Faculty" organized by Marathwada Mitra Mandal Institute of Technology (MMIT), Pune, online during the period 08/05/2020 to 14/05/2020.
 21. Attended a five days online webinar on “Knowledge Sharing Session on Recent Trends and Its Application in Electronics” Organized by Department of Electronics and Communication Engineering St. Martin’s Engineering College from 03rd to 07th May, 2020
 22. Attended a 5-Day STTP on “MATLAB based Teaching –Learning in Mathematics, Science and Engineering” organized by the Department of Electronics Engineering, Ramrao Adik Institute of Technology, Nerul, Navi Mumbai and B&R Automation Pvt. Ltd. Pune during 8-12 June2020.
 23. Participated in the webinar on “Analog IC Design using Mentor EDA Tools” held on 10th April 2020 in Association with CoreEL Technologies and Mentor
 24. Successfully completed a 12 Week NPTEL-(FDP) course "NBA Accreditation and Teaching - Learning in Engineering (NATE)", on the SWAYAM platform (swayam.gov.in), during the Jan-Apr 2020 Semester.
 25. Successfully completed an 8 Week NPTEL-(FDP) course “Fundamentals of MIMO Wireless Communication”, on the SWAYAM platform (swayam.gov.in), during the Jan-Apr 2020 semester.
 26. Successfully completed a 8 Week NPTEL-(FDP) course “Evolution of Air Interface towards 5G”, on the SWAYAM platform (swayam.gov.in), during the Jan-Apr 2020 semester.

27. Successfully completed an 8 Week NPTEL-(FDP) course "Hardware Modeling using Verilog", on the SWAYAM platform (swayam.gov.in), during the Aug.-Nov. 2019 Semester.
28. Successfully completed a 8 Week NPTEL-(FDP) course "CMOS Digital VLSI Design" on the SWAYAM platform (swayam.gov.in), during the Feb-Apr 2019 Semester.
29. Attended "88th Orientation Course" at Osmania University held between 13th December 2018 to 11th January 2019, conducted by UGC-HRDC-Osmania University-Hyderabad.
30. Attended AICTE sponsored two week Faculty development programme on "Simulation of Wireless Sensor Networks using NS-2" during 20th-November to 2nd December 2017 at Sreenidhi Institute of Science and Technology.

International/National Journals from the Year 2017

1. Sarvade Venkata Manogna, P. Chandra Sekhar (2020),"Vertical-Horizontal Binary Common Sub-Expression Elimination based FIR Filter using XOR and MUX based Adder for DWT Applications", International Journal of Management Technology And Engineering (IJMTE), Volume X, Issue VII, JULY/2020 - ISSN: 2249-7455
2. N. Alivelu Manga, E.Chandrasekhar, P. Chandra Sekhar (2019), "Design and Simulation of 12-bit Current Steering DAC", International Research Journal of Engineering and Technology (IRJET), VOLUME: 06 ISSUE: 07, ISSN: 2395-0072.
3. P. Chandra Sekhar (2019), "Low Power High Speed Square Root BK Carry Select Adder using MT-CMOS Technique in Cadence Tool", IJRECE, VOLUME: 07 ISSUE:02 ISSN:2348-2281

International/National Conference Proceedings from the Year 2017

1. S. Greeshmasai, N. Alivelu Manga, P. Chandra Sekhar (2020), "Design and Simulation of FinFET based digital circuits for Low Power Applications", 2020 IEEE International Students' Conference on Electrical, Electronics and Computer Science-, Bhopal,978-1-7281-4862-5,22-23 February 2020.
2. P. Chandra Shekar, Sai Soham Ramagiri, Bharghav Rapolu, Chaithan Nunsavath(2020), "Project Mom- Intelligent Voice- Activated Personal Assistant (VAPA) for usage by Individuals who are Visually Impaired", SIGCHI National Conference at MGIT-Hyderabad.
3. Ms. P. Sriravali, J. Balakrishna, P.Chandra Sekhar, N. Alivelu Manga,(2018)"Design and Implementation of Low Noise Amplifier (LNA) for IRNSS Receiver", Proceedings of the International Conference on Inventive Research in Computing Applications (ICIRCA 2018) ISBN: 978-1-5386-2456-2.