



Career Development Centre

Date: 13.09.2024

MosChip Technologies Analog Design, Digital Design Written Test and Technical Interviews Process

Sl.No	Company	Date	Event	Time	Venue	Eligible Branches 2025 Passout
1	MosChip Technologies	14.09.2024	Analog Design Written Test	8.45 AM to 10.40 AM	7th Floor, My Home Twitza, Plot No 30/A, Survey no. 83/1, TSIIC Hyderabad Knowledge City, Raidurg, Panmaktha, Rangareddy, Telangana - 500081.	
2	MosChip Technologies	14.09.2024	Digital Design Written Test	11:00 AM to 12:00 Noon	7th Floor, My Home Twitza, Plot No 30/A, Survey no. 83/1, TSIIC Hyderabad Knowledge City, Raidurg, Panmaktha, Rangareddy, Telangana - 500081	
3	MosChip Technologies	15.09.2024	Technical Interviews	8.45 AM to 6.30 PM	7th Floor, My Home Twitza, Plot No 30/A, Survey no. 83/1, TSIIC Hyderabad Knowledge City, Raidurg, Panmaktha, Rangareddy, Telangana - 500081	

• The main security has the list of students appearing tomorrow, they can cross check their name by showing any ID proof to enter the building

Note: Kindly as them not to carry any gadgets, calculators, electronic items etc. except pen.

Note: Documents Mandatory, College ID Card, updated resumes-2 copies, Three Photographs & All Photo Copy's Mark Sheets (from Std X), Formal Dress Compulsory.

CC to Principal, CBIT for kind information,

CC to HOD's/FPC's - ECE & EEE.

CC to Director Academics, Jd- Academics, Director IQAC, Director AEC & Library.

Dr.NLN Reddy Advisor-CDC