

**CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY (A)**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**Class Time Table for the AY 2021-22 I-Semester**

**Program : M.E (ECE-ES&VLSID)**

**w.e.f.:25-10-2021**

**Room No: N-109**

Period day	I	II	III	IV	L U N C H	V	VI
	9:10 To 10:10 AM	10:20 To 11:20 AM	11:30 To 12:30PM	1:10 To 2:10 PM		2:20 To 3:20 PM	3:30 To 4:30 PM
MONDAY	ACO	A&D CMOS VLSID	E for RPW	Mentoring		VLSIT & PDA	Fit India
TUESDAY	ACO	M&P DSP	A&D CMOS VLSID Lab			A&D CMOS VLSID Lab	
WEDNESDAY	RM&IPR	E for RPW	ACO	M&P DSP		A&D CMOS VLSID	Library
THURSDAY	A&D CMOS VLSID	VLSIT & PDA	M&P DSP Lab			M&P DSP Lab	
FRIDAY	M&P DSP	VLSIT & PDA	Library	RM&IPR		Library	Library
Theory							
Code	Name of the Course			Name of the Faculty			
Program Core Courses							
20EC C201	Analog and Digital CMOS VLSI Design			Dr.N.AliveLu Manga	9491595585 alivelumanga_ece@cbit.ac.in		
20EC C203	Microcontrollers and Programmable Digital Signal Processors			Smt. D. Sony	9700583344 sonyd_eee@cbit.ac.in		
Mandatory Course							
20MEM103	Research Methodology and IPR			Sri.K.Yasoda Sreeram	9440423518 yasodasreeram_mech@cbit.ac.in		
Program Elective Courses (PECs)							
20EC E201	Advanced computer orgazniation			Dr.P.Sathish	9177545484 psathish_eee@cbit.ac.in		
20EC E213	VLSI Technology and Physical Design Automation			Dr.M.Ramana Reddy	9849634083 mramanareddy_eee@cbit.ac.in		
Audit Course							
20EG A101	English for Research Paper Writing			Mr.Srinivas Andoju	9000356111 andoju_english@cbit.ac.in		
Practical Courses							
Code	Name of the Practical Course			Name of the Faculty			
20EC C205	Analog and Digital CMOS VLSI Design Lab			Mohd Ziauddin Jahangir	9885717845 jahangir_eee@cbit.ac.in		
20EC C206	Microcontrollers and Programmable Digital Signal Processors Lab			Smt. D. Sony	9700583344 sonyd_eee@cbit.ac.in		
Incharge							
ME co ordinator and Class Incharge				Name of the Faculty			
Incharge for Attendance Statements				Mohd Ziauddin Jahangir	9885717845 jahangir_eee@cbit.ac.in		
Incharge for CIE Marks							
Departmental Coordinators for Mentoring and Counselling				Dr. P. Narahari Sastry & Smt. D. Naga Devi	9948397802 naraharisastry_eee@cbit.ac.in 9704111749 dnagadevi_eee@cbit.ac.in		
Departmental Coordinators for Activity Points				Dr. P. Narahari Sastry, Smt.A.Satyavati & Smt.Ch.Navitha	9948397802 naraharisastry_eee@cbit.ac.in 9494235614 satyavati_eee@cbit.ac.in 9966552000 navitha_eee@cbit.ac.in		
Departmental Incharges for Internships				Dr.P.Sathish & Dr.Sai Krishna Kondoju	9177545484 psathish_eee@cbit.ac.in & 8919126650 saikrishna_eee@cbit.ac.in		
Departmental Incharges for Placements				Sri.K.Sudershan Reddy & Sri.J.Bala Krishna	9291522525 sudarshanreddy_eee@cbit.ac.in 8985151787 jbalakrishna_eee@cbit.ac.in		
<b>Mentors: Smt.D.Sony - Entire class</b>							

HEAD, DEPT. OF ECE

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**DEPARTMENT OF ECE**  
 Chaitanya Bharathi Institute of Technology  
 Hyderabad-500 075

**CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY (A)**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
**Class Time Table for the AY 2021-22 III-Semester**

Program : M.E (ES&VLSID)

w.e.f.:23-10-2021

Room No: N-109

Period day	I	II	III	IV	LUNCH	V	VI
	9:10 To 10:10 AM	10:20 To 11:20 AM	11:30 To 12:30PM	1:10 To 2:10 PM		2:20 To 3:20PM	3:30 To 4:30PM
MONDAY	Industrial Project / Dissertation Phase-I		Library	Mentoring		Library	Fit India
TUESDAY	Industrial Project / Dissertation Phase-I						Industrial Project / Dissertation Phase-I
WEDNESDAY	Industrial Project / Dissertation Phase-I						Industrial Project / Dissertation Phase-I
THURSDAY	Library	BA	Library	FPGA & CPLD A		BA	Library
FRIDAY	BA	FPGA & CPLD A	FPGA & CPLD A	Library		Library	Library
SATURDAY	Industrial Project / Dissertation Phase-I						Industrial Project / Dissertation Phase-I
Theory							
Code	Name of the Course			Name of the Faculty			
Program Core Courses							
20EC C110	Industrial Project / Dissertation Phase-I			Dr.D.Krishna Reddy 9866502428 dkrishnareddy_ece@cbit.ac.in / Dr.M.Ramana Reddy 9849634083 mramanareddy_ece@cbit.ac.in/ Sri.Mohd Ziauddin Jahangir 9885717845 jahangir_ece@cbit.ac.in			
Program Elective Courses (PECs)							
20EC E204	FPGA & CPLD Architectures			Sri.N.Jagan Mohan Reddy 9912939138 jaganmohanreddy_ece@cbit.ac.in			
Open Elective Courses (OECs)							
20CS O101	Business Analytics			Dr. M Swamydas 9490475959 msdas_csc@cbit.ac.in			
Incharge				Name of the Faculty			
ME co ordinator and Class Incharge				Dr.M.Ramana Reddy			
Incharge for Attendance Statements				9849634083			
Incharge for CIE Marks				mramanareddy_ece@cbit.ac.in			
Departmental Coordinators for Mentoring and Counselling				Dr. P. Narahari Sastry Smt. D. Naga Devi 9948397802 naraharisastry_ece@cbit.ac.in 9704111749 dnagadevi_ece@cbit.ac.in			
Departmental Coordinators for Activity Points				Dr. P. Narahari Sastry, Smt.A.Satyavati & Smt.Ch.Navitha 9948397802 naraharisastry_ece@cbit.ac.in 9494235614 satyavati_ece@cbit.ac.in 9966552000 navitha_ece@cbit.ac.in			
Departmental Incharges for Internships				Dr.P.Sathish & Dr.Sai Krishna Kondoju 9177545484 psathish_ece@cbit.ac.in & 8919126650 saikrishna_ece@cbit.ac.in			
Departmental Incharges for Placements				Sri.K.Sudershan Reddy & Sri.J.Bala Krishna 9291522525 sudarshanreddy_ece@cbit.ac.in 8985151787 jbalakrishna_ece@cbit.ac.in			
<b>Mentors: Dr.M.Ramana Reddy - Entire class</b>							

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