

Name of Faculty Dr. M. RAMANAREDDY
 Designation Asst. Professor
 Nature of Job/Appointment Regular
 Date of Joining 12 – 07 - 2004
 E-mail mramanareddy_ece@cbit.ac.in



Education Qualifications	Name of the Degree	Class
Ph. D	Doctor of Philosophy (ECE)	Awarded
PG	M. Tech. (ECE)	First
UG	AMIETE(ECE)	First

Work Experience

Teaching	20 Years
Research	--
Industry	04
Others	--

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Area of Specialization Instrumentation & Control Systems, VLSI Design, IoT and A. I

Professional Memberships Member IETE

Responsibilities held at Institution Level

- Responsibilities held at Department Level
1. Member Board of Studies,
 2. Member, ISO Committee
 3. Member, Course Expert Group
 4. Course Coordinator ME(ES&VLSID)

Research Guidance --

Awards Received --

INSTITUTE OF TECHNOLOGY

Courses Handled at Undergraduate / Post Graduate Level. Applied Electronics, Basic Electronics, Basic Circuit Analysis, Instrumentation Engineering, Linear Integrated Circuits, Digital Integrated Circuits, Linear and Digital Integrated Circuits, Integrated Circuits and Applications, Network Theory, Networks and Transmission lines, Analog Electronics Circuits, Digital systems using VHDL, VLSI Technology, VLSI Physical Design and Automation, VLSI design Testing and Verification.

No. of Papers Published National Journals – International Journals – 20
 National Conference – 01 International Conference – 02

Projects Carried out C2S PROJECT entitled "THE DESIGN AND DEVELOPMENT OF SILICON PROVEN IP CORE FOR HIGH RESOLUTION ADPLL " as a Resource Person Since 2023 Till Date

Patents

1. AI Based Drone for Medical Logistics Delivery (Granted) UK Patent-2024
2. IOT Enabled Respiratory

Technology Transfer --

Invited Speaker -

No. of Books/Chapter Published with
details

Details of Short-Term Training Programs /Faculty Development Programs /Seminars/Workshops/ Other Trainings (Attended and/or organized).

1. One week PDP attended on “ Exploring the significance of Indian Knowledge Systems[IKS] at CBIT ,HYD on 5th to 10th Feb 2024
2. One week FDP Attended on “Application of AI/ML Using Pattern Recognition &Intellectual Property Rights “ at CBIT , HYD on 29th to 2nd Feb 2024.
3. Participated in one-week online Faculty development program on “Future Nano Electronics Devices and Circuits at MGIT , Hyderabad during 06-07-2020 to 7-07-2020.
4. Successfully Completed “AI for Everyone” an online non-credit course authorized by University of Colorado System and offered through Coursera on 07-05-2020.
5. Successfully Completed “Introduction to Electronics” an online non-credit course authorized by Georgia Institute of Technology through Coursera on 30-05-2020.
6. A One Week Online Faculty Development Program on “IoT and Machine Learning”, at GPREC, Kurnool during 26-06-2020 to 30-06-2020.
7. A One Week Online Faculty Development Program on “Data science using R Programming” at MISTE, Hyderabad, during 12-06-2020 to 17-06-2020.
8. A one Week International online knowledge development program on “Challenges and advancements in the design of IoT, Embedded and VLSI Systems’ A Researchers View” GEC,

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- Vijayawada, during 08-06-2020 to 13-06-2020
7. A One-Week on line FDP on “Research Trends and Research Areas in Applied VLSI and Advanced Communications, VVIT, Guntur during 8-06-2020 to 12-06-2020
 8. A Two day ISRCE International Symposium on “Machine Learning Algorithms” OUCE,OU,Hyderabad during 21-12-2019to 22-12-2019.
 9. National workshop organized on “Pattern Recognition and machine learning” at CBIT during 30-05-2019 to 31-05-2019.
 10. Guest lecture arranged on “Recent developments in electronic devices and Circuits” at CBIT during 16-08-2019.
 11. A two day workshop on “Analog VLSI Design”,CBIT, Hyderabad 17-03 -2017 to 18-03-2017.

International/National Journals from the Year 2017

1. Viveksingh kushwah, **M Ramana Reddy**, M.Laksmi Narasimha Charyulu, P.Narahari Sstry, , 3 March 2024. Design and analysis of frequency agile LP to CP converter loaded silicon –graphine based MIMO array antenna in THz regime. Springer
2. **M.Ramana Reddy**, M.Laksmi Narasimha Charyulu, Viveksingh kushwah, P.Narahari Sstry, K., 2024, March. Design and investigation on two port circularly polarized graphine silicon based MIMO Antenna with high isolation for THz Wireless applications Springer Nature, under optical society of India
3. M.Ramana Reddy , M.D Shafi , 2024 “Design and Development of Bomb Detection and Disposal Robot with GPS Location &Live Video Streaming using Raspberry PI “
4. M. Ramana Reddy, “Implementation of SVM Machine Learning Algorithm to Predict Lung and Brest Cancer”, the “Turkish Journal of Computer and Mathematics Education” with ISSN: 1309-4653, 2021.
5. M. Ramana Reddy, “Implementation of SVM Machine Learning Algorithm to Predict Lung and Brest Cancer”, the “Turkish Journal of Computer and Mathematics Education” with ISSN: 1309-4653, 2021
6. M. Ramana Reddy, “Low Noise and high linearity Wide-band Low Noise Amplifier for 5G Receiver Front End System”, International Journal of Innovative Technology and Exploring Engineering (IJITEE) ISSN: 2278-3075, Volume-10 Issue-6, April 2021.
7. M. Ramana Reddy, "Design of Static CMOS 16 Bit High Speeds and Low Power Consumption Hybrid Adder Circuit using Brent Kung Adder ", International Journal of Recent Technology and Engineering (IJRTE) ISSN: 2277-3878, Volume-8 Issue-6, March 2020.
8. M. Ramana Reddy, "IoT BASED AIR AND SOUND POLLUTION MONITORING SYSTEM USING MACHINE LEARNING ALGORITHMS", journal of ISMAC (2020). Vol.02/ No. 01. Pages: 13-25, DOI: <https://doi.org/10.36548/jismac.2020.1.002>.
9. M.Ramana Reddy, Dr.N.S.Murthy Sharma, Dr.P.Chandrasekhar, “A Novel RF CMOS Ultra-Wide-Band LNA at 2.66 - 3.75 GHz in 180nm Technology”, International Journal of Engineering and Advanced Technology (IJEAT) ISSN: 2249-8958, Volume-8 Issue-5, June 2019.
10. M.Ramana Reddy, Dr.N.S.Murthy Sharma, Dr.P.Chandrasekhar, “A 2.4 GHz low noise amplifier design at 130nm CMOS technology using common gate topology for Wi/WiMAX application”, International Journal of Engineering&Technology, Vol.7,(1.3),67-73,2018.
11. M.Ramana Reddy, Dr.N.S.Murthy Sharma, Dr.P.Chandrasekhar, “The Design of Cascode, Shunt feedback Low Noise Amplifiers in 180nm Technology for WiMAX Applications”, International Journal of Applied Engineering Research ISSN 0973-4562 Volume 12, pp. 15957-15965, Number 24,2017.
12. M.Ramana Reddy, Dr.N.S.Murthy Sharma, Dr.P.Chandrasekhar, “A 3.5 GHz Low noise, high gain narrow band differential low noise amplifier design for WIMAX applications”, International Journal of Electronics Engineering Research, Vol. 9,NO.4 pp.505-560, ISSN:0975-6450,Impact Factor: 1 March 2017.

Books /Book Chapters

13. The manuscript (Ref. no. 2021/BP/9295D) entitled “Design of Static CMOS 16 Bit High Speeds and Low Power Consumption Hybrid Adder Circuit using Brent Kung Adder” is publication as a book chapter in the following book: New Approaches in Engineering Research (International Book)