

Name of Faculty Dr. P.Anuradha
 Designation Associate Professor
 Nature of Job/Appointment Regular
 Date of Joining 28-12-22
 E-mail anuradhap_ece@cbit.ac.in



Education Qualifications	Name of the Degree	Class
Ph.D.	Doctor of Philosophy (Embedded Systems) JNTU, Hyderabad	Awarded
PG	M.Tech (Embedded Systems) SR Engineering college, Warangal	Distinction
UG	B.Tech (ECE) NIT, Warangal	First

Work Experience

Teaching	18 Years
Research	--
Industry	--
Others	--

Area of Specialization Embedded Systems and VLSI
 Academic Identity Scopus Id: 56928120700 Orcid Id: 0000-0001-5303-1896

Professional Memberships IETE- AM-237174

Responsibilities held at Institution Level ---

Responsibilities held at Department Level ---

Research Guidance Guided 16 MTech projects

Awards Received ---

Courses Handled at Under Graduate / Post Graduate Level. Embedded systems,VLSI Design and Technology, Signals and Systems, Digital signal processing, Microcontrollers and Applications, ARM architectures, Microprocessors and Embedded Systems, Digital Logic Design, CPLD & FPGA Architectures and Applications, RTL Synthesis and Simulation with PLDs

No. of Papers Published National Journals -- International Journals – 35
 National Conference – 2 International Conference –18

Projects Carried out Department of Science and Technology(DST) funded project completed under Women Scientists Scheme (WOS-A) during 2012-2015
 Project Role: Principal Investigator
 Title: High Performance Embedded System Design Using Workload Characterization

Patents 01
 Technology Transfer ---
 Invited Speaker ---

No. of Books/Chapter Published with details

1. J. Ravichander, P. Anuradha, B.Girirajan, Innovative Practices for Effective Education, Lambert Academic Publishing, Germany, 2020, ISBN: 978-620-3-02767-9
2. P. Anuradha, J. Ravichander, B.Girirajan, Efficient Pedagogy for Teaching Learning Process, Lambert Academic Publishing, Germany, 2020, ISBN: 978-620-3-04264-1
3. P. Anuradha, J. Ravichander, B.Girirajan, Effective Instructional Pedagogy Process, Lambert Academic Publishing, Germany, 2020, ISBN: 978-620-3-19839-3

Details of Short-Term Training Programs/Faculty Development Programs/Seminars/Workshops. Other Trainings (Attended and/or Organized).

4. B.Girirajan , J. Ravichander, P. Anuradha, Student Centric Engineering Education, Lambert Academic Publishing, Germany, 2020, ISBN: 978-620-3-20266-3
1. Organizing committee member for FDP program on Machine learning at S R Engineering college on March 29th 2019
2. Organizing committee member for National Level workshop on Digital Signal Processing using Python at S R Engineering College on March 2nd 2019.
3. Organizing committee member for International Conference on Recent Advances in Communication, VLSI Embedded Systems, (ICRACVES-2014), at SR Engineering College, Warangal from 19th and 20th March 2014
4. Coordinator for Workshop on Recent Trends in VLSI Design and Embedded Systems at SR Engineering College, Warangal from 17th and 18th March 2017.
5. Two week AICTE sponsored FDP on “Signal Processing Techniques for Advanced Wireless Communication” during 23rd-28 NOV 2020, Organized by Department of Electronics and Communication Engineering, Jayamukhi Institute of Technological Sciences, Warangal
6. Two week AICTE sponsored FDP on “System on chip design – Basics to development of chips” during 1-1 2th NOV 2020 Organized by Department of Electronics and Communication Engineering, MVSR Engineering College, Hyderabad
7. Two week DST sponsored FDP on “Block chain Technologies” during 17th-28th JUN 2019, Organized by Department of Computer Science and Engineering, S R Engineering College Warangal.
8. One week FDP on “Technical writing” during 7th-11th JAN 2019, Organized by Department of Electronics and Communication Engineering, S R Engineering College Warangal.
9. Five day FDP on “Signal Processing and It’s Applications” during 19th-23rd DEC 2016, Organized by Department of Electronics and Communication Engineering, Kamala Institute of Technology & Science, Karimnagar
10. One day seminar on “Trick your Latent Self & Trigger”, 20th DEC 2016, Organized by Department of Electronics and Communication Engineering, Kamala Institute of Technology & Science, Karimnagar
11. Two day national workshop “Nurturing engineering through cognitive science(NEECS)” during 16th-17th DEC 2016, Organized by Department of Electronics and Communication Engineering, S R Engineering College Warangal
12. Three day workshop “IUCEE International Engineering Educator Certification Workshop(phase I)” during 14th-16th Nov 2016, Organized by Department of Electronics and Communication Engineering, S R Engineering College Warangal
13. Five day workshop on “Signal Processing for Communications” during 4th-8th Jan 2016, Organized by Department of Electronics and Communication Engineering, National Institute of Technology, Warangal.
14. Three day workshop on “System on Chip Design Using ARM” during 23rd-25th June 2014, Organized by Department of Electronics and Communication Engineering, University College of Engineering (A) Osmania University, Hyderabad.
15. Two day TEQIP - II on sponsored workshop on Open Source Software(OSS-NITW-2014), 14 & 15 March 2014 Organized by Department of Electronics and Communication Engineering, National Institute of Technology, Warangal.
16. One day workshop on “Defence Electronics” Organized by Department of Electronics and Communication Engineering , Kakatiya University College Of Engineering Warangal, on February 5th 2014

17. One Day Faculty Development Program on “Outcomes-Based Engineering Education for Enhanced Employability” on 6th Oct, 2013 at S R Engineering College.
18. Two Day Workshop on “VLSI Design Methodologies using FPGA Technology” on 26th & 27th Sep, 2013 at Varadha Reddy College of Engineering.
19. Two week ISTE Workshop on “Analog Electronics” Conducted by IIT Kharagpur on 4th to 14th June, 2013 at Prasad Engineering College.
20. Three day intensive Course on “Electromagnetic Theory, Transmission Lines and Waveguides” conducted at JNTUH College of Engineering, Jagtial karimnagar Dist During March 1-3, 2012
21. One day seminar on “Patent Based Projects” organized by Department of Mechanical Engineering in association with ISTE, New Delhi held on 12th May, 2011.
22. One day seminar on “Opportunistic Computing –A New Programming Paradigm for a Scalable realism on many-cores” in association with ISTE, New Delhi held on 17th May, 2011
23. Two day National Level Workshop on “Recent Advances In Communications & Electronics” organized by Department of Electronics & Communication Engineering, BITS, Narsampet, on 30th June & 1st July, 2009.

Details of Journal Publications/ Conferences

International Journal publications

Anuradha, P., Rajkumar, K., Arabelli, R.R., Shareena, R., “QCA-Based Error Detection Circuit for Nanocommunication Network”, Springer Smart Innovation, Systems and Technologies, ISSN: 2190-3026, Vol. 1, pp.141–149, 2022.

Rajkumar, K., Anuradha, P., Arabelli, R., Vasavi, J., “Design and Synthesis of Random Number Generator Using LFSR”, Springer Smart Innovation, Systems and Technologies, ISSN: 2190-3026, Vol. 1, pp.131–139, 2022.

P. Anuradha, “Effectiveness of device parameters with regard to the electrical characteristics of the MOSFET double gate”, Materials Today: Proceedings, ISSN 2214- 7853, pp. 1-4, Mar 2021.

P. Anuradha, “QCA Based Approximate Multipliers and Adders Method”, Materials Today: Proceedings, ISSN 2214- 7853, pp. 1-4, Mar 2021.

P. Anuradha, Rajeshwar Rao Arabelli, K. Rajkumar, “Noise Removal of ECG Signals with Adaptive Filtering”, Materials Today: Proceedings, ISSN 2214- 7853, pp. 1-6, Mar 2021.

Samra Jabeen, S. Jha, P. Anuradha, “Impact of Variation of Device Parameters on the Electrical Characteristics of Double-Gate MOSFETS”, International Journal of Innovative Technology and Exploring Engineering (IJITEE), ISSN 2278-3075, Vol.9, Issue No.3, pp. 3658-3661 JAN 2020.

P. Anuradha, “An Intelligent Computing Framework For Multicore Heterogeneous Embedded Architectures”, International Journal of Advanced Science and Technology, ISSN 2005-4238, Vol. 28, No. 17, pp. 701-708, DEC 2019

P. Anuradha, “The Teaching Learning Process”, International Journal of Advanced Science and Technology, ISSN 2005-4238, Vol. 28, No. 17, pp. 709-714, DEC 2019.

P. Anuradha, “Software and Hardware Tool for the Development of Embedded Software and a Study on Applications and Characteristics of Embedded System”, International Journal of Advanced Science and Technology, ISSN 2005-4238, Vol. 28, No. 17, pp. 1-8, DEC 2019.

Veeramaneni Sharath kumar, P.Anuradha, "Power Consumption Optimization and Home Automaton using Smart Sensor Networks" International Journal of Innovative Technology and Exploring Engineering (IJITEE) , ISSN 2278-3075, Vol. 8, No.6S4, pp. 837-841, April 2019.

P. Anuradha, Hemalatha Rallapalli, G. Narsimha, "Energy Efficient Scheduling Algorithm For The Multicore Heterogeneous Embedded Architectures", Springer SCIE journal Design automation for embedded systems, Vol. 21, No. 4, pp. 1-12, 2018, DOI: 10.1007/s10617-018-9202-7

P. Anuradha, Hemalatha Rallapalli, G. Narsimha, "Versatile Intelligent ELM Algorithm for Workload Characterization", Journal of Advance Research in Dynamical and Control Systems(JARDCS) , ISSN 1943-023X, Vol. 10, pp. 177-184, July 2018

P. Anuradha, Hemalatha Rallapalli, G. Narsimha, "Characterization of SPEC Benchmarks", International Journal of Pure and Applied Mathematics, ISSN: 1314-3395, Vol. 118, No.24, pp. 1-9, June 2018.

Jaffreen Samdani, P.Anuradha, "VLSI Architecture of a High Speed Polar Code Decoder using Finite Length Scaling LDPC Codes", Journal of Advance Research in Dynamical and Control Systems(JARDCS) , ISSN 1943-023X, Vol. 10, pp. 153-161, July 2018 .

International Conference Publications

P. Anuradha, Rajeshwarrao Arabelli, K. Rajkumar and Ch. Padmaja. "MIMO-OFDM based high-rate information transmission through ultrasonic", AIP Conference Proceedings 2418, 030059 (2022);

P. Anuradha, K. Rajkumar, Rajeshwarrao Arabelli and R. Shareena, "Implementation of high speed 64-bit MAC unit using FPGA", AIP Conference Proceedings 2418, 030060 (2022);

P. Anuradha, Hemalatha Rallapalli, G. Narsimha and Syed Mustak Ahmed, "Efficient Workload Characterization Technique for Heterogeneous Processors", IEEE International conference on Advance Computing Conference (IACC), DOI: 10.1109/IADCC.2015.7154819, pp. 812-817, 12th-13th Jun 2015, B.M.S. College of Engineering, Bangalore, India.

P. Anuradha, Hemalatha Rallapalli, G. Narsimha and Syed Mustak Ahmed, "An efficient methodology for workload reduction", 4th IEEE International Conference on communication and Signal Processing- ICCSP'15, ISBN 978-1-4799-8080-2, pp. 1684-1689, 2nd-3rd April 2015, Adhiparasakthi Engineering College, Melmaruvathur, Tamilnadu.

P. Anuradha, Hemalatha Rallapalli and Syed Mustak Ahmed, "Characterization of workload for multicore processor", International Conference on Photonics, VLSI & Signal Processing, pp.549-553, 28th-29th March 2014, Kakatiya University College of Engineering, Warangal, Andhra Pradesh.

P. Anuradha, Hemalatha Rallapalli and Syed Mustak Ahmed, "Spec 95 benchmarks simulation with simple scalar", 2nd International Conference on Communications & Signal Processing, pp. 157-161, 1st-3rd April 2013, at Ongole, Andhra Pradesh.

P. Anuradha, K.Kalpna and G.Renuka, "Adaptive filtering for noisy ECG signals" in two days National Level conference on Recent trends and Advances in Nano Technology, July 2011, Guru Nanak Engineering College, Hyderabad.