

1	Name of Faculty	Dr. B. Neeraja		
2	Designation	Assistant Professor		
3	Nature of Job/Appointment	Regular		
4	Date of Joining	28-01-2011		
5	E-mail	bneeraja_ece@cbit.ac.in		
6	Education Qualifications	Name of the Degree	Class	
		Ph.D	Ph.D.	Awarded
		PG	M.E. (Digital Systems)	Distinction
		UG	B. TECH (ECE)	Distinction
7	Work Experience			
	Teaching	12Years		
	Research	1 years		
	Industry	--		
	Others	06 Months		
8	Area of Specialization	Signal Processing, Wireless Mobile Communication		
9	Professional Memberships	--		
10	Responsibilities held at Institution Level	<ol style="list-style-type: none"> 1. Member of AICTE Disciplinary Committee-2019. 2. Coordinator of student clubs Chaitanya Seva-(Street cause& Engineers without borders), Chaitanya Kreedasports , Chaitanya Sudhee-2019. 3. Coordinator for IETE Student Forum. 		
11	Responsibilities held at Department Level	<ol style="list-style-type: none"> 1. Subject Co-Ordinator, EECE (Equivalent Subject) from 2019. 2. Member, Disciplinary Committee of Department from 2019. 3. Member, Course Expert Group-2019. 4. Member, Common Course Committee -2019. 5. In-charge, National Career Services -2019. 6. In-charge, individual salary statements for all the faculty of the Department from 2016. 7. In-charge, Attendance Statements. 8. In-charge, Sessional Marks. 9. In-charge, EC Lab-1 from 2013. 10. Mentor, Student Progress report. 11. Worked for NAAK,NBA-UG and NBA-PG. 		
12	Research Guidance	--		
13	Awards Received	--		
14	Courses Handled at Under Graduate / Post Graduate Level.	Electronic Devices and Circuits,Analog Circuits,Basic Electronics,Elements of Electronics and Communication Engineering,Wireless Mobile Communication,Radar and Satellite Communication,Coding Theory and Techniques		
15	No. of Papers Published	National Journals – 00	International Journals – 03	
		National Conference – 01	International Conference – 05	
16	Projects Carried out	--		
17	Patents	--		

18	Technology Transfer	--
19	Invited Speaker	<ol style="list-style-type: none"> 1. Resource person for the five day intensive course on "Hands on LabVIEW" under LabVIEW academy school from 21/04/18 to 28/04/18. 2. Delivered a lecture in a four day intensive course on "Hands on Lab VIEW" under LabVIEW academy school from 27/11/17 to 30/11/17.
20	No. of Books/Chapter Published with details	--
21	Details of Short-Term Training Programs/Faculty Development Programs/Seminars/Workshops. Other Trainings (Attended and/or Organized).	<ol style="list-style-type: none"> 1. Participated in the IIC Online Sessions conducted by Institution's Innovation Council (IIC) of MHRD's Innovation Cell, New Delhi to promote Innovation, IPR, Entrepreneurship, and Start-ups among HEIs from 28th April to 22nd May 2020 during COVID-19 nationwide lockdown. 2. Successfully Completed "MATLAB Onramp" online course authorized by MathWorks Training Services on 19/05/2020. 3. Successfully Completed "Introduction to Electronics "an online non-credit course authorized by Georgia Institute of Technology and offered through Coursera on 14/05/2020. 4. Successfully Completed "AI for Everyone" an online non-credit course authorized by Stanford University and offered through Coursera on 02/05/2020. 5. Successfully Completed "LabVIEW Core 2" online course authorized by NI Customer Education on 27/04/2020. 6. Successfully Completed "LabVIEW Core 1" online course authorized by NI Customer Education on 21/04/2020. 7. Successfully Completed "Wireless Communications for Everybody" an online non-credit course authorized by University of Yonsie and offered through Coursera on 06/04/2020. 8. A One Week online Faculty Development Program on "LaTeX & Technical Report", 25 - 30 May, 2020 organized by Department of Mathematics, Kakatiya University, Warangal, Telangana, in association with Spoken Tutorial, IIT Bombay. 9. Successfully Completed a One Week Faculty Development Program on "Outcome based education and NBA accreditation process" organized by CBIT, Hyderabad, during 28/05/20 to 01/06/20. 10. A One Week online Faculty Development Program on "Recent trends in Advanced Communication", Lakireddy Bali Reddy College Of Engineering, Mylavaram, Krishna(Dt) from 04th -9th may 2020. 11. Successfully Completed NPTEL-AICTE course on "Principles and Techniques of Modern Radar Systems", during Jul-oct, 2019. 12. Participated in one day workshop on "Significance of MATLAB in Applications of Emerging Technologies" Department of Electrical and Electronics Engineering, CBIT, Hyderabad on 26th June 2019. 13. A One Week FDP on "5G Wireless Communication" organized by Electronics and ICT Academy, NIT Warangal. Meity, Govt. of INDIA, during 29th May to 3rd June, 2019. 14. Attended 1-week short term training program through ICT mode on "Development of Laboratory Instruction and Manual "organized by CBIT, Hyderabad during 4 feb to 9 feb 2019. 15. Qualified and Certified LabVIEW Associate developer (CLAD) exam and recognized as one of the members in "NI LabVIEW" academy from Aug 2017 to Aug 2019. 16. Organized a Two-week workshop on "Hands on Lab VIEW" under Lab VIEW academy school from 18/06/18 to 30/06/18. 17. Attended a two-week refresher course on "Spectral.

		<p>Methods for Engineering & Scientific computing Applications” at IIT Bhubaneswar, Govt.of India, MHRD on 5 th – 16 th June 2017.</p> <p>18. Attended a workshop on Matlab Expo 2017 and gained knowledge in HDL code writing.</p> <p>19. Attended a one week workshop on “Hands on myDAQ and myRIO” during “6th – 11th Nov’17(1 Week) Organized by Dept. of ECE (Under LabVIEW Academy school).</p> <p>20. Attended a four day Faculty development course on “Certified LabVIEW Associate Developer” during 21st – 24th Aug’ 17 (4 days) organized by Dept. of ECE,CBIT,Hyderabad.</p>
22	Details of Journal Publications/ Conferences (National and International)	
	<p>International Journal</p> <p>1. B.Neeraja, N .V. Koteswara Rao, B. Rajendra Naik,” Identification of Intra Pulse Modulation Signal in the Presence of Noise ”, ARPJ Journal of Engineering and Applied Sciences, VOL. 18, NO. 21, November 2023, pp. 2445-2554.(Scopus)</p> <p>2. B. Neeraja, N .V. Koteswara Rao, B. Rajendra Naik” Detection and Analysis of closely spaced Multiple Targets using Modified MUSIC method” Journal of Advanced Research in Dynamical and Control Systems,Volume 12, Issue 7, 2020, Pages 2857-2865, doi: 10.5373/JARDCS/V12SP7/20202427. (Scopus)</p> <p>3. Neeraja B, Ramya K “Design and Analysis of an Energy Efficient 4-bit Barrel Shifter Circuits in 45nm Technology”, International Journal of Engineering and Advanced Technology (JEAT) ISSN: 2249 – 8958, Volume-9 Issue-4, April, 2020. (Scopus)</p> <p>International /National Conferences</p> <p>1. B. Neeraja, N.V. Koteswara Rao· B. Rajendra Naik, and K. Harshitha, "Design of High-Speed Ethernet Based Receiver Processor Unit for Radar applications," 2022 4th IEEE International Conference on Advances in Computing, Communication Control and Networking (ICAC3N), Greater Noida, India, 2022, pp. 1656-1660, Doi: 10.1109/ICAC3N56670.2022.10074480.</p> <p>2. N. Bandi, R. Ramavath and S. K. Meda, "Design and Analysis of PTL based Basic gates and Adder with Improved Performance," 2020 International Conference on Recent Trends on Electronics, Information, Communication & Technology (RTEICT), Bangalore, India, 2020, pp. 184-188, doi: 10.1109/RTEICT49044.2020.9315581.</p> <p>3. Neeraja Bandi, Sathish Kumar meda," Multiply-Accumulate unit for Binary Arithmetic using high speed adders and PIPO Accumulator”, Scopus indexed Second International Conference on Computing, Communication and Energy Systems 2020 (ICCCES 2020), MEA College of Engineering, Perinthalmanna, Kerala, India, 2020, pp. 1-5.</p> <p>4. B. Neeraja and R. S. P. Goud, "Design of an Area Efficient Braun Multiplier using High Speed Parallel Prefix Adder in Cadence," 2019 IEEE International Conference on Electrical, Computer and Communication Technologies (ICECCT), Coimbatore, India, 2019, pp. 1-5, doi: 10.1109/ICECCT.2019.8869307.</p> <p>5. T. V. Vardhan, B. Neeraja, B. P. Kumar and C. S. Paidimarry, "Implementation of turbo codes using verilog-HDL and estimation of its error correction capability," 2015 IEEE Asia Pacific Conference on Postgraduate Research in Microelectronics and Electronics (Prime Asia), Hyderabad, 2015, pp. 75-79, doi: 10.1109/PrimeAsia.2015.7450473.</p> <p>6. B. Rajendra Naik Bandi Neeraja, A.K.Singh,” Design of High Speed PCB with Signal Integrity”3rd IETE conference on RF and Wireless (Icon RFW-10),Bangalore,India,2010.</p>	