Name of Faculty	Dr. C. V. Narasimhulu
Designation	Professor & Principal
Nature of Job/Appointment	Regular
Date of Joining	05-06-2023
E-mail	cvnarasimhulu_ece@cbit.ac.in
Education Qualifications	Name of the Degree Class
Ph. D	Doctor of Philosophy(ECE) JNTUK Kakinada, A.P. Awarded
PG	M. Tech(Inst & Cont Systems) R.E.C. Calicut (Now NIT, Calicut, Kerala)
UG	B. Tech. (ECE) First Class SV University, Tirupathi, A.P.
Work Experience	
Teaching	29+ Years
Research	
Industry	
Others	
Area of Specialization	Signal and Image Processing, Inst and Cont Systems
Professional Memberships	SMIEEE, FIE, LMISTE, FIETE, CSI
Responsibilities held at Institution Level	Member-Secretary, Governing Body Chairman, Academic Council Chairman, Finance Committee Chairman, College Academic Committee And all other Institute Level Committees
	 Paper Setters, Moderators and Evaluators Student achievements
Responsibilities held at Department Level	 Coordinator Activity points Incharge-Basic Electronics Lab
Responsibilities held at other Institutions	 Principal, LORDS Institute of Engineering and Technology (A), Hyderabad HoD,ECE and In-charge, NIRF, Geethanjali College of Engineering and Technology (A), Hyderabad Principal, Hasvita Institute of Engineering and Technology, Hyderabad HoD, ECE and Principal, Tirumala Engineering College, Hyderabad HoD, ECE, Lakireddy Bali Reddy Engineering College, Mylavaram, Vijayawada
Research Guidance	Four(4) Scholars are awarded with Ph.D. under JNTUK Kakinada
Awards Received	 Honored as "Best Principal" for the A.Y2021from Nature Science Foundation, Coimbatore, Tamilnadu. Honored "Best Researcher" for the year 2022 from Nature Science Foundation, Coimbatore, Tamilnadu. Honored with Dr. A.P.J. Abdul Kalam Rashtriya Puraskar Award for the year 2023 by the Global Management Council. Honored as Best Academic Leader of the Year 2024 by the Global Management Council.
Courses Handled at Under Graduate / Post Graduate Level.	Signals &Systems, Digital Signal Processing, Control Systems, Advanced Control Systems, Instrumentation and Measurements, Electronic Devices, Analog Circuits and Digital Electronics

	National Journals – Nil	International Journals – 39
No. of Papers Published		
	National Conference – 04	International Conference – 14
Projects Carried out	for the tune of Rs.3 la	TAL FDP for A.Y.22-23 was sanctioned akhs and conducted during 19-30 Sep- SCRO/ATAL FDP/2022-23/613.
	Rs.4.98 lakhs under A	FDP was sanctioned for the tune of QIS-2017-18, and same was conducted File No: 34-55/96/RIFD/FDP/Policy-
	1 K Prayeen Kumar	Dr. C. Venkata Narasimhulu, and
AND.	Dr.K.Satya Prasad "	Methodology and System for image I in the month of April 2019. Indian
	Patent, Application No:17/2019(part-1,page 2. P VenkataLavanya	no: 201941015601. Journal ge no.50) , Dr.C.Venkata Narasimhulu,and
	system thereof" public patent, Application	A method of image denoising and a shed in the month of Oct 2019. Indian no: 201941039885. Journal No:
	42/2019(part-1,page n 3. Appala Raju. Uppa	la, Dr.C.Venkata Narasimhulu, and
	secondary users in a c	Method and system for scheduling ognitive radio networks", Indian patent,
Patents	2021	2141015878A,PublicationDate:09-04-
	calculator display fa	hulu, IC-Calculator: Intelligent scientific ast results using machine learning
EIINSTITUT	patent number:202110	
	Fifth Generation WLA Indian patent, Applica	nhulu, "Cell Capacity Evaluation using N and IEEE802.11ax for VBR Traffic", tion Number : 202241011563, Date of
		nhulu, "IOT Based Intelligent device
	Indian patent, Applica publication: 10th Dec	
		NOLOGY
Technology Transfer	1 Quest lasture size	
Invited Speaker	Discrete Time Signals and Technology, Gunt	non "Frequency Domain Analysis of " at Chalapathi Institute of Engineering our on 31st January 2020 on "Analysis of amplifiers using hybrid
	parameter model" at S	wami Vivekanda Institute of Technology I on 27th February 2017
	Expert lectures given of	on Electronic Circuit Analysis for B.Tech ak Engineering College, Ibrahimpatnam
	4. Guest lecturers give	n on Signals & Systems in Newton Macharla, Guntur District during 2010-
		Control Systems in Royal Engineering lak during 2008-09
	6. Guest lecturers given	on Advanced Control Systems for final . University College of Engineering,
No. of Books/Chapter Published with details	 Authored a text bo Watermarking", Pub Publishing, www.lap 	ok titled "Digital Image and Video blished by LAMBERT Academic -publishing.com, ISBN: 978-620-2-
	55710-8	

FDPs Organized:

- Organized AICTE sponsored Two Week FDP on "Advances in Signal and Image Processing with hands- on training using MATLAB", 18-30, November 2019 in Geethanjali College of Engg & Technology, Cheeryal, Keesara, Medchal Dist.
- Organized a one-week FDP on "Fundamentals of Signals, Systems and Random variables with hands-on sessions using MATLAB", 5-10 June 2017 in Geethanjali College of Engg & Technology, Cheeryal, Keesara, Medchal Dist.
- Organized a one-week FDP on "Electromagnetic Theory And Transmission Lines With Emphasis On MATLAB And Ansys", 22-27DEC2014inGeethanjaliCollegeofEngg&Technology, Cheeryal, Keesara, R.R.Dist
- 4.
- 5. Organized Technical Staff Training Program On "Advanced EDA Tools and Latest Equipments", from 16th 17th DEC 2014 in Geethanjali College of Engg & Technology, Cheeryal, Keesara, R.R.Dist.
- 6. Organized AICTE sponsored two-weeks FDP on "Advanced VLSI systems design" from 17- 29 June 2013 in Geethanjali College of Engineering & Technology, Cheeryal, Keesara, R.R.Dist.
- Organized a one-week Refresher Course for faculty on "Electronics Devices and Circuits" from 17 – 28 June 2013 in Geethanjali College of Engg & Technology, Cheeryal, R.R.Dist

International Conference Organized:

1. Organized 3rd International Conference on Advances in Science, Engineering and Management,18-19,November2022 in Lords Institute of Engineering and Technology, Hyderabad.

National Conference Organized:

1. Organized 3rd National Conference on Latest Trends in Signal Processing, VLSI and Embedded Systems, 20–21, June 2014 in Geethanjali College of Engineering & Technology, Cheeryal, Keesara, R.R.Dist.

Workshops Organized:

- 1. Organized a workshop on "DSP/DIP Applications using LABVIEW" from 24-09-2014 to 25-09-2014 in Geethanjali College of Engineering & Technology, Cheeryal, R.R.Dist
- Organized a workshop on "Latest trends and Developments in Microprocessor Applications " from 20th to 22nd Feb, 2014 in Geethanjali College of Engineering & Technology, Cheeryal,
 R.R.Dist
- R.R.Dist
 Organized a workshop on "Technical Staff Development Program (SDP) " from 20th to 22nd December, 2013 in Geethanjali College of Engg & Technology, Cheeryal,
- R.R.Dist
 Organized a workshop on "PCB Design and Fabrication" from 27th & 28th Dec, 2013 in Geethanjali College of Engineering
- & Technology, Cheeryal, Keesara, R.R.Dist
 6. Organized a workshop on" VLSI System Design and Programming" from 18th to 22nd Feb,2014 in Geethanjali College of Engineering & Technology, Cheeryal, R.R.Dist.

Hardware Exhibitions Organized:

- Organized 'TechExpo'19", A 6th National level hardware and software competition on 30th March 2019 in Geethanjali College of Engg & Technology, Cheeryal, Medchal Dist.
- Organized 'TechExpo'18", A 5th National level hardware and software competition on 24th March 2018 in Geethanjali College of Engg &Technology, Cheeryal, Medchal Dist.

Details of Short-Term Training Programs/Faculty Development Programs / Seminars / Workshops. Other Trainings (Attended and/or Organized).

- 3. Organized 'TechExpo'17" A 4thRegional level hardware and software competition on 12th April 2017 in Geethanjali College of Engg & Technology, Cheeryal, Medchal Dist.
- Organizeda"Techexpo'14",A1st National level Hardware Exhibition on 1st Mar,2014 in Geethanjali College of Engineering & Technology, Cheeryal, Keesara, R.R.Dist
- Organized "Technotronics-2k7" a hardware projects exhibition on 4th April 2007 in Tirumala Engineering College, Keesara, R.R. Dist.
- Organized a "Technotronics-2k6" hardware projects exhibition on 27th Jan 2006 in Tirumala Engineering College,Keesara, R.R. Dist.

National Level Technical Fests Organized:

- Organized a "Bhaswara'19", A 13th National Technical and Management Fest ,7-8 March 2019 in Geethanjali College of Engg & Technology, Cheeryal, Medchal Dist.
- Organized a "Bhaswara'17", A 12th National Technical and Management Fest, 13-14 Oct 2017 in Geethanjali College of Engg & Technology, Cheeryal, Medchal Dist
- 3. Organized a national level technical fest "ADHVAYA'12" on 17th March 2012 in Hasvita Institute of Engineering & Technology, Keesara, R.R. Dist.
- Organized "Raphsody-09" on the occasion of 3rd Anniversary Celebrations of SPICE on 21st March, 2009 at Tirumala Engineering College, Keesara, R. R. Dist.
- 5. Organized the 1st Anniversary Celebrations of SPICE on 4th April 2007 and Avyaya-2k7" a Souvenir was released on the occasion by Dr. K.Lal Kishore, Registrar, JNTU, Hyderabad.
- 6. Instrumental informing a student's technical association of ECE dept. 'SPICE' on 27th Jan. 2006 which is used as platform to excel in their hidden talents in Tirumala Engineering College and it was inaugurated by Dr.B.C.Jinaga, Rector, JNTU. Hyderabad.
- 7. Active participation in organizing ELECTRO FEST-2003, a National level student technical paper contest in LBR college of Engg, Mylavaram, Vijayawada in Feb 2003.

Workshops and FDPs Attended:

- Attended a two day National workshop on "Quality Enhancement Measures in Higher, Technical, Professional and Management Educational Institutions with Special focus on NIRF INDIA RANKINGS", organized by JNTUH, Hyderabad and KAB consultants from 18th -19th October 2019.
- Participated a two day All India Seminar on "Cyber Security and Image Processing", organized by The Institution of Engineers, Telangana State, Hyderabad from 10th-11th August 2019
- Attended a one week FDP on "Digital Signal Processing", organized by JNTUH, Hyderabad from 11th - 16th December 2017.
- 4. Attended a one week FDP on "Signals, Systems and Transformation Techniques theory with MATLAB", organized by NIT Warangal from 26th -31st December 2016.
- 5. Attended a one week FDP on "Speech Signal Processing", organized by NIT Warangal from 21st -26th November 2016.
- Attended a one week FDP on "Digital signal processing fundamentals and applications" organized by GCET from 30th November – 5th December 2015.
- Attended a Two Day Workshop on "Advances in Image processing & Application "Conducted by BITS Pilani Hyderabad Campus from 26th Oct to 27th October 2013.
- 8. Attended a Three Day workshop on "Outcome based Accreditation process and Parameters "jointly organized by NBA and OU College of Engineering (Nodal Center) in GCET from 26th September to 28th September 2013.

- 9. Attended a Two day FDP on "Pedagogy" Conducted by GCET, 29 30 April 2013.
- 10. Attended a Three days refreshers course on "Implementation of DSP ALGORITHMS using MATLAB" conducted by Malla Reddy Institute of Technology & Sciences, 20 22 Feb 2008.
- 11. Attended a Six days workshop on "Digital Image Analysis & Processing" conducted by G. Narayanamma Institute of Technology & Sciences, 11-16 Feb 2008.
- 12. Attended a two days orientation program on "Institutional Governance & Leadership Skills to Academic Administrators", conducted by UGC-Academic Staff College, JNTU, Hyderabad, 12- 13 March 2009.
- Attended a three day workshop on "Digital Signal Processing & MATLAB" conducted in JNTU in association with Cranes Software International Limited & JNTU, Hyderabad, 13-15 Dec 2004.
- 14. Attended a one day workshop on "RF & MICROWAVE MEASUREMENT" by Agilent Technologies in hotel TAJ KRISHNA, Hyderabad on 16th Aug 2004.
- **15. 1Attended the AICTE-ISTE** short term training program on "ECAD-Laboratory" conducted by ECE Dept., Sree Nidhi Institute of Science and Technology, Hyderabad from 17 -29 June 2002.

Details of Journal Publications/ Conferences (National and International) Year (s): 2017-2022 International Journals:

- 1. K. Praveen Kumar, Dr. C. Venkata Narasimhulu, and Dr. K. Satya Prasad, "A new image Restoration technique Based on Optimized Deep Generative Adversarial Network with Reinforced Black Widow algorithm" International Journal of Intelligent systems and Application in Engineering, accepted for publication, March 2022. (SCOPUS)
- Venkata Krishna, Dr. C. Venkata Narasimhulu, and Dr. K. Satya Prasad. "A novel filter-bank architecture of 2D-FIR symmetry filters using LUT based multipliers" Integration, the VLSI Journal Vol. 84, pp. 12-25, January 2022. (SCI, SCIE, SCOPUS & WoS-ELSEVIER).
- 3. Odugu Venkata Krishna, C. Venkata Narasimhulu, and K. Satya Prasad "Implementation of LowPower Generic 2D FIR filter bank Architecture using memory-based Multipliers" Journal of Mobile Multimedia, Vol. 18, no. 3, pp. 583–602, January 2022, ISSN:1550-4646, (SCOPUS-RIVER PUBLICATIONS)
- 4. Praveen Kumar, K., C. Venkata Narasimhulu, and K. Satya Prasad. "2D Wavelet Tree Ordering Based Localized Total Variation Model for Efficient Image Restoration."International Journal of Image and Graphics (2022): 2240009. (SCOPUS -WORLD SCIENTIFIC PUBLISHERS)
- Venkata Krishna, Dr. C. Venkata Narasimhulu, and Dr. K. Satya Prasad "An efficient VLSI architecture of 2D FIR filter using enhanced approximate compressor circuits "International Journal of circuit theory and application, Vol.49, no.11, pp.3653-3668, August 2021, ISSN: 1097-007X. (SCI, SCIE, SCOPUS & WoS - Wiley Publishers).
- AppalaRaju. Uppala, Dr.C.Venkata Narasimhulu, and Dr.K.Satya Prasad, "Spectrum sensing By Correlation Modulation Format Identifier and Improved Energy Detection Using Noise Estimation Based Dynamic Threshold" Ad Hoc & Sensor Wireless Networks, Vol-50, no. 1-4, pp.215-237, 2021. (SCIE & SCOPUS-Old City Publishers).
- 7. Appala Raju. Uppala, Dr.C. Venkata Narasimhulu, and Dr.K. Satya Prasad, "Improved Convolutional neural network based on cooperative spectrum sensing for cognitive radio" KSII transactions on Internet and information system, Vol.15, no.06, pp.2128-2147, June 2021. (SCIE & SCOPUS-TIIS Publishers).
- Uppala, Appala Raju, C.Venkata Narasimhulu, and K.SatyaPrasad. "ANovel MAC Protocol with Fusion Center and Adaptive Full-Duplex Communication for Cognitive Radio Networks." IETE Journal of Research (2021):1-13. (SCIE & SCOPUS-Tayler & Francis Publishers).
- Appala Raju. Uppala, Dr.C.Venkata Narasimhulu, and Dr.K.Satya Prasad, "DynMAC Based Spectrum Handoff Algorithm for Deterministic Multihop Industrial Networks", Journal of Green Engineering-March 2021. (11): 3042- 3062. ISSN: 2245-4586 (SCOPUS- Alpha Publishers).
- 10. Dr. C. Venkata Narasimhulu "An automatic feature selection and classification framework for analyzing ultrasound kidney images using dragonfly algorithm and random forest classifier" -IET Image Processing, 15.9(2021):2080-2096.(SCI,SCIE,SCOPUS&WoS-WileyPublishers).

- 11. Dr. C. Venkata Narasimhulu, A New Blind Watermark Embedding Model: Spiral Updated Rider Optimization Algorithm-The computer journal, February 2021. (SCIE & SCOPUS-Oxford Publishers).
- Kumar,K.Praveen,C.Venkata Narasimhulu,andK.SatyaPrasad. "Anewimage restorationapproachby combining empirical wavelet transform and total variation using chaotic squirrelsearch optimization." International Journal of Numerical Modelling: Electronic Networks, Devices and Fields 34.2(2021): e2824. (SCI,SCIE&SCOPUS, WoS -Wiley Publishers).
- 13. P.Venkata Lavanya, Dr.C.Venkata Narasimhulu, K.SatyaPrasad, "Denoising Images by Dual Tree Complex wavelet Transform combined with Metaheuristic Optimization Algorithms", International Journal of Innovative Technology and Exploring Engineering, Feb 2020. (Scopus-Blue Eyes Publishers)
- P. Venkata Lavanya, Dr. C. Venkata Narasimhulu, and Dr. K. Satya Prasad, "Dual Stage Bayesian Network with Dual-Tree ComplexWavelet Transformation for Image Denoising" Journal of Engineering Research, Vol. 8 No. (1), March 2020. ISSN: 2307-1885 (SCIE & SCOPUS-Academic Publication Council)
- Odugu Venkata Krishna, C. Venkata Narasimhulu, and K. Satya Prasad "Design and implementation of low complexity circularly symmetric 2D FIR filter architectures." Multidimensional Systems and Signal Processing, Vol. 31, no. 4, pp. 1385–1410, March 2020. ISSN: 1573-0824, (SCI, SCIE & SCOPUS-Springer).
- Saladi Saritha , Yepuganti Karuna and Dr. C. Venkata Narasimhulu , "segmentation of tumor using PCA based Modified fuzzy C Means algorithms on MR brain images", is accepted for publication in International journal of imaging system and technology , May 2020, ISSN:1098-1098 (SCIE, SCOPUS, Wos- Wiley Publishers).
- 17. P. Venkata Lavanya, Dr. C. Venkata Narasimhulu, and Dr. K. Satya Prasad, "Modified Euler Frobenius Half band Polynomial Filter based Dual-Tree Complex Wavelet Transform and Metaheuristic Optimization Algorithms for Image Denoising along with Artificial Neural Networks" is accepted for publication in the International Journal of Advanced Science and Technology (IJAST), May2020.
- AppalaRaju.Uppala, Dr.C.Venkata Narasimhulu, and Dr.K.Satya Prasad, "Implementation of MAC Protocol for Analysis of Traffic in Smart Cities", Journal of Adv Research in Dynamical & Control Systems, Vol. 11, 06- Special Issue, June 2019, ISSN: 1943-023X (SCOPUS-Institute of Advanced Scientific Research)
- VenkataKrishna, Dr.C.VenkataNarasimhulu, and Dr.K.SatyaPrasad[®]Implementation of LowPowerandMemor y Efficient 2DF inite Impulse Response Filter architecture", International Journal of RecentTechnology and Engineering (IJRTE), Volume-8, Issue-1, May 2019 ISSN: 2277-3878, (Scopus-Blue Eyes Publishers)
- 20. AppalaRaju.Uppala,Dr.C.VenkataNarasimhulu,andDr.K.SatyaPrasad,"AProficientModifiedMulti-ChannelMAC Protocol for Wireless Network", IJRECE, Volume 6,Issue 1,March2018
- 21. P. Venkata Lavanya, Dr. C. Venkata Narasimhulu, and Dr. K. Satya Prasad, "Image denoising using dual tree complex wavelet transform based thresholding method", IJCRT, Volume6, Issue 1,February 2018.
- 22. Venkata Krishna, Dr. C. Venkata Narasimhulu, and Dr. K. Satya Prasad "Design and Implementation of Block Transpose form FIR Filter",IJCA,Volume1,Issue 8, February 2018
- K.Praveen Kumar, Dr.C.Venkata Narasimhulu, and Dr.K. Satya Prasad "AUnit Plane Edge on-off Slope Algorithm Based Fast LTVR Restoration Analysis", International Journal of Engineering & Technology, Volume7, 2018
- 24. Venkata Krishna, Dr.C.Venkata Narasimhulu, and Dr.K.SatyaPrasad, "Efficient VLSI Architectures for FIR Filters" IOSR JVSP, January 2017.
- 25. M.Krishna Chaitanya and Dr.C.V.Narasimhulu, "Remote Sensing Image Enhancement Techniques: A Survey" IJEAM: International Journal of Engineering, Applied and Management Sciences Paradigms, Volume 34, Issue 01, March 2016.
- 26. Alladi Praveena, C.V. Narasimhulu, "Design of DFCWaveforms for MIMO RadarUsing Accelerated Particle Swarm Optimization Algorithm" IJETT : International Journal of Engineering Trends and Technology, Volume33Number2-March 2016
- P. Venkata Lavanya, Dr. C. Venkata Narasimhulu, and Dr. K. Satya Prasad, "A Combined Approach of Wavelet Thresholding and Wavelet Bayesian Network models for Image Denoising" International Journal of Applied Engineering Research, ISSN 0973-4562 Vol. 10, No.44 (2015).
- 28. AppalaRaju.Uppala, Dr.C.Venkata Narasimhulu, and Dr.K.Satya Prasad, "Analysis of Cyclostationary Spectrum Sensing with PSK ModulationSchemes for Cognitive Radio"International Journal of Applied Engg Research, ISSN 0973-4562 Vol. 10, No.44 (2015).

- 29. K. Praveen Kumar, Dr. C. Venkata Narasimhulu, and Dr. K. Satya Prasad "Review on Digital Image Restoration Methods and Future Directions", IJSER, Volume 6, Issue 8, August-2015.
- 30. B.Suresh Babu &C.Venkata Narasimhulu, "Robust Model To Access Consumer Appliances Using Android" IJRAT: International Journals of Research in advent technology, Vol.2, No.10, Oct 2014
- 31. S.Mallesh & Dr. C.V. Narasimhulu "Design of QSD Number System Addition Using Delayed Addition Technique" IJEEE, Vol.1, Issue.10, Oct 2014.
- S.Suresh Kumar,Dr.C.V.Narasimhulu & T Rama Krishna, "The Architecture and Applications of 12 C Device Driver in Embedded Systems", IJR: International Journal of Research, Vol-1, Issue-10 November 2014.
- RadhikaGrandhi & C.VenkataNarasimhulu, "The Flexible BUS Systems Using Zig beeasa Communication Media", IJARCSSE- International Journal of Advanced Research In Computer Science and Software Engineering, Volume- 3, Issue-9, Sept 2013.
- 34. Nagarjuna Gorumuchu, M.Hemalatha Reddy & Dr.C.V.Narasimhulu, "AnEfficient Mining Strategy for the Wireless Surveillance", IJRRECS: International Journal of Review on Recent Electronics and Computer Science, Volume-1, Issue-5, Sept 2013.
- 35. Mahadevuni Sridhar, Dr.C.Venkata Narasimhulu, "Real time security system using face detection and recognition system" IJESR: International Journal of Engineering & Science Research, Volume 3, Issue 10, October 2013.
- 36. C.Venkata Narasimhulu & K. Satya Prasad, "A novel robust watermarking technique based on non-sub sampled contour let transform and SVD", International Journal of multimedia & applications Vol3, No1, Feb 2011
- 37. C.Venkata Narasimhulu & K.Satya Prasad: "A new SVD based hybrid color image watermarking for copy right protection using Contour let transform", International Journal of computer and Applications(IJCA) in Volume20–No.8, April 2011
- 38. C.Venkata Narasimhulu & K.Satya Prasad: "Robust Color Image Watermarking Using Non sub sampled Contour let Transform", IJCSIS: International Journal of Computer Science and Information Security, Vol.9, No.3, March2011.
- 39. C.Venkata Narasimhulu & K.Satya Prasad, "A hybrid watermarking scheme using contour let Transform and Singular value decomposition" IJCSNS: International Journal of Computer Science and Network Security, Vol.10, No.9, September 2010.

International Conferences:

- C.Venkata Narasimhulu & K.Satya Prasad, "Invisible and Robust Color Image Watermarking in Contour let Domain for Image Authentication", Proceedings of the International Conference on Image Processing Computer Vision and Pattern Recognition; IPCV 2011, ISBN: 1-60132-189-9, 1-60132-190-2 (1-60132-191-0), United States of America.
- C.Venkata Narasimhulu & K. Satya Prasad, "A Non-Blind Hybrid Video watermarking Scheme based on Singular Value decomposition and Contourlet Transform", Proceedings of International Conference on Advances in Computer Science and Electronics Engineering – CSEE 2013. ISBN: 978-981-07-5461-7, DOI: 10.3850/978-981-07-5461-7_07.
- 3. P.Venkata Lavanya, C. Venkata Narasimhulu, and Dr. K. Satya Prasad, "Transformations Analysis for Image Denoising Using Complex Wavelet Transform ",Proceedings of 2017 IEEE International Conference on Innovations in Information, Embedded and Communication Systems, (ICIIEC), 17 - 18 March 2017, ISBN:978-1-5090-3294-5. https://doi.org/10.1109/ICIIECS.2017.8276155
- 4. C.Venkata Narasimhulu , "A Robust Hybrid Video Watermarking algorithm Using NSCT and SVD", Proceedings of 2017 IEEE International Conference on Power, Control, Signals and Instrumentation Engineering (ICPCSI-2017), 21-22, Sep 2017, ISBN:978-1-5386-0814-2 https://doi.org/10.1109/ICPCSI.2017.8391961
- 5. P. Spandana and C. Venkata Narasimhulu, "Mammogram Classification using Rotation Invariant Local Frequency Features", International Conference on Recent Trends in Machine Learning, IOT, Smart Cities & Applications (ICMISC 2020), 28-29 March 2020 by Springer
- P. Venkata Lavanya, C. Venkata Narasimhulu, and Dr. K. Satya Prasad, "Image Denoising using an Artificial Neural Network and Genetic Optimization algorithm Based Dual-Tree wavelet Transform ", ICSCSP20, 19-20 June 2020 by Springer.

- 7. P. Venkata Lavanya, Dr. C.Venkata Narasimhulu, and Dr. K. Satya Prasad, "LOG-GABOR Filter Based Dual Tree Complex Wavelet Transform And Optimization using Metaheuristic Algorithms for Image Denoising with Deep Belief Networks", Accepted & Presented paper -ICSVCE2021-29th & 30th April 2021.
- 8. P. Venkata Lavanya, C. Venkata Narasimhulu, and Dr. K. Satya Prasad, "Image Denoising using DT-CWT combined with ANN and Grey wolf Optimization Algorithm", ICAIAA 2022, April 23-24, 2022 by Springer.
- 9. Dr. C.Venkata Narasimhulu. S.Siva Priyanka, Bindu Vasini, "Agroprobe: Integrated Npk and Moisture Content Analyzer", 3rd International Conference on Modelling, Simulation and Intelligent Computing (MoSICom 2024), 9-11 December 2024 (IEEE)
- DLS Reddy, Siva Priyanka, C. Venkata Narsimhulu, Dhanalakshmi, "Modeling efficient deep learning network on imbalanced learning network on imbalanced data for pest prediction", 3rd International Conference on Automation, Signal Processing, Instrumentation and Control (ICASIC), Wiley Publisher,7th - 8th Nov 2024 (Wiley Book Chapter)
- 11. Vasanth, K., Nithin, M.C., Sri Datta, A.L.N., Narasimhulu, C.V., Anuradha, P., "VLSI Implementation of an Efficient 2D Median Finding Algorithm for Non Linear Image Filtering", Proceedings of International Conference on Circuit Power and Computing Technologies, ICCPCT 2024, 2024, pp. 1387–1391.
- 12. K.Vasanth, C.Venkata Narasimhulu, "Design of Ground Station Antennas for Tracking of LoRa based Satellites at 433MHz Amateur Frequencies", Paper accepted in IEEE Conference at NMIT, Bangalore.
- 13. K.Vasanth, C.Venkata Narasimhulu, "An Efficient Selection gate logic based Ripple Carry Adder for Deep Learning Networks", Paper presented at IEEE Conference at ICMACC 2024, Hyderabad.
- 14. K.Vasanth, C.Venkata Narasimhulu, "Area Optimized Vasanth Sorting Hardware for Image Denoising Application", Paper presented at AIP Conference publishing, Vardhaman college of Engineering, Hyderabad.

National Conferences:

- 1. U.Appala Raju and Dr.C.Venkata Narasimhulu, "Investigating Suitable Signal processing Techniques for spectrum sensing" Proceeding of 3rd National conference on Latest trends in Signal Processing, VLSI and Embedded Systems, 20-21June 2014, ISBN: 978-93-83459-63-6
- A. Shankar and Dr.C. Venkata Narasimhulu, "A Survey on Face Recognition Techniques" Proceeding of 3rd National conference on Latest trends in Signal Processing, VLSI and Embedded Systems, 20 - 21 June 2014, ISBN: 978-93- 83459-63-6
- T.Rama Krishna and Dr.C.Venkata Narasimhulu "NSCT and SVD based colour Image Watermarking for Noise Attacks" Proceeding of 3rd National conference on Latest trends in Signal Processing, VLSI and Embedded Systems, 20 _ 21 June 2014, ISBN: 978-93-83459-63-6.
- 4. P.Venkata Lavanya and Dr.C.Venkata Narasimhulu, "A review on Different Techniques of Digital Image Denoising" Proceeding of 3rd National conference on Latest trends in Signal Processing, VLSI and Embedded Systems, 20-21June 2014, ISBN:978-93-83459-63-6.

Other Relevant Information:

1. Attended Indian Engineering Services (IES-98) interview on 11th Mar 1999 at UPSC, New Delhi.

Interaction with outside world:

- 1. Vice-Chairman, IETEHyderabadCenterfortheterm2018-20and2016-18
- 2. Session chair and Reviewer: IEEE-RTEICT-2016-Banglore, Springer-ICIECE-2016-Hyderabad, IEEE-ICPCSI-2017- Chennai, NCCSPVLSI- 2016-Hyderabad, and NCAETS-2019-Hyderabad.
- 3. Subject-expert for JNTUH-Staff Selection Committees for several reputed Colleges in and around Hyderabad
- 4. Resource person for several technical events of HEIs, in AP & Telangana.
- 5. Paper setter for B.Tech., M.Tech., and Ph.D. programs of various reputed autonomous institutions which include JNTUH, K L University, CVR, SNIST, KITS, GRIET, MREC, MRCET, GNITC, and CMRIT.
- 6. Reviewer and Examiner for Ph.D. thesis (ref code:KITS/CoE/Ph.D./R-384/2019) of Karunya Institute of Technology and Sciences (Deemed to be University), Coimbatore, TN.
- 7. Speaker : for 24th World Education Summit, 27-28 July 2022 organized by Elastin Hotel LeMeridian, Hyderabad.

Media Exposure:

1. Participated in the live interaction on the topic "how to select a branch and college", for engineering aspirants under career guidance in various TV channels as follows.

- I. SAKSHI-TV-July2012
- II. CVR(Eng)-August 2020
- III. HMTV-September 2021
- IV. 99-TV-September2021
- 2. Several activities organized were published in leading English and Telugu newspapers which include The Hindu, EENADU, SAKSHI, SIASAT and Namaste Telangana respectively.

Achievements as a visionary Leader :

As the Principal of CBIT, I have successfully demonstrated visionary leadership and strategic acumen, achieving significant milestones, by sustaining its brand image. The key outcomes of my tenure include:

- Revamped curriculum to align with industry standards, resulting in higher employability and academic achievements.
- Introduced innovative teaching-learning methodologies and ensured faculty development, leading to consistent academic rankings and student performance.
- ✓ Spearheaded initiatives that doubled the institution's research output through enhanced support for faculty and students.
- Secured funding from national and international agencies for cutting-edge research and innovation projects.
- ✓ Fostered a collaborative research environment, forming partnerships with leading industries and academic institutions globally.
- Established a robust entrepreneurial ecosystem, leading to a higher entrepreneurial ratio compared to enrolment.
- ✓ Mentored several startups, some of which have gained recognition for their innovative solutions.
- ✓ Positioned CBIT as a hub for innovation, earning accolades at regional and national levels.
- ✓ Focused on skill development, internships, and live projects to make students globally competitive.
- ✓ Promoted extracurricular and co-curricular activities for all-rounded personality development.
- Successfully improved infrastructure and resource allocation to support academic and non-academic initiatives.
- Strengthened stakeholder trust through transparent governance and result-oriented leadership.
- Positioned CBIT as a leader in startup culture and research among peer institutions.
- Secured national and regional awards, including recognition for the Innovation Ecosystem/Achievements under my leadership.
- These accomplishments reflect my commitment to advancing the academic, research, and innovation frontiers of CBIT, showcasing my capability to lead a University towards greater excellence as Vice-Chancellor. My focus remains on fostering academic and societal impact while preparing students to thrive in a global landscape.

Key Achievements at CBIT :

- a. 6 UG & 5 PG Programs on time got accredited by NBA.
 - UG Mechanical Engineering
 - > UG Computer Science & Engineering
 - UG Civil Engineering
 - > UG Electronics & Communication Engineering
 - UG Information Technology
 - UG Chemical Engineering
 - PG CAD/CAM
 - > PG Communication Engineering ODD 0 00
 - PG Power Systems & Power Electronics
 - PG Structural Engineering
 - PG Embedded Systems & VLSI Design () ~
- b. Secured Extension of Autonomous Status by UGC for a period of 10 years from 2023-24 to 2033-34
- c. Rated 4 starts by MoE, Government of India for conducting sufficient IIC Activities.
- d. Ranked under the Band 151-200 in the NIRF Ranking in the year 2024.
- e. Ranked 1st under private self-financing Institutes in Telangana by The WEEK magazine in its June 2024 edition,
- f. Ranked 16th out of top 100 Private Engineering Colleges in the Country by The Week magazine in June, 2024 edition.
- g. Won IEI Engineering Education Excellence Award to CBIT, in recognition of the commendable performance in the category of Engineering Institutions and Colleges for the year 2024.

Key achievements at LORDS :

- a. Instrumental in getting UGC Autonomous Status from 2021-22 to 2031-32.
- b. Accreditation by NBA for Civil Engineering Department.
- c. Extension of Accreditation by NAAC with A-Grade for 5 years from 2021-22

Finance and Accounting Aspects :

As a Principal of CBIT & LIET, a pivotal role in overseeing the institution's financial management and accounting to ensure fiscal responsibility and sustainability. This includes formulating and monitoring budgets, ensuring compliance with financial regulations, and optimizing resource allocation to support academic, research, and infrastructural needs. They are responsible for maintaining transparency and accountability in financial transactions, implementing cost-effective strategies, and exploring diverse funding sources such as grants, endowments, and partnerships. Further, collaborating with financial officers to conduct regular audits, manage risks, and ensure the Institute's financial stability.

New Programs introduced under my guidance :

Courses/Programs	Year of Introduction
New Programs introduced	
B.E. – Electronics Engineering (VLSI Design and Technology)	2024-25
M.Tech Information Technology (Artificial Intelligence and Robotics)	2024-25
New Programs introduced for Working Professionals	
B.E. Computer Science & Engineering	2024-25
B.E. Civil Engineering	2024-25
B.E. Mechanical Engineering	2024-25
M.Tech. Computer Science & Engineering	2024-25
M.E. Embedded System and VLSI Design	2024-25
M.E. Structural Engineering	2024-25

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