



CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY (A)

To
Sri P. Sai Kumar
Scientist 'F',
DRDL, Hyderabad. (Email: sai_drdo@rediffmail.com)

Date : 01-12-2022

Sir,

Sub:-CBIT - Submission of Project Proposal - "Design and Development of Multi-Input Self Diplexing S-band Antennas for a missile application" - Reg

Ref:- Telephonic Conversations and discussions.

Greetings from Chaitanya Bharathi Institute of Technology!!!

With reference to discussions held, we hereby submit the project proposal on "Design and Development of Multi-Input Self Diplexing S-band Antennas for a missile application". The main objective of the project is to develop and design self diplexing dual port and dual frequency antenna for the missile application. The band gap between the two operating frequency band is around 50 MHz. Moreover, in this project, we would reduce the Radar Cross Section of the antenna to make suitable for the missile and defense application. Furthermore, the antenna placement analysis and conformal analysis would be performed in this project.

Our team is capable of delivering the project deliverables on time and the necessary computing facilities shall be provided by the institute

We submit that the budgetary requirements towards the implementation of this project are Rs. 9.8 Lakhs (Including GST). The details of PI and CO-PI are furnished below for your kind perusal.

Principal Investigator

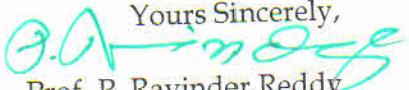
Dr. N. V. Koteswara Rao
Professor, ECE Dept & Director IQAC.

Co-investigator

Dr. Jeet Ghosh
Assistant Professor, ECE Dept.

Kindly consider our proposal and we shall be waiting for your call.

Best Regards

Yours Sincerely,

Prof. P. Ravinder Reddy
PRINCIPAL

Chaitanya Bharathi Institute of Technology (A)

Chaitanya Bharathi (PO), Kokapet (V), Gandipet (M),
Ranga Reddy District, Hyderabad - 500 075, Telangana, India

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CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY (A)

08 Jan 2021

To
Dr.M.Ravindra Kumar
Scientist-G
DLRL, Hyderabad
kumarmvr@yahoo.com

Sir

Sub: Submission of Project Proposal – “Development of Software Modules for Spoofing IRNSS and Beidou Signals” – reg.
Ref: Telephonic Conversations and discussions

Greetings from Chaitanya Bharathi Institute of Technology (CBIT) !!!

With reference to the technical discussions held, we hereby submit the Project Proposal on “Development of Software Modules for Spoofing IRNSS and Beidou Signals”. Dr.A.D.Sarma, Professor, ECE Dept. and Director, R&D, CBIT is the Principal Investigator of the project. The main objective of the project is to develop software modules for spoofing IRNSS and Beidou signals. To fulfill the aim of the project, it is proposed to generate spoofing signals for Beidou and transmit both Beidou and IRNSS spoofed signals. Later, the performance of the developed spoofer application will be evaluated for its efficacy. Also, advanced spoofing detection algorithms for detecting IRNSS and Beidou spoofed signals will be developed. The project is proposed for a period of 30 months with a budget of 49.55 lakhs and four deliverables. The institute shall provide the necessary facilities for carrying out the project and ensure the deliverables as per schedule. Kindly do the needful to provide an opportunity to work on the proposed project.

Yours Sincerely

Dr.P.Ravinder Reddy
Principal

Principal

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CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY (A)

22 Apr 2022

To
Shri.Manish Kumar
Scientist
RCI, Hyderabad
manish.kumar23@rcilab.in

Sir

Sub: Submission of Project Proposal – “Design of Compact Low Loss Rectangular to Cylindrical Iris Power Coupler for W-band Applications” – reg.
Ref: Telephonic Conversations and discussions

Greetings from Chaitanya Bharathi Institute of Technology (CBIT) !!!

With reference to the technical discussions held, we hereby submit the Project Proposal on “Design of Compact Low Loss Rectangular to Cylindrical Iris Power Coupler for W-band Applications”. Dr.Vinodh Kumar Minchula, Associate Professor, ECE Dept. and Research Coordinator is the Principal Investigator of the project. The main objective of the project is to design an iris power coupler between rectangular waveguide and cylindrical cavity to transfer the maximum power from the signal generator source to the load by minimizing the reflections. The project is proposed for a period of 18 months with a budget of 23.88 lakhs and four deliverables. The institute shall provide the necessary facilities for carrying out the project and ensure the deliverables as per schedule. Kindly do the needful to provide an opportunity to work on the proposed project.

Yours Sincerely

Dr.P.Ravinder Reddy
Principal

Principal

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Gandipet, Hyderabad-500 075.

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