

**PG (ES&VLSID) - Student Publications
Summary**

	Book Chapters	International Journals	International Conferences	National Conferences	Total
2024-25	1	-	1	-	2
2023-24	-	1	1	2	4
2022-23	-	-	-	7	7
2021-22	-	1	-	16	17
2020- 21	-	5	1	-	6
2019-20	-	-	2	4	6

AY 24-25

Book Chapters

- Niharika, S. (160122744401)**, Radha, S., Rekha, S., Let, G.S., Nagabushanam, P. (2024). Bulk Isolation Technique for LNA Using 45 nm CMOS Technology. In: Abraham, A., Bajaj, A., Hanne, T. (eds) *Intelligent Systems Design and Applications. ISDA 2023. Lecture Notes in Networks and Systems*, vol 1052. Springer, Cham. https://doi.org/10.1007/978-3-031-64776-5_32.

International Conference

- ABNP Sirisha**, N Dhana Lakshmi, S Siva Priyanka, "Chronic Kidney Disease Stage Prediction Using SGD with ElasticNet Regularization: A Streamlit Application", 24-25 Oct 2024, ICCSC 2024

AY 23-24

International Journals

- Md. Shafi**, Reddy, M. Ramana. "Design and Development of Bomb Detection & Disposal Robot with GPS Location & Live Video Streaming Using Raspberry Pi." *International Journal of Information Technology & Computer Engineering* 12, no. 3. 2024, ISSN 2347-3657.

International Conferences

- Niharika, S.(160122744401)**, Radha, S., Rekha, S., Let, G.S., Nagabushanam, P. (2024). "Bulk Isolation Technique for LNA Using 45 nm CMOS Technology." In *Intelligent Systems Design and Applications. ISDA 2023. Lecture Notes in Networks and Systems*, vol 1052. Springer, Cham. DOI: [10.1007/978-3-031-64776-5_32](https://doi.org/10.1007/978-3-031-64776-5_32), 11-13 December 2023.

National Conference

1. **Shivani D (1601-23-744-402)**, Advanced Agricultural Drone Spraying Technology, R&D Day, CBIT Hyderabad
2. **Movva Laxmi Shivani (1601-23-744-413)**, Motion of an image model using Real-time image animation, R&D Day, CBIT Hyderabad

AY 22-23

National Conference

1. **D.Balram (1601-21-744-406)**, B. Neeraja “An Automatic Driver Drowsiness Alert System Using Alcohol Sensor And Eye Blink Sensor”, Synapse 2023, 31st Jan- 1st feb 2023, organized by CBIT, Hyderabad.
2. **Aesuri Saipriya (1601-21-744-408)**, “Coolant Temperature Meter SWC Development”, Synapse 2023, 31st Jan- 1st feb 2023, organized by CBIT, Hyderabad.
3. **N.Kavyasree, (1601-21-744-403)**, “Design and Implementation Of 32-Bit Alu Using Reversible Logic Gates”, Synapse 2023, 31st Jan- 1st feb 2023, organized by CBIT, Hyderabad.
4. **P.Shirisha (1601-21-744-405)**, “Design Of Multiplexer Using Different Adiabatic Logic In 45nm Technology ”, Synapse 2023, 31st Jan- 1st feb 2023, organized by CBIT, Hyderabad.
5. **Movva Sai Laxmi (1601-21-744-411)**, “CNN Based Lane Detection Using Instance Segmentation”, Synapse 2023, 31st Jan- 1st feb 2023, organized by CBIT, Hyderabad.
6. **Yennati Nagaveni (1601-21-744-410)**, “Satellite Image Resolution Enhancement Using Dwt And Contrast Enhancement Using SVD”, Synapse 2023, 31st Jan- 1st feb 2023, organized by CBIT, Hyderabad.
7. **Pandala.Charishma (1601-21-744-404)**, “Text To Image Synthesis Based On Generative Adversarial Network”, Synapse 2023, 31st Jan- 1st feb 2023, organized by CBIT, Hyderabad.

AY 21-22

International Journals

1. **Kamlikar Sowmya (1601-19-744-402)**, Alenoor Krishna Kumar, Design and Simulation of Area Efficient Carry Select Adder using Verilog HDL, Design Engineering (Toronto), vol no: 2021, Issue no: 7, pp: 7206 - 7216, August - 2021, ISSN: 0011-9342 (Scopus Indexed)

National Conference

1. N. Alivelu Manga,**S. Sai Sandeep Kumar (1601-20-744-407)** , "Design And Simulation of Approximate Multiplier for Low Power Applications", Research Day, CBIT, 28 August 2021.
2. **Chimata Lakshmanaswami (1601-20-744-401)**, Sri. Mohd.Ziauddin Jahangir, "Design Of Digitally Controlled Vco", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
3. **S. Vishnu Vardhan (1601-20-744-402)**, Dr. M. Ramana Reddy, "Design And Verification Of Serial Peripheral Interface (Spi) Protocol", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022

4. **N. Manikanth (1601-20-744-403)**, Dr. M.L. Narasimha Charyulu, "Design And Verification Of Ddr SDRAM Memory Controller Using System Verilog For Higher Coverage", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
5. **S. Swetha (1601-20-744-404)**, Smt. B. Neeraja, "Design And Verification Of 1x3 Router", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
6. **Mohammed Riyazuddin (1601-20-744-405)**, Sri A. Krishna Kumar, "Uvm Based Verification Of Dmac With Advanced Linked List Features", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
7. **S. Sai Sandeep Kumar (1601-20-744-407)**, Dr. N. Alivelu Manga, "Uvm Based Verification Of I3c", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
8. **P. Nazima (1601-20-744-408)**, Smt. D. Sony, "Implementation Of Bird Monitoring System", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
9. **G. Sai Koushik (1601-20-744-409)**, Sri. E. Chandrasekhar, "High-Speed Area-Efficient Vlsi Architecture Of Three-Operand Binary Adder", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
10. **F. Florance (1601-20-744-410)**, Dr. K. Sudharshan Reddy, "Implementation Of Automated Health Monitoring System On Arm Processor", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
11. **Y. Nandini (1601-20-744-411)**, Sri. T. Sreedhar, M. Sandeep Kumar, "Implementation Of Hardware Module Of Iot Based Infant Monitoring System", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
12. **K. Krishna Vamshi (1601-20-744-412)**, Sri. G. Mallikharjuna Rao, "Hardware Implementation And Vlsi Realisation Of Ecc Cryptosystem For Security", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
13. **Susrutha Reddy Kilaru (1601-20-744-413)**, Sri. N. Jagan Mohan Reddy, "Logic Synthesis Using Front End Design Flow And Improve Correlation With Placement And Route", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
14. **G. Sameera (1601-20-744-406)**, Smt. N. Dhanalakshmi, "Biometric Locker Security System", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24th march, 2022

AY 20 - 21

International Journals

1. **Sarvade Venkata Manogna (1601-18-744-401)**, P. Chandra Sekhar, "Vertical-Horizontal Binary Common Sub-Expression Elimination based FIR Filter using XOR and MUX based Adder for DWT Applications", IJMTE, Vol: X, Issue VII, Jul-20, ISSN: 2249-7455
2. **K. Navya Sree (1601-18-744-412)**, G. Mallikharjuna Rao, "IoT Based Low Cost and Ageing Healthcare Monitoring System", International Journal of Engineering and Advanced Technology (IJEAT), ISSN: 2249-8958, Volume-9 Issue-6, August 2020, DOI: 10.35940/ijeat.F1192.089620.
3. **G. Sai Teja (1601-18-744-411)**, P. Sathish, N. Alivelu Manga (2020) "Development of Voice Assisted Application for Real-Time Refrigerator Door control", Journal of Xidian University,

VOLUME 14, ISSUE 7, July 2020, ISSN No:1001-2400.

4. Sathish Pasika, **Sai Teja Gandla(1601-18-744-411)** “ Smart water quality monitoring system with cost effective using IoT” Heliyon Journal Elsevier Publication e04096, ISSN: 2405-8440, Vol:6 Issue:7, July-2020
5. **G. Sai Teja(1601-18-744-411)**, P. Sathish “MQTT Protocol based Smart Greenhouse Environment Monitoring System using Machine Learning” International Journal of Innovative Technology and Exploring Engineering, ISSN: 2278-3075, Vol:9 Issue:9, July-2020.
6. **M. Udaya (1601-18-744-402)**, D. Sony, Dr.D. Krishna Reddy, "Design of IRNSS Tracking System using 1.5 bit ADPLL and Correlator", International Journal of Innovative Technology and Exploring Engineering (IJITEE) ISSN: 2278-3075, Volume-9, Issue-10, August 2020.

International Conference

1. **Varala Pasula Nikhila** , N. Alivelu Manga, “Design and Analysis of Approximate Lower-Part OR Adder for Low Power Computations”, 1st International Conference on Emerging Trends in Circuit-branch Technologies and Applications (ETCTA-2021), CBIT (A), 3-4 April 2021.

AY 19-20

International Conference

1. **S. Greeshmasai**, N. Alivelu Manga, P. Chandra Sekhar, “Design and Simulation of FinFET based digital circuits for low power applications”, 2020 IEEE International Students' Conference on Electrical, Electronics and Computer Science (SCEECS), DOI: 10.1109/SCEECS48394.2020.123, 22-23 Feb. 2020.
2. Neeraja Bandi, **Sathish kumar meda**, "Multiply-Accumulate unit for Binary Arithmetic using high speed adders and PIPO Accumulator", published in Scopus indexed Second International Conference on Computing, Communication and Energy Systems 2020 (ICCCES 2020), Perinthalmanna, Kerala, India.

National Conference

1. **Gandla Sai Teja (1601-18-744-411)**, IoT based Smart and Cost-Effective Water Quality Monitoring System, Synapse 2k19, 13-14 Sep 2019, organized by CBIT, Hyderabad.
2. **K.Harshitha**, Design of energy efficient ALU using 45nm technology, Synapse 2k19, 13-14 Sep 2019, organized by CBIT, Hyderabad.
3. **K.Navya Sree**, IoT based Patient Health Monitoring System, Synapse 2k19, 13-14 Sep 2019, organized by CBIT, Hyderabad.

4. **M. Sathish Kumar**, Design of Multiply-Accumulate Unit using High Speed Multiplier, Synapse 2k19, 13-14 Sep 2019, organized by CBIT, Hyderabad.
5. **M. Udaya (1601-18-744-402)**, Design of single precision floating point vector processor, Synapse 2k19, 13-14 Sep 2019, organized by CBIT, Hyderabad.