



CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY

An Autonomous Institute | Affiliated to Osmania University
Kokapet Village, Gandipet Mandal, Hyderabad, Telangana-500075, www.cbit.ac.in



COMMITTED TO
RESEARCH,
INNOVATION AND
EDUCATION

45

years



IETE STUDENT FORUM

Organizes

Two-Day Workshop on

VLSI DESIGN-Learn & Build with Cadence

30th and 31st August, 2024

(Offline Mode)

Ref.no CBIT/ECE/IETE/WS/01/2024

Chief Patron

Sri. N. Subhash
President, CBIT

Patron

Prof. C. V. Narasimhulu(FIETE)
Principal, CBIT

Technical Advisor

Prof. A. D. Sarma(FIETE)
Advisor, R&D, CBIT
Prof. N. V. Koteswara Rao(FIETE)
Director-IQAC
Prof. D. Krishna Reddy(FIETE)

Convener

Dr. K. Vasanth(FIETE)
Head Dept of ECE

Resource Persons

Dr. Anjali Priya, Asst. Professor, UOH
Dr. B. Neeraja, Asst. Professor, CBIT
Sri. P. Chandrasekhar, Asst. Professor, CBIT
Sri. E.Chandrasekhar, Asst. Professor, CBIT

Faculty Coordinators (IETE-ISF)

Dr. B. Neeraja, Asst. Professor
Dr. T. Sridher, Asst. Professor

Technical Lab Incharge

Mr. M. Krishna, Technician Grade - II,
ES & VLSIDs Lab

Student Coordinators (IETE-ISF)

Mr. Udith Pulijala, Chairperson
Ms. Aditi Battu, Vice Chairperson

Event Organizers (IETE-ISF)

Mr. R Rohith
Ms. S Dharani
Ms. Bhavya Sree
Ms. Navya Vatturi

About ECE Department

The Department of ECE offers two undergraduate and two postgraduate programs, namely B.E.(ECE), B.E.(EVL), M.E.(CE) and M.E.(ES&VLSI). The department features 13 well-equipped laboratories, including dedicated research labs and a center of excellence, making significant contributions in electronics and communication.

About IETE

CBIT IETE Student Forum's aim to enhance student's technical and professional skills by organizing programs, lectures, workshops, and visits, fostering idea exchange, and facilitating industry and academic connections. They encourage teamwork, self-reliance, and overall growth in technical proficiency among students.

About the Program

The workshop provides hands-on experience in Analog VLSI design, covering the entire design flow from specifications to simulation using industry-standard tools. It offers practical projects and expert guidance to equip participants with essential skills for advancing in VLSI technology.

About the Speaker

Dr. Anjali Priya, Assistant Professor, CASEST, University of Hyderabad is the resource person. She specializes in Semiconductor Device Modeling and Analog VLSI, with current research focusing on device modeling, simulation, ADCs for biomedical applications, and Power Management ICs.

Registration Fee

Rs.100/- per person

Target Audience

BE ECE, EEE, 2026 passouts

Registration Link

<https://forms.gle/YEZUUmLRzXbgxq2k6>

For further information

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