



One Week
National Level Faculty Development Program on
Tools for Research in Communications and VLSI
Signal Processing for RF Applications
17 September to 23 September 2024

Chief Patron

Sri. N. Subash, President, CBIT

Patron

Prof. C. V. Narasimhulu, Principal, CBIT

Advisory Committee

Prof. P. Ravinder Reddy, Director - R&E Hub

Prof. A. D. Sarma, Advisor – R & D

Prof. N. V. Koteswara Rao, Director -IQAC

Prof. Suresh Pabboju, Director -AEC & CoE

Prof. K. Krishnaveni, Director-Academics

Prof. P.V.R. Ravindra Reddy, Director- SAP

Prof. Umakanta Chaudhury, Advisor – I & I

Dr. N. L. N. Reddy, Advisor –CDC

Convener:

Dr. K. Vasanth, HoD - ECE

Organizing Committee

Coordinators:

Dr. K. Vasanth, Associate Professor, ECE

Dr. S. Radha, Associate Professor, ECE

Co-Coordinator:

Dr. M. Rajkumar Naik, Assistant Professor, ECE

Registration Details:

Registration Fee: Rs. 200/-

Registration Link (Fill details and Pay using QR Code): <https://forms.gle/D6PzGWE9UAA5AA5GA>



Important Dates:

Last date for Registration: **14.09.2024**

FDP dates: **17th Sep to 23rd Sep 2024**

FDP Mode: **Online**

Topics:

- ❖ Research Trends- Signal and Image Processing
- ❖ Python and Matlab for Image Processing
- ❖ NS2 for Communications
- ❖ Cadence for VLSI architectures
- ❖ HFSS for Antenna designs
- ❖ Research Trends in GNSS
- ❖ Research scope- Vehicular Networks
- ❖ MATLAB and Simulink for project implementations
- ❖ APPs in MATLAB - Explore

Objectives:

- To introduce the scope and availability of research resources.
- To familiarize the pros and cons of research.
- To explore the steps in research.

Outcomes:

After completion of the FDP, participants will be able to:

- ✓ Understand the evolution, concepts and basic methods in various research areas.
- ✓ Get insights into various tools for projects in different research areas of electronics.
- ✓ Acquire knowledge on extending research after PhD / graduation.

Address for Communication:

Dr. S. Radha, Ph:9677532662

radhas_ece@cbit.ac.in

About the College

CBIT is one of the premier Engineering Institutes in India, pioneer in Telangana State, which is at idyllic surroundings of Gandipet Lake, Hyderabad. The college offers 12 UG and 10 PG programs. It has been standing as a temple of knowledge for the past 45 years by producing more than 25,000 eminent and skillful graduate engineers, who are successful in their careers, serving all over the globe. The Institute has been accredited by NAAC – UGC with 'A++' Grade and various programs are accredited by NBA – AICTE. The institution is UGC autonomous since 2013-14. Stringent academic standards, industry compliant teaching methodology, research projects from private and public sector organizations and consultancy practice enabled the Institute to establish its identity in the Technical Education and is ranked as one of the best amongst the private engineering colleges.

About the Department

The Department of Electronics and Communication Engineering, CBIT is offering one UG Program in ECE and with an intake of 180 and two PG programs in Communication Engineering and Embedded Systems & VLSI Design. The department has 48 faculty members out of which 35 are PhDs. The department is growing exponentially in terms of R&D and consultancy. The Ongoing Sponsored research projects worth of Rs. 245.01 lakhs were awarded to the faculty by ISRO, DST, AICTE, UGC, DRDO etc. A total of 19 patents were published and 6 patents are granted as on date. The Department is well equipped with all state-of-the-art equipment's, Laboratories & workshops providing all necessary facilities to the students. There are two special industry-oriented labs namely Navigation & Comm. Research Centre (NCRC) Lab and Green Opto Nano Energy (G-1) Lab.

Resource Persons

Dr. K. Vasanth, HoD-ECE, CBIT, Hyd
Dr. D. Jude Hemanth, HoD-ECE, Karunya University, CBE
Dr. M. Senthilarasi, TCE, Madurai
Dr. G.V. Pradeep Kumar, CBIT, Hyd
Dr. K. Sai Krishna, CBIT, Hyd
Sri. P. Chandra Sekhar, CBIT, Hyd
Sri. Mohd. Ziauddin Jahangir, CBIT, Hyd
Dr. G. Shine Let, Karunya University, CBE
Ms. Blessy Annie Flora, Karunya University, CBE
Dr. A. Supraja Reddy, CBIT, Hyd
Dr. M. Vinodh Kumar, CBIT, Hyd
Dr. S. Radha, CBIT, Hyd

Who can attend?

Academicians, Research scholars, Industry personnel, UG/ PG students

Schedule

Date/ Time	Forenoon	Afternoon
17 th Sep 2024	Inaugural.... S1: Research trends in Signal and Image Processing	S2: Python and Matlab for Image Processing
18 th Sep 2024	S3: Introduction to NS2, node creation and analysis	S4: Research Projects using NS2
19 th Sep 2024	S5: Introduction to Cadence, circuit design and symbol creation	S6: Schematic and analysis of VLSI architecture in Cadence
20 th Sep 2024	S7: Introduction to HFSS and research scope	S8: Antenna design and analysis using HFSS
21 st Sep 2024	S9: Research trends in GNSS	S10: Research Scope in Vehicular Networks
23 rd Sep 2024	S11: MATLAB and Simulink for research	S12: MATLAB APPs for projects- Explore Valedictory....



Organized by

**Department of Electronics and Communication Engineering,
Chaitanya Bharathi Institute of Technology (A)
Gandipet, Hyderabad- 500075**