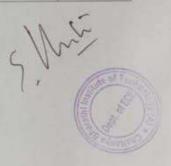
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Chaitanya Bharathi Institute of Technology (A) Department of Electronics and Communication Engineering

1.3.4: Students undertaking Field Projects/Internships INDEX M E ESVLSID

Student Name	Roll No.	Page No.
Sureddy Niharika	160122744401	1-2
V K Vagdhish	160122744403	3-5
Kshieerasagara Rakesh	160122744404	6-7
Md. Shafi	160122744405	8-9
Ramagiri Sreeja	160122744406	10-11
Kuthadi Aruna	160122744408	12-13
Hemalatha Munnuru Kapu	160122744409	14-15
A.B.N.P Shirisha	160122744411	16-17





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We are pleased to confirm the affiliation of SUREDDY NIHARIKA (Roll Number: 1601-22-744-401) of M.E (ES&VLSID) with Chaitanya Bharathi Institute of Technology (CBIT) through participation in the Chip 2 Startup (C2S) projects. During this period, SUREDDY NIHARIKA has actively engaged in learning and applying various advanced tools and technologies, further enhancing their skills and expertise in the field of technology and innovation.

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BONAFIDE CERTIFICATE

This is to certify that Industrial project/dissertation phase I report "DESIGN OF HIGHLY LINEAR, SUB-mW LNA in 45nm CMOS TECHNOLOGY" is the bonafide work of Ms. SUREDDY NIHARIKA bearing Roll No.1601-22-744-401 of M.E(ES&VLSID) who carried out major (Industrial project dissertation phase II) under my supervision during the academic year 2023-2024.

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We are pleased to confirm the affiliation of V.K. VAGDHISH (Roll Number: 1601-22-744-403) of M.E (ES&VLSID) with Chaitanya Bharathi Institute of Technology (CBIT) through participation in the Chip 2 Startup (C2S) projects. During this period, V.K. VAGDHISH has actively engaged in learning and applying various advanced tools and technologies, further enhancing their skills and expertise in the field of technology and innovation.

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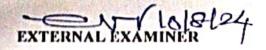
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We are pleased to confirm the affiliation of KSHEERASGAR RAKESH (Roll Number: 1601-22-744-404) of M.E (ES&VLSID) with Chaitanya Bharathi Institute of Technology (CBIT) through participation in the Chip 2 Startup (C2S) projects. During this period, KSHEERASAGAR RAKESII has actively engaged in learning and applying various advanced tools and technologies, further enhancing their skills and expertise in the field of technology and innovation.

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This is to certify that Industrial Project/Dissertation Phase II report "DESIGN OF 64-BIT ALU USING PARALLEL PREFIX ADDER AND CONTROL APPLICATION GUI INTERFACE" is the bonafide work of "KSHEERASAGAR RAKESH" bearing Roll No:1601-22-744-404 of M.E (ES&VLSID) who carriedout major project under my supervision during the academic year 2023-2024.

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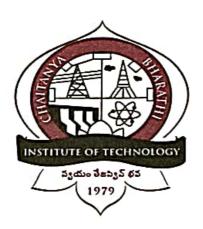
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This is to certify that Industrial Project/Dissertation Phase II report "DESIGN AND DEVELOPMENT OF BOMB DETECTION AND DISPOSAL ROBOT WITH GPS LOACTION & LIVE VIDEO STREAMING USING RASPBERRY PI" is the bonafide work of "MD. SHAFI" bearing Roll No:1601-22-744-405 of M.E (ES&VLSID) who carried out major project under my supervision during the academic year 2023-2024.

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This is to certify that Industrial Project/Dissertation Phase II report "DESIGN AND IMPLEMENTATION OF CML FREQUENCY DIVIDER CIRCUIT "is the bonafide work of "RAMAGIRI SREEJA" bearing Roll No:1601-22-744-406 of M.E (ES&VLSID) who carried out major project under my supervision during the academic year 2023-2024.

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This is to certify that Industrial Project/Dissertation Phase II report "DESIGN AND Implementation OF Time Amplifier Based Time To Digital Converter Using 180nm Technology" is the bonafide work of "KUTHADI ARUNA" bearing Roll No:1601-22-744-408 of M.E (ES&VLSID) who carried out major project under my supervision during the academic year 2023-2024.

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We are pleased to confirm the affiliation of HEMALATHA MUNNURUKAPU (Roll Number: 1601-22-744-409) of M.E (ES&VLSID) with Chaitanya Bharathi Institute of Technology (CBIT) through participation in the Chip 2 Startup (C2S) projects. During this period, HEMALATHA MUNNURUKAPU has actively engaged in learning and applying various advanced tools and technologies, further enhancing their skills and expertise in the field of technology and innovation.

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This is to certify that Industrial Project/Dissertation Phase II report "DESIGN AND IMPLEMENTATION OF 12 BIT TIME INTERLEAVED PIPELINE SAR HYBRID ADC" is the bonafide work of "HEMALATHA MUNNURUKAPU" bearing Roll No:1601-22-744-409 of M.E (ES&VLSID) who carried out major project under my supervision during the academic year 2023-2024.

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We are pleased to confirm the affiliation of A.B.N.P.SIRISHA (Roll Number: 1601-22-744-411) of M.E (ES&VLSID) with Chaitanya Bharathi Institute of Technology (CBIT) through participation in the Chip 2 Startup (C2S) projects. During this period, A.B.N.P.SIRISHA has actively engaged in learning and applying various advanced tools and technologies, further enhancing their skills and expertise in the field of technology and innovation.

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This is to certify that Industrial Project/Dissertation Phase II report "HARDWARE IMPLEMENTATION OF OPTIMIZED PREDICTIVE MODELS FOR EARLY IDENTIFICATION OF CHRONIC KIDNEY DISEASE USING RASPBERRY PI PICO" is the bonafide work of "A.B.N.P.SIRISHA" bearing Roll No:1601-22-744-411 of M.E (ES&VLSID) who carried out major project under my supervision during the academic year 2022-2024.

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