



**CHAITANYA BHARATHI  
INSTITUTE OF TECHNOLOGY**  
An Autonomous Institute | Affiliated to Osmania University  
Kokapet Village, Gandipet Mandal, Hyderabad, Telangana-500075, www.cbti.ac.in



COMMITTED TO  
RESEARCH,  
INNOVATION AND  
EDUCATION

**45**  
years

**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**  
**Stake holder involvement in Curriculum Development**  
**R-22 Regulation**  
**Suggestions Received From Stake Holders**  
**B.E (ECE) AY - 2023-24**

<b>S.No</b>	<b>Name of the Topic</b>	<b>Page No</b>
1	Student Feedback	2-10
2	Faculty Feedback	11-26
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4	Alumni Feedback	46-52
5	Parents Feedback	53-65

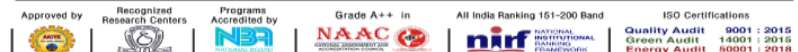
*S. Murthy*






# CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY


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Kokapet Village, Gandipet Mandal, Hyderabad, Telangana-500075, www.cbit.ac.in



## 1) Students

S.No	Name of the expert	Company & Designation	Mobile No Mail ID	Suggestions & opinion	Link/Received Mail details
1	Afiza Bee	4th year ,7 sem Student 	9014668778 ugs204218_ece.afiza@cbit.org.in	<p>1 In Microcontrollers ARM Programming plays major role in Placements. Please include More details of ARM Programming in classwork as well as in lab.</p> <p>2 Java, IWT subjects as professional elective 1 help us more Instead of professional elective 2, please include these subjects in 5<sup>th</sup> sem.</p> <p>3 Subjects like CN has more importance in</p>	<a href="https://drive.google.com/file/d/1oOD-siEHij_m2HXT885bOgkPUozVubtZ/view?usp=sharing">https://drive.google.com/file/d/1oOD-siEHij_m2HXT885bOgkPUozVubtZ/view?usp=sharing</a>

					placements it would be helpful if we learn it in earlier sems rather than in 7th sem .	
2	T.Manasvi	4th year ,7th sem Student	9398526581  manasvithota2002@gmail.com	1	CN subject plays a major role in placements. Therefore including it before 7th sem would be beneficial.	<a href="https://drive.google.com/file/d/1oqr6WbQrxf80i2iYFN7i620eTg3S1o/view?usp=drive_link">https://drive.google.com/file/d/1oqr6WbQrxf80i2iYFN7i620eTg3S1o/view?usp=drive_link</a>
				2	For embedded c programming there has to be a lab along with theory subject.	
				3	For dft elective having a lab would be more beneficial.	
				4	Adding more information about ARM in Microcontrollers	
3	Meghana Konolla	4th year ,7th sem Student	8919561917  meghanakonolla@gmail.com	1	Programming languages helps us a lot in placements instead of only theory there need to be practical knowledge like java and python.	<a href="https://drive.google.com/file/d/1Bty27eCkWQWyYqbTN6yb8aeDA-MEwYiC/view?usp=drive_link">https://drive.google.com/file/d/1Bty27eCkWQWyYqbTN6yb8aeDA-MEwYiC/view?usp=drive_link</a>
4	Yelmula Mounika	4th year ,7th sem Student	9391304484  ugs204237_ece.mounika@cbit.org.in	1	Electives like IWT, Java ,Python should be included in earlier semester.	<a href="https://drive.google.com/file/d/1-NiKDbxegFSmUzEpmHB4SMLtV0SFhLg5/view?usp=drive_link">https://drive.google.com/file/d/1-NiKDbxegFSmUzEpmHB4SMLtV0SFhLg5/view?usp=drive_link</a>
				2	Topics which are useful for placements like CN, OS and Data Structures & Algorithms should be included in the semester.	
				3	Topics like ARM processing in Microcontrollers,	

					Labs include Cadence, Labview should go in depth.	
5	Desabhatla Sriya	4th year, 7th sem Student	8978982660 ugs204241_ece.sriya@cbit.org.in	1	Switch CN (sem 7) with PE-3 (sem 6)	<a href="https://drive.google.com/file/d/1SplgEjU571FwJdDhytJLOBM-e15lhPQJ/view?usp=drive_link">https://drive.google.com/file/d/1SplgEjU571FwJdDhytJLOBM-e15lhPQJ/view?usp=drive_link</a>
				2	Make 5G communications a main subject instead of an elective as it might help everyone to be updated with the new emerging technology	
				3	For the electives which are related to coding like Python, Java, conduct lab classes too instead of just theory classes as it is hard to learn coding only through theory classes and with no explanation and no execution of code	
6	Goli Varshini	4th year ,7th sem Student	9550385113 <a href="mailto:ugs204239_ece.vars_hini@cbit.org.in">ugs204239_ece.vars_hini@cbit.org.in</a> 	1	Programming Languages like Python, Java should Include in earlier semesters rather than in last semesters.	<a href="https://drive.google.com/file/d/1V475lb8EYcCH85mK2y2I7LdSDjOkZLnb/view?usp=drive_link">https://drive.google.com/file/d/1V475lb8EYcCH85mK2y2I7LdSDjOkZLnb/view?usp=drive_link</a>
				2	Subjects like OS should include in semesters as they help in placements.	
				3	Cadence ,Lab View ..these labs are very important for gaining practical knowledge .They should include more topics regarding this.	

12/27/23, 4:07 PM

C.B.I.T Mail - Feedback061



Dr. M. Bhanuchandra Asst. Professor <bhanuchandram\_ece@cbit.ac.in>

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**Feedback061**

1 message

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**061 Afiza bee** <afizabee365@gmail.com>  
To: bhanuchandram\_ece@cbit.ac.in

Thu, Dec 21, 2023 at 11:19 AM

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 **061Feedback.pdf**  
193K

*S. M. K.*  


12/27/23, 4:09 PM

C.B.I.T Mail - Document shared with you: "Stake holders Suggestions R-22.docx"



Dr. M. Bhanuchandra Asst, Professor <bhanuchandram\_eca@cbit.ac.in>

**Document shared with you: "Stake holders Suggestions R-22.docx"**

1 message

**065-Manasvi Thota (via Google Docs)** <drive-shares-dm-noreply@google.com>  
Reply-To: 065-Manasvi Thota <manasvithota2002@gmail.com>  
To: bhanuchandram\_eca@cbit.ac.in

Thu, Dec 21, 2023 at 12:26 PM

**065-Manasvi Thota shared a document**



065-Manasvi Thota (manasvithota2002@gmail.com) has invited you to **edit** the following document:

Stake holders Suggestions R-22.docx

065-Manasvi Thota is outside your organization.

[Open](#)

If you don't want to receive files from this person, [block the sender](#) from Drive

Google LLC, 1600 Amphitheatre Parkway, Mountain View, CA 94043, USA  
You have received this email because manasvithota2002@gmail.com shared a document with you from Google Docs.



12/27/23, 4:11 PM

C.B.I.T Mail - Stake holders Suggestions R-22.docx



Dr. M. Bhanuchandra Asst. Professor <bhanuchandram\_ece@cbit.ac.in>

**Stake holders Suggestions R-22.docx**

1 message

067-Meghana (via Google Docs) <meghanakonolla@gmail.com>  
Reply-To: 067-Meghana <meghanakonolla@gmail.com>  
To: bhanuchandram\_ece@cbit.ac.in

Thu, Dec 21, 2023 at 10:34 PM

067-Meghana attached a document



067-Meghana (meghanakonolla@gmail.com) has attached the following document:

Stake holders Suggestions R-22.docx

Google LLC, 1600 Amphitheatre Parkway, Mountain View, CA 94043, USA  
You have received this email because meghanakonolla@gmail.com shared a document with you from Google Docs.



Stake holders Suggestions R-22.docx.pdf  
117K



12/27/23, 4:10 PM

C.B.I.T Mail - Reg. Stakeholder Feedback



Dr. M. Bhanuchandra Asst. Professor <bhanuchandram\_ece@cbit.ac.in>

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**Reg. Stakeholder Feedback**

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**Yelumula Mounika** <yelumulamounika@gmail.com>  
To: "Dr. M. Bhanuchandra Asst. Professor" <bhanuchandram\_ece@cbit.ac.in>

Thu, Dec 21, 2023 at 7:45 PM

[Quoted text hidden]

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 **068Feedback.pdf**  
647K

*S. Mounika*  




12/27/23, 4:10 PM

C.B.I.T Mail - Reg. Stakeholder Feedback



Dr. M. Bhanuchandra Asst. Professor <bhanuchandram\_ece@cbit.ac.in>

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**Reg. Stakeholder Feedback**

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074- Sriya D <dsriya2020@gmail.com>

Thu, Dec 21, 2023 at 8:26 PM

To: "Dr. M. Bhanuchandra Asst. Professor" <bhanuchandram\_ece@cbit.ac.in>

Name: Desabhatla Sriya  
Roll No: 160120735074

<https://docs.google.com/document/d/1LsHIWIF2jdU0kB6vRwqDTAQYrPruIT42/edit?usp=sharing&ouid=100900670526372785537&rtopf=true&sd=true>

[Quoted text hidden]

*S. Sriya*



12/27/23, 4:09 PM

C.B.I.T Mail - (no subject)



Dr. M. Bhanuchandra Asst, Professor <bhanuchandram\_ece@cbit.ac.in>

(no subject)

1 message

076-Varshini Goli <golivarshini2002@gmail.com>  
To: bhanuchandram\_ece@cbit.ac.in

Thu, Dec 21, 2023 at 2:18 PM

 076Feedback.pdf  
647K





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## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING


Stake holder involvement in Curriculum Development

R-22 Regulation

Suggestions Received From Stake Holders

### 2) Faculty

S.No	Name of the Industry expert	Company & Designation	Mobile No Mail ID	Suggestions & opinion	Link/Received Mail details	
1	Dr M Raj Kumar Naik	CBIT, Assistant Professor (Contract)	9966880273, rajkumarnaik_ece@cbit.ac.in	1	Unit 1 is very lengthy, Data Link Layer concepts need to be shifted to unit 2	<a href="https://drive.google.com/file/d/1ffx3tSP6xwHp5VGZEyiHEkPUICT26RLX/view?usp=sharing">https://drive.google.com/file/d/1ffx3tSP6xwHp5VGZEyiHEkPUICT26RLX/view?usp=sharing</a> 
				2	Unit 4, the Application Layer concepts need to be shifted to unit 5	
				3	In Unit 2, the Data Link Layer switching, Bluetooth is repeated twice. The concept of Zigbee can also be removed as it can be studied in detail in WSN subject.	
<a href="https://drive.google.com/file/d/1ffx3tSP6xwHp5VGZEyiHEkPUICT26RLX/view?usp=sharing">https://drive.google.com/file/d/1ffx3tSP6xwHp5VGZEyiHEkPUICT26RLX/view?usp=sharing</a>						
2	Dr. A. Vani	CBIT, Associate Professor	9182152967 avani_ece@cbit.ac.in	1 COMPUTER NETWORKS i. Security. Overview of threats, cryptography, authentication, and firewalls.	<a href="https://drive.google.com/file/d/1XqHrJn2QwqWb7BaLsAYyndIDuBxnJ_9x/view?usp=sharing">https://drive.google.com/file/d/1XqHrJn2QwqWb7BaLsAYyndIDuBxnJ_9x/view?usp=sharing</a>	

				ii. Network troubleshooting. Hot topics such as SDN and IoT.		
				2 Mobile Cellular Communication i. Industry requirements of 4G and 5G may be included		
<a href="https://drive.google.com/file/d/1XqHrJn2QwqWb7BaLsAYndIDuBxnJ_9x/view?usp=sharing">https://drive.google.com/file/d/1XqHrJn2QwqWb7BaLsAYndIDuBxnJ_9x/view?usp=sharing</a>						
3	Dr. D. Bhasker	CBIT, Assistant Professor	9677240684, <a href="mailto:Bhaskerd_ece@cbit.ac.in">Bhaskerd_ece@cbit.ac.in</a> Bhasker.bvs@gmail.com	1	Suggestion to include a Introductory course on 6G Communication and Networks, it may be suggested in the VI semester. The Indian government's implementation of 5G and preparation for 6G going to create substantial job opportunities in this specialization.	<a href="https://drive.google.com/file/d/1-ZQEJslXUGJu9JCjPg4f78s5tzKu5YXR/view?usp=sharing">https://drive.google.com/file/d/1-ZQEJslXUGJu9JCjPg4f78s5tzKu5YXR/view?usp=sharing</a> 
				2	Recommend incorporating cutting-edge technology subjects, such as Deep Learning and Reinforcement Learning, into the professional electives curriculum.	
				3	A professional elective course focused on "Signal Intelligence Systems (20EC E111), ME Communication Program Elective course " would be highly beneficial for students with an interest in space and defense related studies, equipping them with valuable knowledge and skills to enhance their project pursuits.	
<a href="https://drive.google.com/file/d/1-ZQEJslXUGJu9JCjPg4f78s5tzKu5YXR/view?usp=sharing">https://drive.google.com/file/d/1-ZQEJslXUGJu9JCjPg4f78s5tzKu5YXR/view?usp=sharing</a>						
4	Dr.A.Supraja Reddy	Associate Professor CBIT	9866064120 suprajareddy_ece@cbit.ac.in	1	It is suggested to make room for more core courses.	<a href="https://drive.google.com/file/d/1V4ywZHDLIjHYXqxiG6ph-KM8w3qQoGZ/view?usp=sharing">https://drive.google.com/file/d/1V4ywZHDLIjHYXqxiG6ph-KM8w3qQoGZ/view?usp=sharing</a>
				2	It is advised to look into the need for courses such as "Chemistry" and "Engineering Mechanics" for ECE students. The credits allotted for these subjects can in turn be allocated for more core courses.	

				3	Courses such as “5G Communications”, “IoT and Applications” may be made core courses.	
<a href="https://drive.google.com/file/d/1V4ywZHDLLJiHYXqxig6ph-KM8w3qQoGZ/view?usp=sharing">https://drive.google.com/file/d/1V4ywZHDLLJiHYXqxig6ph-KM8w3qQoGZ/view?usp=sharing</a>						
5	D.Sony	CBIT & Assistant Professor	9700583344 sonyd_ece@cbit.ac.in	1	Regarding computer architecture and microprocessor course to allocate unit 4 and 5 for processors and controllers which is included under unit 5 in curriculum.	<a href="https://drive.google.com/file/d/1iEjOzKG8MSSjTgWIGUVHdDdLdDw9seJUf/view?usp=sharing">https://drive.google.com/file/d/1iEjOzKG8MSSjTgWIGUVHdDdLdDw9seJUf/view?usp=sharing</a>
				2	To introduce simple projects / case studies in all the course as a part of unit 5 wherever possible	
<a href="https://drive.google.com/file/d/1iEjOzKG8MSSjTgWIGUVHdDdLdDw9seJUf/view?usp=sharing">https://drive.google.com/file/d/1iEjOzKG8MSSjTgWIGUVHdDdLdDw9seJUf/view?usp=sharing</a>						
6	Dr. Sai Krishna Kondoju	Chaitanya Bharathi Institute of Technology	9000285206	1	There had been few redundant topics seen in the syllabus offered in different courses.	<a href="https://drive.google.com/file/d/1HHhs2Ot75Js9yJiC9gwSoKQmGWeF0T7a/view?usp=sharing">https://drive.google.com/file/d/1HHhs2Ot75Js9yJiC9gwSoKQmGWeF0T7a/view?usp=sharing</a>
				2	The subjects offered as program elective need to be offered in clusters such as electronics, signal processing, communication systems and advanced topics related to ECE.	
				3	More visualization or Impact on Laboratory courses are required by incorporating advanced experiments.	
<a href="https://drive.google.com/file/d/1HHhs2Ot75Js9yJiC9gwSoKQmGWeF0T7a/view?usp=sharing">https://drive.google.com/file/d/1HHhs2Ot75Js9yJiC9gwSoKQmGWeF0T7a/view?usp=sharing</a>						

*S. Krishna*



**Gmail** Search in mail Active C.B.I.T.

Compose

Inbox 1,573

Starred

Snoozed

Sent

Drafts 16

More

Labels +

- Algorithm for VLSI 6
- covid details- Fan p... 1
- Dept Submissions- goog... 1
- Digital system des... 9
- ES &RTOS 1
- FP - details
- ISDA 2023 Conference
- Journal submissions
- Juni bgn details
- Mentees Details: 2

Stake holders feedback- Faculty

Dr. S. Radha Associate Professor [radhas\\_ece@cbit.ac.in](mailto:radhas_ece@cbit.ac.in) to M

Sun, Jan 21, 10:40PM (16 hours ago)

Dear Dr.M. Rajkumar Naik,

"Greetings from Department of ECE, CBIT"

We are in the process of developing a curriculum for B.E-(ECE)-R-22.In this connection the Department has recognized you as the potential stake holder to give valuable **inputs** for developing curriculum based on your expertise in the field of ECE.

The department **has** implemented R-22 curriculum for semesters from I to IV(AY 2022-2023) & (AY 2023-2024).Now we need your suggestions for finalizing structure and course of semester V<sup>th</sup> to VIII<sup>th</sup> semester to be implement(AY 2024-25 & AY 2025-26).

Suggestion **may** contain deletion and or addition of **any subject and also deletion and or addition of any topics in the subject.**

For your reference we **are** hereby attaching the following existing document for ready reference.

- Existing R- 20 B.E (ECE) Syllabus of V to VIII Semesters
- Proposed R-22 BE (ECE) I<sup>st</sup> to VIII<sup>th</sup> Semesters

Kindly give us your valuable suggestions / Opinion

*S. Radha*



The screenshot shows an email client interface with a sidebar on the left and a main content area on the right. The sidebar includes a 'Compose' button, a list of folders (Inbox: 1,573, Starred, Snoozed, Sent, Drafts: 15, More), and a 'Labels' section with various categories like 'Algorithm for VLSI', 'covid details', 'Dept Submissions', 'Digital system desi...', 'ES & RTOS', 'FP - details', 'ISDA 2023 Conference', 'Journal submissions', and 'Jrnl login details'.

The main content area displays an email titled 'Stake holders feedback- Faculty'. The email is from 'Dr. S.Radha Associate Professor' and is dated 'Sun, Jan 21, 10:40 PM (16 hours ago)'. The body of the email starts with 'Dear Dr.M.Rajkumar Naik, "Greetings from Department of ECE, CBIT" We are in the process of developing a curriculum for B.E-(ECE)-R-22. In this connection the Dep...'. Below this, a reply from 'Dr. M Raj Kumar Naik Asst. Professor' is shown, dated '1:53 PM (1 hour ago)'. A pop-up window displays the email's metadata:

- from: Dr. M Raj Kumar Naik Asst. Professor <rajkumarnaik\_ece@cbit.ac.in>
- to: "Dr. S.Radha Associate Professor" <radhas\_ece@cbit.ac.in>
- date: Jan 22, 2024, 1:53 PM
- subject: Re: Stake holders feedback- Faculty
- mailed-by: cbt.ac.in
- signed-by: cbt-ac-in.20230601.gappsmtmp.com
- security: Standard encryption (TLS) [Learn more](#)

A yellow banner at the bottom of the pop-up reads: 'Important mainly because it was sent directly to you.' Below the email content, there are 'Reply' and 'Forward' buttons.

*S. Radha*

**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**  
**Stakeholder involvement in Curriculum Development**  
**R-22 Regulation**  
**Suggestions Received From Stake Holders**

1) Faculty

S.No	Name of the Industry expert	Company & Designation	Mobile No Mail ID	Suggestions & Remarks	Link/Attached Mail data/ie
1	Dr M Raj Kumar Nair	CDIT, Assistant Professor (Contract)	996680275, rajkumarraj_raj@cdit.ac.in	1 Unit 1 is very lengthy, Data Link Layer concepts need to be shifted to unit 2. 2 Unit 4, the Application Layer concepts need to be shifted to unit 5. 3 In Unit 1, the Data Link Layer switching Bluetooth is repeated twice. The concept of Zigbee can also be removed as it can be studied in detail in WSN subject. 4	Received Mail from Dr S Radha, Associate Professor, Subject: Computer Networks, UTMSS.

*S. Radha*





← <https://mail.google.com/mail/u/0/#inbox/FQRgRdjfHNHmgrNqhDdSNmxKhnfbgWCqnbGimSvIKg> **C.B.I.T.**  
Gmail

Search in mail

Active

99+ Compose

Mail

Chat

Meet

**Inbox** 1,573

Starred

Snoozed

Sent

**Drafts** 15

More

Labels +

- Algorithm for VLSI** 6
- covid details- Fun p...** 1
- Dept Submissions- goog...
- Digital system desi...** 9
- ES & RTOS** 1
- FP - details
- ISDA 2023 Conference
- Journal submissions
- Jrrl login details
- Mentees Details** 2

←

9 of 3,561

**Stake holders feedback- Faculty**

**Dr. S.Radha Associate Professor** <radhas\_ace@cbit.ac.in> to Vani

Sun, Jan 21, 10:36 PM (16 hours ago)

Dear Dr. A.Vani,

“Greetings from Department of ECE, CBIT”

We are in the process of developing a curriculum for **B.E-(ECE)-R-22**. In this connection the Department has recognized you as the potential stake holder to give valuable inputs for developing curriculum based on your expertise in the field of ECE.

The department has implemented R-22 curriculum for semesters from I to IV (AY 2022-2023) & (AY 2023-2024). Now we need your suggestions for finalizing structure and course of semester V<sup>th</sup> to VIII<sup>th</sup> semester to be implement (AY 2024-25 & AY 2025-26).

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**Kindly give us your valuable suggestions / Opinion**

*S. Radha*



https://mail.google.com/mail/u/0/#inbox/FQRgRdJffHNHmgrNqhdD5NmxKhnbfgWQnbgIm5vIKg

**Gmail** Search in mail Active **C.B.I.T.**

99+ Mail **Compose**

Mail **Inbox** 1,573

Chat **Starred**

Meet **Snoozed**

**Sent**

**Drafts** 15

More

**Labels** +

- Algorithm for VLSI 6
- covid details- Fun p... 1
- Dept Submissions- goog... 1
- Digital system desi... 9
- ES & RTOS 1
- FP - details 1
- ISDA 2023 Conference 1
- Journal submissions 1

9 of 3,561

**Dr.A.Vani Associate Professor (Adhoc)** 11:05AM (4 hours ago)

to me

Good morning Madam  
 Thank you madam for giving the opportunity to give faculty feedback on Courses syllabus

1.COMPUTER NETWORKS

- i. Security. Overview of threats, cryptography, authentication, and firewalls.
- ii. Network troubleshooting. Hot topics such as SDN and IoT..

2.Mobile Cellular Communication

- i. Industry requirements of 4G and 5G may be included

\*\*\*

Thanks a lot for sharing. Thank you for your feedback. Thank you for the information.

Reply Forward

*S. Vani*



← ↻ https://mail.google.com/mail/u/0/#inbox/RdDgqcJHpWvcDjPskZMBQnjXsPPwmHHhwdMwNMcsqHL

☰ Gmail Search in mail Active C.B.I.T

99+ Mail

✍ Compose

☑ Mail

☑ Chat

☑ Meet

☑ Inbox 1,573

☆ Starred

🕒 Snoozed

▶ Sent

📄 Drafts 15

∨ More

Labels +

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- ES & RTOS 1
- FP - details 1
- ISDA 2023 Conference 1
- Journal submissions 1
- Jml login details 1
- Mentees Details 2

Stake holders feedback- Faculty Inbox x

Dr. S.Radha Associate Professor <radhas\_ece@cbit.ac.in> to bhasker\_d\_ece

Sun, Jan 21, 10:36 PM (16 hours ago)

Dear Dr.D.Bhaskar,

"Greetings from Department of ECE, CBIT"

We are in the process of developing a curriculum for B.E-(ECE)-R-22.In this connection the Department has recognized you as the potential stake holder to give valuable inputs for developing curriculum based on your expertise in the field of ECE.

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**Kindly give us your valuable suggestions / Opinion**

*S. Radha*



Gmail interface showing an email from Dr. Bhasker Dappuri Assistant Professor, Dept. of ECE. The email subject is "Re: Stake holders feedback- Faculty". A metadata popup is visible over the email content, showing details like "from: Dr. Bhasker Dappuri Assistant Professor, Dept. of ECE", "to: 'Dr. S.Radhe Associate Professor' <radhas\_ace@cbit.ac.in>", "date: Jan 22, 2024, 3:03 PM", "subject: Re: Stake holders feedback- Faculty", "mailed-by: cbit.ac.in", "signed-by: cbit-ac-in.20230601.gappssmtp.com", and "security: Standard encryption (TLS) Learn more". Below the email content, there are three response buttons: "Received, thank you.", "Thanks, I have received it.", and "Thanks a lot.".

*S. Radhe*



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Gmail

in:sent

Active

C.B.I.T

16 of 1817

Stake holders feedback- Faculty

Dr. S.Radha Associate Professor <radhas\_eca@cbit.ac.in>  
to Supraja

Sun, Jan 21, 10:41PM (3 days ago)

Dear Dr. A. Supraja Reddy,

"Greetings from Department of ECE, CBIT"

We are in the process of developing a curriculum for **B.E-(ECE)-R-22**. In this connection the Department has recognized you as the potential stake holder to give valuable inputs for developing curriculum based on your expertise in the field of ECE.

The department has implemented R-22 curriculum for semesters from I to IV (AY 2022-2023) & (AY 2023-2024). Now we need your suggestions for finalizing structure and course of semester V<sup>th</sup> to VIII<sup>th</sup> semester to be implement (AY 2024-25 & AY 2025-26).

Suggestion may contain deletion and or addition of any subject and also deletion and or addition of any topics in the subject.

For your reference we are hereby attaching the following existing document for ready reference.

- Existing R- 20 B E (ECE) Syllabus of V to VIII Semesters
- Proposed R-22 BE (ECE) I<sup>th</sup> to VIII<sup>th</sup> Semesters

Kindly give us your valuable suggestions / Opinion

*S. Radha*



https://mail.google.com/mail/u/0/#inbox/FMfcgzGwJmMSKLGJHwxcWsbgzM/hqTTh

Gmail Search in mail Active C.B.I.T.

Compose

Mail 99+

Chat

Meet

Inbox 1,582

Starred

Snoozed

Sent

Drafts 15

More

Labels +

- Algorithm for VLSI 6
- covid details- Fun p... 1
- Dept Submissions- goog... 1
- Digital system desi... 9
- ES & RTOS 1
- FP - details
- ISDA 2023 Conference
- Journal submissions

Stake holder feedback

Dr.A.Supraja Reddy to me

Macam PFA Best Wishes

Mrs, Jan 22, 4:07 PM (2 days ago)

Dr. Supraja Reddy Ammana  
Associate Professor  
Dept. of ECE  
CBIT, Hyderabad - 500075  
Cell. 9966064120

One attachment • Scanned by Gmail

Dr.ASR\_Stake hol...

*S. Murthy*



← → ↻ <https://mail.google.com/mail/u/0/#inbox/LXphbRLrghxk:JIVKkcVVHzPhdZJkVPDcJhKvFNkmDB> 🔍 Search in mail 📧 Active ? ⚙️ ☰ **C.B.I.T.**  
SCHOOL & HED SERVICES

☰ **Compose**

Mail **Inbox** 1,583

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More

Labels +

- Algorithm for VLSI 6
- covid details- Fun p... 1
- Dept Submissions- goog... 1
- Digital system desi... 9
- ES & RTOS 1
- FP - details
- ISDA 2023 Conference
- Journal submissions

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**Stake holders feedback- Faculty** inbox x ⌵ 🖨️ 📧

**D** **Dr. S.Radha Associate Professor** <radhas\_ece@cbit.ac.in> Sun, Jan 21, 10:46 PM (3 days ago) ☆ ↶ ⋮  
to sonyd\_ece ▾

Dear Smt. D. Sony,

“Greetings from Department of ECE, CBIT”

We are in the process of developing a curriculum for **B.E-(ECE)-R-22**. In this connection the Department has recognized you as the potential stake holder to give valuable inputs for developing curriculum based on your expertise in the field of ECE.

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Suggestion may contain deletion and or addition of any subject and also deletion and or addition of any topics in the subject.

For your reference we are hereby attaching the following existing document for ready reference.

*S. Radha*



Gmail interface showing an email from Sony Devalapally to Mam PFA. The email subject is "Re: Stake holders feedback- Faculty" and includes a scanned attachment titled "Stake holders Su...". The interface includes a search bar, navigation icons, and a sidebar with folders like Compose, Inbox (1,583), Starred, Snoozed, Sent, Drafts, and Labels.

**Header:** Gmail | Search in mail | Active | C.B.I.T. (C.B.I.T. BANGALORE - A WEB SERVICES)

**Navigation:** Compose, Mail, Chat, Meet

**Folder List:**

- Inbox: 1,583
- Starred
- Snoozed
- Sent
- Drafts: 15
- More

**Labels:**

- Algorithm for VLSI: 6
- covid details- Fun p...: 1
- Dept Submissions- goog...: 1
- Digital system desi...: 9
- ES & RTOS: 1
- FP - details: 1
- ISDA 2023 Conference: 1

**Email Details:**

- From:** Sony Devalapally <sonyd\_ece@cbit.ac.in>
- To:** "Dr. S.Radha Associate Professor" <radhas\_ece@cbit.ac.in>
- Date:** Jan 24, 2024, 10:28 AM
- Subject:** Re: Stake holders feedback- Faculty
- Mailed-by:** cbit.ac.in
- Signed-by:** cbit-ac-in.20230601.gappsmtmp.com
- Security:** Standard encryption (TLS) [Learn more](#)
- Importance:** Important mainly because it was sent directly to you.

**Attachment:** One attachment • Scanned by Gmail

**Attachment Preview:** Stake holders Su... (Scanned document with logos and text)

*S. Radha*





https://mail.google.com/mail/u/0/#sent/LXphbRLrghvkrJltvTZDdbpqSbbYbbIGnSGmNLQXngq

Gmail

in:sent

Active

C.B.I.T

15 of 1,817

### Stake holders feedback- Faculty

Dr. S.Radha Associate Professor -rredhas\_ece@cbit.ac.in-  
to Dr.Sai

Sun, Jan 21, 10:44 PM (3 days ago)

Dear Dr. K. Sai Krishna,

"Greetings from Department of ECE, CBIT"

We are in the process of developing a curriculum for **B.E-(ECE)-R-22**. In this connection the Department has recognized you as the potential stake holder to give valuable inputs for developing curriculum based on your expertise in the field of ECE.

The department has implemented R-22 curriculum for semesters from I to IV (AY 2022-2023) & (AY 2023-2024). Now we need your suggestions for finalizing structure and course of semester V<sup>th</sup> to VIII<sup>th</sup> semester to be implement (AY 2024-25 & AY 2025-26).

Suggestion may contain deletion and or addition of any subject and also deletion and or addition of any topics in the subject.

For your reference we are hereby attaching the following existing document for ready reference.

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2. Proposed R-22 BE (ECE) I<sup>st</sup> to VIII<sup>th</sup> Semesters

**Kindly give us your valuable suggestions / Opinion**

*S. Krishna*



https://mail.google.com/mail/u/0/#inbox/FMfcgzGwJmPktgVVzCJmNnNmQNWRTMxB

**Gmail** Search in mail Active **C.B.I.T.**

99+ Mail **Compose**

Mail **Inbox** 1,582

Chat **Starred**

Meet **Snoozed**

**Sent**

**Drafts** 15

**More**

**Labels** +

- Algorithm for VLSI 6
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- Dept Submissions- goog... 1
- Digital system desi... 9
- ES & RTOS 1
- FP - details 1
- ISDA 2023 Conference
- Journal submissions
- Jrnl login details

**Stake holders Suggestions R-22** Inbox x 1 of 1,588

**Dr.Sai Krishna Kondoju ECE** to me 10:52 AM (5 minutes ago)

Dear ...

from: **Dr.Sai Krishna Kondoju ECE** <ksaikrishna\_ece@cbit.ac.in>  
 to: radhas\_ece@cbit.ac.in  
 date: Jan 24, 2024, 10:52 AM  
 subject: Stake holders Suggestions R-22  
 mailed-by: cbit.ac.in  
 signed-by: cbit-ac-in.20230601.gappsmtmp.com  
 security: Standard encryption (TLS) [Learn more](#)  
 Important according to Google magic.

**One attachment** • Scanned by Gmail

Stake holders Su...

*S. Krishna*





# CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY

An Autonomous Institute | Affiliated to Osmania University  
Kokapet Village, Gandipet Mandal, Hyderabad, Telangana-500075, www.cbit.ac.in




## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING


### Stake holder involvement in Curriculum Development


### R-22 Regulation


### Suggestions Received From Stake Holders


#### 3) Industry

S.No	Name of the Industry expert	Company & Designation	Mobile No Mail ID	Suggestions & opinion		Link/Received Mail details
1	Mr. Arunraj	Senior Manager Cognizant  	9632769346 Arunraj.selvaraj@yahoo.com	1	The Concept of PCB designing through Software and practical approach can be incorporated.	<a href="https://drive.google.com/file/d/1Q8OsLzHMgni6tEo2twq1kPnyrLEPZCPs/view?usp=sharing">https://drive.google.com/file/d/1Q8OsLzHMgni6tEo2twq1kPnyrLEPZCPs/view?usp=sharing</a>
				2	The Labs can involve in More design oriented Experiments	
				3	The Subjects offered are voluminous such as cyber security, Robotics etc, Make sure the Topics of the syllabus are offered from the standard Text book.	
				4	I feel that II semester and third semester offer the same subject such as Electromagnetic Theory. Kindly check	
				5	Subjects such as Digital Communication can be integrated with Analog communication as the concepts in DC will be discussed in CMOS, Data Converters, theory and coding	
2	Mr. Hariharan Anantharaman	Technical Manager, Backbase, India	9445566390 Hariharan.anantharaman@gmail.com	1	The Choice of Subjects in Elective can be rearranged into various domains such as Embedded System Sensors and Actuators RTOS Embedded C Programming Please see if the same can be done for other areas like Navigation, advanced technologies	<a href="https://drive.google.com/file/d/1id5Utb1phEeIy0sULVMotbuPt3n0IBHc/view?usp=sharing">https://drive.google.com/file/d/1id5Utb1phEeIy0sULVMotbuPt3n0IBHc/view?usp=sharing</a>
				2	If Electives are distributed then what happens to students who opt for other electives without any prerequisite. Will	

					they be able to understand the concepts clearly? How are we going to address it?	
				3	Ensure students does not miss out basic subjects while selecting electives. Electives can be clustered and Offered as per students preference and market demands.	
				4	Can Electronic CAD in first year as part of First year engineering workshop	
				5	Subjects such as DSD and Digital integrated circuits are Redundant. Please check.	
3	Mr.S.Kesavan,	Junior Telecom Officer, BSNL	9486101811 kesavanjeys@gmail.com	1	Is V semester not overloaded? Seems more number of Subjects offered ? Kindly check for possible change	
				2	Subject such as Microprocessor can be clubbed with Microcontrollers so that students will have enhanced idea on the differentiation between processors and controllers	
				3	CMOS Data Converters subject is also Redundant. Most of the concepts covered in LDICA	
				4	Computer networks theory/lab can be Data communication theory/ lab is offered then scripting languages can also be an Elective. If Possible kindly accommodate. At Least in the lab. The technology world has advanced to AI. It needs some scripting techniques.	
				5	Data Structures through Python can be done rather than C or C++ Programming. ( python is a free source which is openly available which help them ) all my opinions / suggestions are industry based and the future change over of industries .	
4	Mr. Mahendra Prabu Subramani	Analyst 4, Western Digital	810 5233954 Mahendra.Subramani@wdc.com	1	Principles of AI, Cloud Computing Can be introduced under Open Electives as students does not required to have and Pre-requisites.	<a href="https://drive.google.com/file/d/1KgeqHH43UWX190N1QoCXeYvrbX26WAI5/view?usp=sharing">https://drive.google.com/file/d/1KgeqHH43UWX190N1QoCXeYvrbX26WAI5/view?usp=sharing</a>
				2	Packet tracer software can be introduced in Data Communication Lab. Few experiments can be included.	
				3	We can have few experiments in Antenna Lab using many open source software such as 4nec2? If syllabus does not accommodate then used it for teaching the Subject. Students can understand principles of radiating element very easily. Atleast Antennas lab can be part of Microwave lab If possible.	

				4	Numerical Methods subject is missing as part of the Engineering Mathematics	
				5	Syllabus can also have FRDM Controllers as an alternative	
5	Mr. Rengaraja	HW SoC Micro-Architect Intel, Malaysia  	9566107770 <a href="mailto:rengaraja.sudarnani@intel.com">rengaraja.sudarnani@intel.com</a>	1	<b>COMPUTER ARCHITECTURE AND MICROPROCESSORS-</b> Concept on Memory centric architecture, CPU centric Architecture vs Memory centric architecture	<a href="https://drive.google.com/file/d/1HsSDzcji8l6Qk7PqLw5xG9qdvASfPxDt/view?usp=sharing">https://drive.google.com/file/d/1HsSDzcji8l6Qk7PqLw5xG9qdvASfPxDt/view?usp=sharing</a>
				2	<b>CAD for VLSI verification-</b> Cover How the ASIC health is checked once out from Fabrication (ATE silicon test, Yield Shmoo graph)	
				3	<b>FUNDAMENTALS OF CLOUD COMPUTING-</b> data reliability- RAS(Reliability Availability and Serviceability)	
				4	<b>VLSI DESIGN-</b> RTL to GDS flow	
				5	<b>CPLD AND FPGA ARCHITECTURES</b> Clock and memory generation for FPGA <b>DESIGN FOR TESTABILITY</b> Cover on Design For Debug (DFD) <b>ADVANCED MICROPROCESSORS AND APPLICATIONS</b> Interrupt handling <b>VLSI TECHNOLOGY</b> Semiconductor Materials and Physics - Understand the materials used for fabrication and it's characteristics	
6	Ms.Macharla Mounika	Embedded Engineer Dura Automotive Services(I) pvt.ltd	Mobile no: +91 9133785929 <a href="mailto:macharlamounika85@gmail.com">macharlamounika85@gmail.com</a>	1	Find an Alternate so that Computer architecture, Microprocessor and Microcontrollers are offered as two different subjects.	<a href="https://drive.google.com/file/d/1fD57LiGDVeSfCa8KJLYwi344jKWdoes/view?usp=sharing">https://drive.google.com/file/d/1fD57LiGDVeSfCa8KJLYwi344jKWdoes/view?usp=sharing</a>
				2	Neural Networks or Fundamentals of Neural Networks Subject can be offered. Students will gain good knowledge in Machine learning and Deep learning Subjects	
				3	Syllabus is good. Lot of advanced subjects is included. Kindly look into the flow of Electives it look odd.	

				4	Kindly include Python, Java programming in III semester for the purpose to strengthen the students for IT jobs	
				5	VLSI Subjects flow needed to be checked. Also subjects seems voluminous in VLSI based subjects. If possible Introduce basics in those subjects	
				6	PTSP subject is missing in the III Semester. How does students learn processing of Random Signals? Kindly accommodate in few places	
7	Ms. Vaishnavi Konnoju	Network Consulting Engineer, Cisco	9704534844 <a href="mailto:konnojuvaishnavi99@gmail.com">konnojuvaishnavi99@gmail.com</a>	1	Digital communication theory and lab can be merged with Analog Communication. Advanced experiments can be offered through a Matlab Lab as advanced communication lab. Engineering is all about Learning by doing. Faculty can frame syllabus related to 3G,4G,5G experiments or few techniques related to it.	<a href="https://drive.google.com/file/d/1XtWFZd47KidzaTLwqEsqh4RPrL3-2Olh/view?usp=sharing">https://drive.google.com/file/d/1XtWFZd47KidzaTLwqEsqh4RPrL3-2Olh/view?usp=sharing</a>
				2	Semester 1 students learning subjects called Robotics and Drones as Lab. But theory is offered later? Is it correlatable? May be look into the possibility of Reshuffling	
				3	Basic Electrical Engineering and Network theory can be merged to offer a new Circuit based subject for ECE students if possible. It might lead to a good space for other basic subjects.	
				4	Syllabus seems good but keep GATE, MS syllabus in Mind. More advanced subjects might eliminate basic knowledge is the intended domain.	
				5	Subject Such as Computer networks can be reconed as data communication and corresponding Lab can be offered? That may be suitable for ECE Domain	
8.	Dr.Pogula Rakesh	Audio AI Engineer at Meeami Technologies, Inc.		rakesh.pogula@meeamitech.com 9492674292	1	The program seems to be proactive in adapting to emerging technologies, with graduates showcasing a good understanding of the latest advancements in IoT, 5G, and AI.
				2	The program effectively covers sensor networks, communication protocols, and connectivity aspects relevant to IoT. Graduates are well-versed in choosing and implementing suitable communication protocols.	

				3	The program effectively covers antenna design principles and propagation characteristics relevant to 5G, with graduates demonstrating competence in designing and optimizing antennas for 5G applications.	
				4	The program's emphasis on practical projects provides graduates with valuable hands-on experience, allowing them to apply speech signal processing concepts to real-world scenarios.	
				5	The Program gives proficiency in applying deep learning techniques to speech processing, reflecting the industry's shift toward neural network-based models.	
9.	Muhibullah Shah Pahelwan	Technical Leader @ Nokia   Mobile Networks   Software Solutions   Debuggers	<a href="mailto:muhibullah.pahelwan@nokia.com">muhibullah.pahelwan@nokia.com</a> 9886841642	1	<b>Principles and Applications of AI</b> It would be helpful for the students when they participate in the basic trainings available/provided on/by Google. Though this suggestion is in contradictory to class room trainings, but such steps will help students in their career. Such trainings can be labeled as 'Homework' or 'Personal development Plan' for the students. For example, <a href="https://ai.google/build?featured=learn_gen_ai">https://ai.google/build?featured=learn_gen_ai</a>	
				2	<b>Fundamentals of Cloud Computing:</b> I see, there's no lab planned for Dockers and Containers concepts in Cloud computing. These are important topics to be covered under lab or class room.	
				3	<b>General:</b> Primary focus on AI, ML, 5G as every industry is focusing on these for near future. Make students to focus on Coding Logic and developing domain knowledge in each topics. The Analytical skills are mandatory now a days, encourage students in this aspects. Considering the advances in technology, ChatGPT, Gemini etc tools are available which can do coding on a tip of a finger, but they do lack Domain expertise that	

					a	human	brain	can	do.	
--	--	--	--	--	---	-------	-------	-----	-----	--

*S. N. S.*





**Syllabus revision - Feedback on existing curriculum - CBIT - Reg**

Dr. K.Vasanth Associate Prof., ECE <vasanthk\_ece@cbit.ac.in>  
To: Arunraj.selvaraj@yahoo.com

Sat, Dec 9, 2023 at 2:29 PM

Dear

Mr. Arunraj

Senior Manager

Cognizant

9632769346

[Arunraj.selvaraj@yahoo.com](mailto:Arunraj.selvaraj@yahoo.com)

“Greetings from Department of ECE, CBIT”

We are in the process of developing a curriculum for **B.E-(ECE)-R-22**. In this connection the Department has recognized you as the potential stake holder to give valuable inputs for developing curriculum based on your expertise in the field of ECE.

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Suggestion may contain deletion and or addition of any subject and also deletion and or addition of any topics in the subject.

For your reference we are hereby attaching the following existing document for ready reference.

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2. Proposed R-22 BE (ECE) I<sup>th</sup> to VIII<sup>th</sup> Semesters

**Kindly give us your valuable suggestions / Opinion**

Your suggestions are helpful in designing the good curriculum

Regards

**Dr.K.Vasanth**



---

**Request for Suggestions in Syllabus ECE Dept - CBIT - Reg**

---

**Arunraj** <nurasin@gmail.com>

Thu, Dec 21, 2023 at 5:04 PM

To: "Dr. K.Vasanth Associate Prof., ECE" <vasanthk\_ece@cbit.ac.in>

Dear Dr.K.Vasanth

The Structure of the syllabus is well balanced. Though I have suggestions to make

1. The Concept of PCB designing through Software and practical approach can be incorporated.
2. The Labs can involve in More design oriented Experiments
3. The Subjects offered are voluminous such as cyber security, Robotics etc, Make sure the Topics of the syllabus are offered from the standard Text book.
4. I feel that II semester and third semester offer the same subject such as Electromagnetic Theory. Kindly check
5. Subjects such as Digital Communication can be integrated with Analog communication as the concepts in DC will be discussed in CMOS, Data Converters, theory and coding

Thank You

Arunraj.S

[Quoted text hidden]


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**Invitation for Syllabus Revision - ECE - CBIT - Reg**

---

Dr. K.Vasanth Associate Prof., ECE <vasanthk\_ece@cbit.ac.in>  
To: Hariharan Anantharaman <hariharan.anantharaman@gmail.com>

Fri, Dec 8, 2023 at 8:46 PM

Dear Hariharan Anantharaman,  
Technical Manager,  
Backbase, India

“Greetings from Department of ECE, CBIT”

We are in the process of developing a curriculum for **B.E-(ECE)-R-22**. In this connection the Department has recognized you as the potential stake holder to give valuable inputs for developing curriculum based on your expertise in the field of ECE.

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**Kindly give us your valuable suggestions / Opinion**

Your suggestions are helpful in designing the good curriculum

Regards

**Dr.K.Vasanth**

**Associate Professor**





Dr. K.Vasanth Associate Prof., ECE &lt;vasanthk\_ece@cbit.ac.in&gt;

---

**Invitation for Syllabus Revision - ECE - CBIT - Reg**

---

**Hariharan Anantharaman** <hariharan.anantharaman@gmail.com>  
To: "Dr. K.Vasanth Associate Prof., ECE" <vasanthk\_ece@cbit.ac.in>

Wed, Dec 27, 2023 at 7:31 AM

Hello Sir,

I had a look at the syllabus. While it is good overall, below are a few suggestions that I can think of. Please do the needful

1. The Choice of Subjects in Elective can be rearranged into various domains such as

- Embedded System
- Sensors and Actuators
- RTOS
- Embedded C Programming

Please see if the same can be done for other areas like Navigation, advanced technologies and VLSI

2. If Electives are distributed then what happens to students who opt for other electives without any prerequisite. Will they be able to understand the concepts clearly? How are we going to address it?

3. Ensure students does not miss out basic subjects while selecting electives. Electives can be clustered and Offered as per students preference and market demands.

4. Can Electronic CAD in first year as part of First year engineering workshop

5. Subjects such as DSD and Digital integrated circuits are Redundant. Please check.

Wishing the students and faculties all the very best.

Thanks

Hari

[Quoted text hidden]

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Thanks and Regards

Hariharan Anantharaman



**Syllabus Revision - Requesting your feedback - CBIT - Regarding**

2 messages

**Dr. K.Vasanth Associate Prof., ECE** <vasanthk\_ece@cbit.ac.in>  
To: Mahendra.Subramani@wdc.com

Sat, Dec 9, 2023 at 8:02 PM

Dear

Mahendra Prabu Subramani

Analyst 4,

Western Digital

810 5233954

[Mahendra.Subramani@wdc.com](mailto:Mahendra.Subramani@wdc.com)

Personal email: [Mahendra.prabu@gmail.com](mailto:Mahendra.prabu@gmail.com) "Greetings from Department of ECE, CBIT"

We are in the process of developing a curriculum for **B.E-(ECE)-R-22**. In this connection the Department has recognized you as the potential stake holder to give valuable inputs for developing curriculum based on your expertise in the field of ECE.

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**Kindly give us your valuable suggestions / Opinion**

Your suggestions are helpful in designing the good curriculum





Regards

**Dr.K.Vasanth**  
Associate Professor



---

**4 attachments**

-  **Mr.Mahendra Prabhu.docx**  
14K
-  **Stake holders Suggestions R-22.docx**  
99K
-  **R 22 BE (ECE) I to VIII Semesters.docx**  
225K
-  **R 20 BE(ECE) Syllabus of V to VIII Semesters.docx**  
650K

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**Mahendraprabu Subramani** <Mahendra.Subramani@wdc.com>  
To: "Dr. K.Vasanth Associate Prof., ECE" <vasanthk\_ece@cbit.ac.in>

Tue, Dec 26, 2023 at 9:23 AM

Dear Vasanth,

Here are some suggestions:

1. Principles of AI, Cloud Computing Can be introduced under Open Electives as students does not required to have and Pre-requisites.
2. Packet tracer software can be introduced in Data Communication Lab. Few experiments can be included.
3. We can have few experiments in Antenna Lab using many open source software such as 4nec2? If syllabus does not accommodate then used it for teaching the Subject. Students can understand principles of radiating element very easily. Atleast Antennas lab can be part of Microwave lab If possible.
4. Numerical Methods subject is missing as part of the Engineering Mathematics
5. Syllabus can also have FRDM Controllers as an alternative

Thanks and Regards

Mahendraprabu

Analyst 4,

Western Digital

Bengaluru



**Syllabus Feedback - Your views to enhance the quality of the subject - CBIT - Reg**

Dr. K.Vasanth Associate Prof., ECE <vasanthk\_ece@cbit.ac.in>  
To: rengaraja.sudarmani@intel.com

Wed, Dec 13, 2023 at 5:49 PM

Dear Rengaraja  
HW SoC Micro Architect  
Intel  
9566107770  
[rengaraja.sudarmani@intel.com](mailto:rengaraja.sudarmani@intel.com)

“Greetings from Department of ECE, CBIT”

We are in the process of developing a curriculum for **B.E-(ECE)-R-22**. In this connection the Department has recognized you as the potential stake holder to give valuable inputs for developing curriculum based on your expertise in the field of ECE.

The department has implemented R-22 curriculum for semesters from I to IV (AY 2022-2023) & (AY 2023-2024). Now we need your suggestions for finalizing structure and course of semester V<sup>th</sup> to VIII<sup>th</sup> semester to be implemented (AY 2024-25 & AY 2025-26).

Suggestion may contain deletion and or addition of any subject and also deletion and or addition of any topics in the subject.

For your reference we are hereby attaching the following existing document for ready reference.

1. Existing R- 20 B.E (ECE) Syllabus of V to VIII Semesters
2. Proposed R-22 BE (ECE) I<sup>th</sup> to VIII<sup>th</sup> Semesters

**Kindly give us your valuable suggestions / Opinion**

Your suggestions are helpful in designing the good curriculum

Regards

**Dr.K.Vasanth**  
Associate Professor



---

**Syllabus Feedback - Your views to enhance the quality of the subject - CBIT - Reg**

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Sudarmani, Rengaraja <rengaraja.sudarmani@intel.com>  
To: "Dr. K.Vasanth Associate Prof., ECE" <vasanthk\_ece@cbit.ac.in>  
Cc: "Sudarmani, Rengaraja" <rengaraja.sudarmani@intel.com>

Wed, Dec 27, 2023 at 7:34 AM

Hi Dr.Vasanth,

Please find my feedback below, I focused on the VLSI related course and suggested more topics just as hint, Hope it's fine for you if need more details please let me know.

**COMPUTER ARCHITECTURE AND MICROPROCESSORS**

Concept on Memory centric architecture

CPU centric Architecture vs Memory centric architecture

**CAD for VLSI verification**

Cover How the ASIC health is checked once out from Fabrication (ATE silicon test, Yield Shmoo graph)

**FUNDAMENTALS OF CLOUD COMPUTING**

data reliability- RAS(Reliability Availability and Serviceability)

**VLSI DESIGN**

RTL to GDS flow

**CPLD AND FPGA ARCHITECTURES**

Clock and memory generation for FPGA

**DESIGN FOR TESTABILITY**

Cover on Design For Debug (DFD)

*S. Vasanth*  




**Syllabus Revision - Reviews by you - CBIT - Reg**

Dr. K.Vasanth Associate Prof., ECE <vasanthk\_ece@cbit.ac.in>  
To: macharlamounika85@gmail.com

Wed, Dec 13, 2023 at 5:59 PM

Dear Macharla Mounika

Embedded Engineer

Dura Automotive Services(I) pvt.ltd

Mobile no: +91 9133785929

Mail Id: [macharlamounika85@gmail.com](mailto:macharlamounika85@gmail.com)

“Greetings from Department of ECE, CBIT”

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**Kindly give us your valuable suggestions / Opinion**

Your suggestions are helpful in designing the good curriculum

**Regards**

**Dr.K.Vasanth**

**Associate Professor**



**Syllabus Revision - Reviews by you - CBIT - Reg**

**Macharla Mounika** <macharlamounika85@gmail.com>  
To: "Dr. K.Vasanth Associate Prof., ECE" <vasanthk\_ece@cbit.ac.in>

Tue, Dec 26, 2023 at 8:52 PM

Dear Sir,

I found a few observations in the specified curriculum, please find the attachment below.

Thanks and regards,  
M. Mounika

On Wed, Dec 13, 2023 at 1:24 PM Dr. K.Vasanth Associate Prof., ECE &lt;vasanthk\_ece@cbit.ac.in&gt; wrote:

Dear Macharla Mounika

Embedded Engineer

Dura Automotive Services(I) pvt.ltd

Mobile no: +91 9133785929

Mail Id: [macharlamounika85@gmail.com](mailto:macharlamounika85@gmail.com)

"Greetings from Department of ECE, CBIT"

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Suggestion may contain deletion and or addition of any subject and also deletion and or addition of any topics in the subject.

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**Feed back to be made for Curriculum revision - ECE - CBIT- Reg**

2 messages

Dr. K.Vasanth Associate Prof., ECE <vasanthk\_ece@cbit.ac.in>  
To: konnoju vaishnavi <konnojuvaishnavi99@gmail.com>

Sat, Dec 16, 2023 at 11:45 AM

Dear Vaishnavi Konnoju

Network Consulting Engineer

9704534844

[konnojuvaishnavi99@gmail.com](mailto:konnojuvaishnavi99@gmail.com)

"Greetings from Department of ECE, CBIT"

We are in the process of developing a curriculum for **B.E-(ECE)-R-22**. In this connection the Department has recognized you as the potential stake holder to give valuable inputs for developing curriculum based on your expertise in the field of ECE.

The department has implemented R-22 curriculum for semesters from I to IV (AY 2022-2023) & (AY 2023-2024). Now we need your suggestions for finalizing structure and course of semester V<sup>th</sup> to VIII<sup>th</sup> semester to be implemented (AY 2024-25 & AY 2025-26).

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**Kindly give us your valuable suggestions / Opinion**

Your suggestions are helpful in designing the good curriculum


**Regards**


**Dr.K.Vasanth**  
**Associate Professor**



1/10/24, 3:33 PM

C.B.I.T Mail - Feed back to be made for Curriculum revision - ECE - CBIT- Reg

 **R 22 BE (ECE) I to VIII Semesters.docx**  
225K

 **R 20 BE(ECE) Syllabus of V to VIII Semesters.docx**  
650K

**konnoju vaishnavi** <konnojuvaishnavi99@gmail.com>  
To: "Dr. K.Vasanth Associate Prof., ECE" <vasanthk\_ece@cbit.ac.in>

Thu, Dec 28, 2023 at 8:33 AM

Hello Dr. K.Vasanth,

Thank you for considering me for this process, here are my comments

- Digital communication theory and lab can be merged with Analog Communication. Advanced experiments can be offered through a Matlab Lab as advanced communication lab. Engineering is all about Learning by doing. Faculty can frame syllabus related to 3G,4G,5G experiments or few techniques related to it.

- Semester 1 students learning subjects called Robotics and Drones as Lab. But theory is offered later? Is it correlatable? May be look into the possibility of Reshuffling

- Basic Electrical Engineering and Network theory can be merged to offer a new Circuit based subject for ECE students if possible. It might lead to a good space for other basic subjects.

- Syllabus seems good but keep GATE, MS syllabus in Mind. More advanced subjects might eliminate basic knowledge is the intended domain.

- Subject Such as Computer networks can be recoined as data communication and corresponding Lab can be offered? That may be suitable for ECE Domain

I hope you find my suggestions are insightful.

Best Regards,  
Vaishnavi Konnoju  
[Quoted text hidden]




Dr. Sivapriyanka S Asst. Professor, Dept. of ECE <sivapriyankas\_ece@cbit.ac.in>

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## Stake holder feedback R-22 Regulations

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**Rakesh Pogula** <rakesh.pogula@meeamitech.com>

Thu, Dec 28, 2023 at 6:00 PM

To: "Dr. Sivapriyanka S Asst. Professor, Dept. of ECE" <sivapriyankas\_ece@cbit.ac.in>

Dear sir/madam

After reviewing the curriculum of B.E-(ECE)-R-22, herewith giving few comments to benefit the student career towards industry.

- 1.The program seems to be proactive in adapting to emerging technologies, with graduates showcasing a good understanding of the latest advancements in IoT, 5G, and AI.
2. The program effectively covers sensor networks, communication protocols, and connectivity aspects relevant to IoT. Graduates are well-versed in choosing and implementing suitable communication protocols.
- 3.The program effectively covers antenna design principles and propagation characteristics relevant to 5G, with graduates demonstrating competence in designing and optimizing antennas for 5G applications.
- 4.The program's emphasis on practical projects provides graduates with valuable hands-on experience, allowing them to apply speech signal processing concepts to real-world scenarios.
- 5.The Program gives proficiency in applying deep learning techniques to speech processing, reflecting the industry's shift toward neural network-based models.

Thanks & Regards,  
Dr. P.Rakesh

---

**From:** Dr. Sivapriyanka S Asst. Professor, Dept. of ECE <sivapriyankas\_ece@cbit.ac.in>

**Sent:** Wednesday, December 27, 2023 11:09 AM

**To:** Rakesh Pogula <rakesh.pogula@meeamitech.com>

**Subject:** Fwd: Stake holder feedback R-22 Regulations

[Quoted text hidden]





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COMMITTED TO  
RESEARCH,  
INNOVATION AND  
EDUCATION

**45**  
years


## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Stake holder involvement in Curriculum Development

R-22 Regulation

Suggestions Received From Stake Holders

### 4) Alumni

S.No	Name of the Industry expert	Company & Designation	Mobile No Mail ID	Suggestions & opinion		Link/Received Mail details
1	Bharath Nidikonda	Sr project manager	8800257400 Bharathnidikonda023@gmail.com	1	The curriculum should include industry-advanced technologies like Generative - AI/ML/DI/NLP/NLV technologies	<a href="https://drive.google.com/file/d/19kaLdaUxESQIFV7A13snSAvNbexndrUD/view?usp=sharing">https://drive.google.com/file/d/19kaLdaUxESQIFV7A13snSAvNbexndrUD/view?usp=sharing</a>
2	Lokesh kandregula	Software Engineer	8019184999 Lokesh7naani@gmail.com	1	English and English Lab should be included in V sem	<a href="https://drive.google.com/file/d/1NGAfsfDLmGFEmZARQQlpPCfjfEmsgAqm/view?usp=sharing">https://drive.google.com/file/d/1NGAfsfDLmGFEmZARQQlpPCfjfEmsgAqm/view?usp=sharing</a> 
				2	Programming subjects should be continued in II & III semesters	
				3	In addition to existing curriculum, Extracurricular activities plays major role in surviving in industry.	
				4	Need to include tools like SAP, MuleSoft etc.	

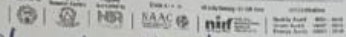
				5		
3	U. Shalomji	App Developer	8919991400 usiripellishalomji@gmail.com	1	Awareness of DSA and problem-solving needs to be increased.	<a href="https://drive.google.com/file/d/1Kg9YJs_TcNp_ECkWIwt4bth7ltelqRwE/view?usp=sharing">https://drive.google.com/file/d/1Kg9YJs_TcNp_ECkWIwt4bth7ltelqRwE/view?usp=sharing</a>
				2	More project events related to Nanotechnology, AI, etc.	
4	D. saipranav	Specialist programmer	9493757258 saipranavdevineni@gmail.com	1	Focus on solving problems	<a href="https://drive.google.com/file/d/1Kg9YJs_TcNp_ECkWIwt4bth7ltelqRwE/view?usp=sharing">https://drive.google.com/file/d/1Kg9YJs_TcNp_ECkWIwt4bth7ltelqRwE/view?usp=sharing</a>
				2	Focus on generative AI	
				3	Focus on extracurricular activities	
5	Maddi Prashnth Reddy	VLSI hardware Engineer	(+1)5104565323 Prashanthmaddi96@gmail.com	1	More practical work in 3rd and 4th years.	<a href="https://drive.google.com/file/d/1wVY72Gn1a61Ofk5VAYxboZ8dkdQ0jS/view?usp=sharing">https://drive.google.com/file/d/1wVY72Gn1a61Ofk5VAYxboZ8dkdQ0jS/view?usp=sharing</a>
				2	Exposure in VLSI-relevant course may have bright future for technical advancement in relevant industry	





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COMMITTED TO RESEARCH, INNOVATION AND EDUCATION

**45**  
years

Name: Bharath Nidikonda Present Designation: Sr project manager  
 Name of the Programme studied: EC Year of Pass out: 1998  
 Contact No.: 9800257400 Email-ID: bharathnidikonda@gmail.com  
 Address of the Company: HCF Technologies Pvt. Avane SEZ  
 Feedback on UG/PG Curriculum: EC

**Feedback on Curriculum by Alumni**

This questionnaire is intended to collect information relating to curriculum, teaching, learning and evaluation. The information provided by you will be pivotal for quality improvement of the programme.

Please answer the following questions on the scale of 1 to 5 where 1 indicates little satisfaction and 5 indicates higher satisfaction. Please mark a tick '✓' in the appropriate cell.

Sl.No.	Questions	1	2	3	4	5
1.	The curriculum and syllabus provide sufficient knowledge in the area of study			✓		
2.	Holistic development is ensured through this curriculum			✓		
3.	The curriculum focusses on developing technical and communication skills of the students			✓		
4.	The curriculum develops the employability potential of the students.			✓		
5.	The curriculum instils the professional ethics among the students.			✓		
6.	Industrial exposure in the curriculum			✓		
7.	The curriculum and non-curricular initiatives taken up help the students to attain the required competency level			✓		
8.	Does the Curriculum satisfy the current industry requirement?			✓		
9.	How do you rate the relevance of the curriculum to the organisation			✓		
10.	Any Suggestions: <u>Curriculum should include industry advanced technologies like Generative AI / ML / DL / NLP / NLU technologies.</u>					

Bharath  
Alumni Signature

S. N. S.







Name: Lokesh Kandregala Present Designation: Software Engineer  
 Name of the Programme studied: E.C.E Year of Pass out: 2017  
 Contact No.: 8019124999 Email-ID: lokeshnaani@gmail.com  
 Address of the Company: Skyview 10, Hitech City, Hyd.  
 Feedback on UG/PG Curriculum: For many good changes made compared to our curriculum.

**Feedback on Curriculum by Alumni**

This questionnaire is intended to collect information relating to curriculum, teaching, learning and evaluation. The information provided by you will be pivotal for quality improvement of the programme.

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Sl.No.	Questions	1	2	3	4	5
1.	The curriculum and syllabus provide sufficient knowledge in the area of study				✓	
2.	Holistic development is ensured through this curriculum				✓	
3.	The curriculum focusses on developing technical and communication skills of the students		✓			
4.	The curriculum develops the employability potential of the students					✓
5.	The curriculum instils the professional ethics among the students.		✓			
6.	Industrial exposure in the curriculum			✓		
7.	The curriculum and non-curricular initiatives taken up help the students to attain the required competency level					✓
8.	Does the Curriculum satisfy the current industry requirement?			✓		
9.	How do you rate the relevance of the curriculum to the organisation	✓				
10.	Any Suggestions: English & English Lab should be included in sem-V Programming should be continued in 2-3 sems at least. In addition to Existing Curriculum, Extra-curricular activities plays major role in surviving in Industry. Need to include tools like SAP, MuleSoft etc.					

Alumni Signature

*S. Nuthi*





Name: D. Sai pranoj Present Designation: specialist programmer

Name of the Programme studied: ..... Year of Pass out: 2020

Contact No: 9443353258 Email-ID: sai.pranoj@nexus1212@gmail.com

Address of the Company: Patnam, Telangana

Feedback on UG/PG Curriculum: Good. Having regular competitions in respective tech stacks would help in improving interest in students better.

**Feedback on Curriculum by Alumni**

This questionnaire is intended to collect information relating to curriculum, teaching, learning and evaluation. The information provided by you will be pivotal for quality improvement of the programme.

Please answer the following questions on the scale of 1 to 5 where 1 indicates little satisfaction and 5 indicates higher satisfaction. Please mark a tick '✓' in the appropriate cell.

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1.	The curriculum and syllabus provide sufficient knowledge in the area of study				✓	
2.	Holistic development is ensured through this curriculum				✓	
3.	The curriculum focusses on developing technical and communication skills of the students				✓	
4.	The curriculum develops the employability potential of the students.			✓		
5.	The curriculum instils the professional ethics among the students.			✓		
6.	Industrial exposure in the curriculum			✓		
7.	The curriculum and non-curricular initiatives taken up help the students to attain the required competency level			✓		
8.	Does the Curriculum satisfy the current industry requirement?			✓		
9.	How do you rate the relevance of the curriculum to the organisation.				✓	
10.	Any Suggestions: <u>1) Focus on solving problems (codeforces, codechef)</u> <u>2) Focus on generative AI</u> <u>3) Focus on elect. extracurricular activities</u>					

Alumni Signature

*S. Sai pranoj*





# CHAITANYA BHARATHI INSTITUTE OF TECHNOLOGY

An Autonomous Institute Affiliated to Osmania University  
Kuvempu Circle, Guntur, Andhra Pradesh, India - 526002

COMMITTED TO RESEARCH, INNOVATION AND EDUCATION

# 45 years

Name: D. Sai Prasad Present Designation: specialist programmer

Name of the Programme studied: ..... Year of Pass out: 2020

Contact No: 9945257258 Email-ID: sai.prasad.neni123@gmail.com

Address of the Company: Pacharam, Jangoji

Feedback on UG/PG Curriculum: Good. Hosting regular competitions in respective tech stacks would help in improving interest in students better.

### Feedback on Curriculum by Alumni

This questionnaire is intended to collect information relating to curriculum, teaching, learning and evaluation. The information provided by you will be pivotal for quality improvement of the programme.

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3.	The curriculum focusses on developing technical and communication skills of the students				✓	
4.	The curriculum develops the employability potential of the students.			✓		
5.	The curriculum instils the professional ethics among the students.			✓		
6.	Industrial exposure in the curriculum			✓		
7.	The curriculum and non-curricular initiatives taken up help the students to attain the required competency level			✓		
8.	Does the Curriculum satisfy the current industry requirement?			✓		
9.	How do you rate the relevance of the curriculum to the organisation				✓	
10.	Any Suggestions: 1) focus on solving problems (codeforces, codechef) 2) focus on generative AI 3) focus on extra-curricular activities					

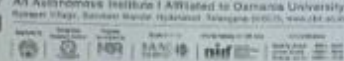
*S. Prasad*

Alumni Signature





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Bosheri Village, Saranampalle, Hyderabad - 500075, www.cbri.ac.in



COMMITTED TO RESEARCH INNOVATION AND EDUCATION

**45**  
years

Name: Prashanth Laddu Maddi Present Designation: VLSI HW Engineer  
Name of the Programme studied: ECE Year of Pass out: 2017  
Contact No: (+91) 510 456 5223 Email-ID: prashanthmaddi96@gmail.com  
Address of the Company: Apple  
Feedback on UG/PG Curriculum: C.B.IT (UG), Arisema Tech University (OS)

**Feedback on Curriculum by Alumni**

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2.	Holistic development is ensured through this curriculum					✓
3.	The curriculum focusses on developing technical and communication skills of the students			✓		
4.	The curriculum develops the employability potential of the students.				✓	
5.	The curriculum instils the professional ethics among the students.				✓	
6.	Industrial exposure in the curriculum		✓			
7.	The curriculum and non-curricular initiatives taken up help the students to attain the required competency level				✓	
8.	Does the Curriculum satisfy the current industry requirement?			✓		
9.	How do you rate the relevance of the curriculum to the organisation			✓		
10.	Any Suggestions:	<p>More practical work in third and fourth year. Expand in <u>VLSI</u> relevant course may have bright future for technical advancement in relevant industry.</p>				

May be in Undergraduate work as well  
M. Prashanth Reddy  
Alumni Signature

*M. Prashanth Reddy*





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## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING


Stake holder involvement in Curriculum Development


R-22 Regulation



Suggestions Received From Stake Holders

### 5) PARENTS


S. No	Name of the Industry expert	Company & Designation	Mobile No Mail ID	Suggestions & opinion	Link/Received Mail details
1.	Prasad Rao Rudrapati	BSNL Area General Manager	Mobile No:9490000246 <a href="mailto:rprao1970@gmail.com">rprao1970@gmail.com</a>	1 In my experience, certain subjects have proven crucial for core placements, and I believe they should be given additional emphasis and content. These include microcontrollers, computer networks,	<a href="https://drive.google.com/drive/folders/1NGL6CIYiJK6WcrKj8fRpEOzPe-zJPB0W?usp=sharing">https://drive.google.com/drive/folders/1NGL6CIYiJK6WcrKj8fRpEOzPe-zJPB0W?usp=sharing</a> 



				<p>digital integrated circuits, and DSD. Strengthening the curriculum in these areas can significantly benefit students in their future endeavours.</p>	<p><i>S. V. S.</i></p> 
			2	<p>Additionally, I suggest introducing courses on Dotnet, Linux and cloud computing to provide students with a broader exposure to various technologies. This exposure can prove beneficial as they enter the professional world, where diverse skill sets are</p>	

				increasingly valued.	
			3	<p>I am pleased to hear about the existence of clubs that support education and provide practical knowledge. It would be helpful to receive more detailed information about these clubs, such as their activities, events, and how students can actively engage with them.</p> <p>Practical experiences gained through clubs can greatly enhance the theoretical knowledge acquired in the classroom</p>	

2	Konda Reddy U	Excelmax Technologies pvt Ltd Senior Manager , Physical Design	ukreddy1@gmail.com 08977444150	<p>1 In semester V, 7 theory subjects and 3 labs is heavy to cover in one semester. Better to move one theory subject to next semester.</p> <p>2 In Computer Architecture and Microprocessors subject, Microprocessors part can be clubbed with Microcontrollers subject, and Advanced Computer architecture can be added to this subject.</p> <p>3 In semester VIII, there are no subjects related to ECE stream in open Elective. better to add subjects</p>	<p><a href="https://drive.google.com/drive/folders/1YTiahhwtXZHbWeMM0AbUp2Xprm4n2Sgk?usp=sharing">https://drive.google.com/drive/folders/1YTiahhwtXZHbWeMM0AbUp2Xprm4n2Sgk?usp=sharing</a></p> <p style="text-align: right;">    </p>



					in ECE stream in advanced technology.	
3	Dr T R Vijaya Lakshmi	MGIT Associate Professor	Mobile No:940019191 <a href="mailto:trvijayalakshmi_ece@mgit.ac.in">trvijayalakshmi_ece@mgit.ac.in</a>	1	In the DSP lab syllabus, it is suggested to add experiments	 <a href="https://drive.google.com/drive/folders/1EGIE9A5UnnNpjojCtVYDdLBpOcfLDEQM?usp=sharing">https://drive.google.com/drive/folders/1EGIE9A5UnnNpjojCtVYDdLBpOcfLDEQM?usp=sharing</a>

	.	r ECE Dept		<p>related to 1D (Speech/ECG) and 2D (Images) signals. Also it is advisable to include Signals and Systems lab in III sem so that students will get explored to prove the properties of Fourier Transform and many more.</p>	<p>In VI sem, VLSI design subject was offered and in V sem CAD for VLSI verification was offered as a Professional Elective where VLSI design is a pre-requisite to CAD for VLSI verification. Kindly look</p> <p style="text-align: right;">    </p>
			2	<p>In VI sem, VLSI design subject was offered and in V sem CAD for VLSI verification was offered as a Professional Elective where VLSI design is a pre-requisite to CAD for VLSI verification. Kindly look</p>	

				into it if possible.	
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*S. N. S.*





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Parent feedback to frame the Syllabus from V to VIII Semester(UG) Ex

**D** Dr.A.Vani Associate Professor (Adhoc) <avani\_ece@cbit.ac.in> to rprao1970 Wi

Good Evening Sir  
 Sir, we are (BoS) framing the syllabus for **B.E-(ECE)-R-22** V-VIII semesters(UG).In stakeholders(Parents) to give suggestions and comments or any feedback . For this, We ha distribution of Credits for V to VIII semesters Structure. And also I to IV semesters Course struct  
 Sir, Please suggest any new topics(content) that are to be included in each course.  
 Sir please do the needful as early as possible( sir within 10 days)  
 Thank you very much.

3 Attachments • Scanned by Gmail



**D** Dr.A.Vani Associate Professor (Adhoc)  
 Good eveningq Sir Sir, Please consider this document for R22-Curriculum -Course Structure Sorry for inconvenience Th



1/12/24, 1:06 PM

Parent feedback to frame the Syllabus from V to VIII Semester(UG) - avani\_ece@cbit.ac.in - C.B.I.T Mail



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Good morning Madam,

I hope this message finds you well. I am R. Prasada Rao, F/o Rudrapati Ritisha. I appreciate the opportunity to review t provided.

Having gone through the course details, I would like to offer some suggestions for consideration. In my experience, cer placements, and I believe they should be given additional emphasis and content. These include microcontrollers, comp DSD. Strengthening the curriculum in these areas can significantly benefit students in their future endeavours.

Additionally, I suggest introducing courses on Dotnet, Linux and cloud computing to provide students with a broader exp exposure can prove beneficial as they enter the professional world, where diverse skill sets are increasingly valued. I an clubs that support education and provide practical knowledge. It would be helpful to receive more detailed information e events, and how students can actively engage with them. Practical experiences gained through clubs can greatly enhar the classroom.

Thank you for considering these suggestions. I believe that a well-rounded education, with a balance of theoretical and students for success in their careers.

Regards,

Prasada Rao.

Sent from [Mail](#) for Windows

**From:** [Dr.A.Vani Associate Professor \(Adhoc\)](#)

**Sent:** 24 December 2023 21:24

*S. Vani*

1/12/24, 1:09 PM

Parent feedback to frame the Syllabus from V to VIII Semester(UG) - avani\_ece@cbit.ac.in - C.B.I.T Mail



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Parent feedback to frame the Syllabus from V to VIII Semester(UG) Ex

**Dr.A.Vani Associate Professor (Adhoc)** <avani\_ece@cbit.ac.in> to Konda

Good morning Sir  
 Sir, we are (BoS) framing the syllabus for **B.E-(ECE)-R-22** V-VIII semesters(UG).In stakeholders(Parents) to give suggestions and comments or any feedback . For this, We hz distribution of Credits for V to VIII semesters Structure. And also I to IV semesters Course struct  
 Sir, Please suggest any new topics(content) that are to be included in each course.  
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 Thank you very much.

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 Sir Please consider this document for R22-Curriculum -Course Structure Sorry for inconvenience Thank you

 **Konda Reddy** to me Jan 9, 2024

Hello Madam,  
Thank You very much for giving me the opportunity to give feedback on Syllabus for the ECE stream.

1. In semester V, 7 theory subjects and 3 labs is heavy to cover in one semester. Better to move one theory subject to r
2. In Computer Architecture and Microprocessors subject, Microprocessors part can be clubbed with Microcontrollers s architecture can be added to this subject.
3. In semester VIII, there are no subjects related to ECE stream in open Elective. better to add subjects in ECE stream

Thanks,  
Konda Reddy U  
08977444150

 **Dr.A.Vani Associate Professor (Adhoc)** <avani\_ece@cbit.ac.in> to Konda Jan 9, 2024

Thank you very much sir



1/12/24, 2:16 PM

Parent feedback to frame the Syllabus from V to VIII Semester(UG) - avani\_ece@cbit.ac.in - C.B.I.T Mail



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### Parent feedback to frame the Syllabus from V to VIII Semester(UG)

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**Dr.A.Vani Associate Professor (Adhoc)** <avani\_ece@cbit.ac.in> to trvijayalakshmi\_ece

Tu

Good Evening Madam

Sir, we are (BoS) framing the syllabus for **B.E-(ECE)-R-22** V-VIII semesters(UG).In stakeholders(Parents) to give suggestions and comments or any feedback . For this, We have distribution of Credits for V to VIII semesters Structure. And also I to IV semesters Course structure. Madam, Please suggest any new topics(content) that are to be included in each course. Madam please do the needful  
Thank you very much.

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1/12/24, 2:19 PM

Parent feedback to frame the Syllabus from V to VIII Semester(UG) - avani\_ece@cbit.ac.in - C.B.I.T Mail

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Good Evening Madam Sir, we are (BoS) framing the syllabus for B.E-(ECE)-R-22 V-VIII semesters(UG).In this regard, v

 **T R Vijaya Lakshmi**  
to me

Dear Madam,

Thank you for the invitation mail. The following are some of my observations/suggestions related to Course structure of

1. In the DSP lab syllabus, it is suggested to add experiments related to 1D (Speech/ECG) and 2D (Images) signals. AI Systems lab in III sem so that students will get explored to prove the properties of Fourier Transform and many more.

2. In VI sem, VLSI design subject was offered and in V sem CAD for VLSI verification was offered as a Professional Ele CAD for VLSI verification. Kindly look into it if possible.

Regards

Dr T R Vijaya Lakshmi  
Associate Professor of ECE Dept.  
MGIT, Gandipet

 **Dr.A.Vani Associate Professor (Adhoc)** <avani\_ece@cbit.ac.in>

