

AY 21-22

International Journals

1. **Kamlikar Sowmya (1601-19-744-402)**, Alenoor Krishna Kumar, Design and Simulation of Area Efficient Carry Select Adder using Verilog HDL, Design Engineering (Toronto), vol no: 2021, Issue no: 7, pp: 7206 - 7216, August - 2021, ISSN: 0011-9342 (Scopus Indexed)

National Conference

1. N. Alivelu Manga, **S. Sai Sandeep Kumar (1601-20-744-407)** , "Design And Simulation of Approximate Multiplier for Low Power Applications", Research Day, CBIT, 28 August 2021.
2. **Chimata Lakshmanaswami (1601-20-744-401)**, Sri. Mohd.Ziauddin Jahangir, "Design Of Digitally Controlled Vco", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
3. **S. Vishnu Vardhan (1601-20-744-402)**, Dr. M. Ramana Reddy, "Design And Verification Of Serial Peripheral Interface (Spi) Protocol", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
4. **N. Manikanth (1601-20-744-403)**, Dr. M.L. Narasimha Charyulu, "Design And Verification Of Ddr SDRAM Memory Controller Using System Verilog For Higher Coverage", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
5. **S. Swetha (1601-20-744-404)**, Smt. B. Neeraja, "Design And Verification Of 1x3 Router", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
6. **Mohammed Riyazuddin (1601-20-744-405)**, Sri A. Krishna Kumar. "Uvm Based Verification Of Dmac With Advanced Linked List Features", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
7. **S. Sai Sandeep Kumar (1601-20-744-407)**, Dr. N. Alivelu Manga, "Uvm Based Verification Of I3c", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
8. **P. Nazima (1601-20-744-408)**, Smt. D. Sony, "Implementation Of Bird Monitoring System", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
9. **G. Sai Koushik (1601-20-744-409)**, Sri. E. Chandrasekhar, "High-Speed Area-Efficient Vlsi Architecture Of Three-Operand Binary Adder", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
10. **F. Florance (1601-20-744-410)**, Dr. K. Sudharshan Reddy, "Implementation Of Automated Health Monitoring System On ARM Processor", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
11. **Y. Nandini (1601-20-744-411)**, Sri. T. Sreedhar, M. Sandeep Kumar, "Implementation Of Hardware Module Of Iot Based Infant Monitoring System", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
12. **K. Krishna Vamshi (1601-20-744-412)**, Sri. G. Mallikharjuna Rao, "Hardware Implementation And Vlsi Realisation Of ECC Cryptosystem For Security", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022

13. **Susrutha Reddy Kilaru (1601-20-744-413)**, Sri. N. Jagan Mohan Reddy, "Logic Synthesis Using Front End Design Flow And Improve Correlation With Placement And Route", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24thmarch, 2022
14. **G.Sameera(1601-20-744-406)**, Smt. N.Dhanalakshmi, "Biometric Locker Security System", Synapse - 2022 under Sudhee 2022 at CBIT 23rd&24th march, 2022